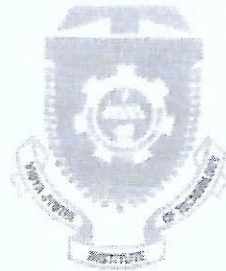


VIDYA JYOTHI INSTITUTE OF TECHNOLOGY
(AUTONOMOUS)

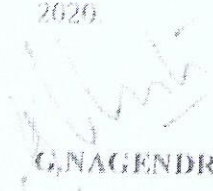
AZIZ NAGAR-500075, HYDERABAD


DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING





CERTIFICATE

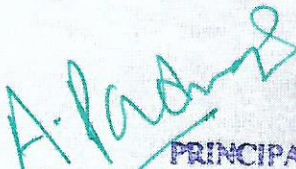
This is to certify that the project entitled "VLSI DESIGN OF HIGH SPEED AND LOW EFFICIENT RADIX-8 SERIAL PARALLEL MULTIPLIER", done by **TEJASWINI** bearing **18N1605701**, Submitted to the faculty of Electronics and Communication Engineering, in partial fulfillment of the requirements for the Degree of **BACHELOR OF TECHNOLOGY** with specialization in **VLSI SYSTEM DESIGN** from **Jawahar Lal Nehru Technological University Hyderabad**, during the year **2019-2020**.


G. NAGENDRA
Associate Professor


G. SRINIVAS RAO
Associate Professor
M. Tech Project Coordinator


D. K. VASANTH
HOD, ECE


EXTERNAL EXAMINER


PRINCIPAL
Vidya Jyothi Institute of Technology
Himayatnagar (VIII), C. B. Post,
Hyderabad-75.

VIDYA JYOTHI INSTITUTE OF TECHNOLOGY

(AUTONOMOUS)

AZIZ NAGAR-500075, HYDERABAD

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project entitled "FILTER USING FAST BINARY COUNTERS BASED ON SYMMETRIC STACKING", done by MSAI KEERTHANA bearing 1501005703, Submitted to the faculty of Electronics and Communication Engineering, in partial fulfillment of the requirements for the Degree of MASTER OF TECHNOLOGY with specialization in VLSI SYSTEM DESIGN from Jawaharlal Nehru Technological University Hyderabad, during the year 2019-2020.


Mr. UPENDAR SAPATI

Associate Professor

Supervisor


G. SRINIVAS RAO


Associate Professor

M. Tech Project Coordinator


Dr. K. VASANTH

HOD, ECE


Towheed S
20/12/20
EXTERNAL EXAMINER


PRINCIPAL
Vidya Jyothi Institute of Technology
Himayatnagar (VIII), C. B. Post.
Hyderabad-75.

VIDYA JYOTHI INSTITUTE OF TECHNOLOGY

(AUTONOMOUS)

AZIZ NAGAR-500075, HYDERABAD

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project entitled "IMPLEMENTATION OF FIR FILTER BASED
MULTIPLIER USING APPROXIMATE MULTIPLIER AND
COMPLESSOR DESIGN", done by **RAJOTHEE RAJESWARI** bearing 1891105704,
Submitted to the faculty of Electronics and Communication Engineering, in partial fulfillment
of the requirements for the Degree of **MASTER OF TECHNOLOGY** with specialization in
NEW SYSTEM DESIGN from Jawaharlal Nehru Technological University Hyderabad,
during the year 2019-2020.

Mrs. M. Renuka (PhD)

Assitant Professor

Supervisor

Mr. G. Srinivas Rao

Associate Professor

M. Tech Project Coordinator

Dr. K. VASANTH

HOD, ECE

29/12/20

EXTERNAL EXAMINER

PRINCIPAL

Vidya Jyothi Institute of Technology
Himayunagar (VIII), C. B. Pura
Hyderabad-75.

VIDYA JYOTHI INSTITUTE OF TECHNOLOGY

(AUTONOMOUS)

AZIZ NAGAR-500075, HYDERABAD

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



CERTIFICATE

This is to certify that the project entitled "THERMAL ANALYSIS OF THROUGH SILICON VIAS USING MWCNT BASED TSV IN 3D INTEGRATED PACKAGING SYSTEMS", done by SANTAPURAM SAIKIRAN bearing 18911D5707, Submitted to the faculty of Electronics and Communication Engineering, in partial fulfillment of the requirements for the Degree of MASTER OF TECHNOLOGY with specialization in VLSI SYSTEM DESIGN from Jawaharlal Nehru Technological University Hyderabad, during the year 2019-2020.


G.SREENIVASA RAO

Associate Professor

Supervisor and M. Tech Project Coordinator


Dr. K. VASANTH

HOD, ECE


EXTERNAL EXAMINER


PRINCIPAL

Vidya Jyothi Institute of Technology
Himayatnagar (VIII), C. B. Post
Hyderabad-75.

VIDYA JYOTHI INSTITUTE OF TECHNOLOGY
(AUTONOMOUS)

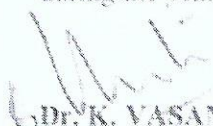
AZIZ NAGAR, 500075, HYDERABAD

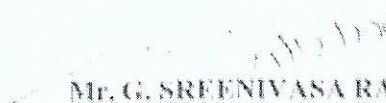
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING




CERTIFICATE

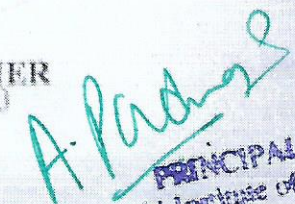
This is to certify that the project entitled "MINI/MIZI/FIRCA COMPARATOR AND ENCRYPTED SPEED ARCHITECTURE FOR SORTING APPLICATIONS", done by KAVANAJI RAJESH bearing 1901115709, Submitted to the faculty of Electronics and Communication Engineering, in partial fulfillment of the requirements for the Degree of MASTER OF TECHNOLOGY with specialization in VLSI SYSTEM DESIGN from Jawaharlal Nehru Technological University Hyderabad, during the year 2019-2020.


Dr. K. VASANTH
Professor
Supervisor


Mr. G. SREENIVASA RAO
Associate Professor
M. Tech Project Coordinator


Dr. K. VASANTH
HOD, ECE

EXTERNAL EXAMINER


A. Padma
PRINCIPAL
Vidya Jyothi Institute of Technology
Minnaganur (V.H), C.B. Post,
Hyderabad-75.

VIDYA JYOTHI INSTITUTE OF TECHNOLOGY

(AUTONOMOUS)

AZIZ NAGAR-500075, HYDERABAD

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING.



CERTIFICATE

This is to certify that the project entitled " *IMPLEMENTATION OF XOR BASED* ", done by *SRINIVASA RAO* bearing *1801105703*, submit to the faculty of Electronics and Communication Engineering, in partial fulfillment of the requirements for the Degree of *B.TECH IN ELECTRONICS AND COMMUNICATION ENGINEERING* with specialization in *VLSI* from Jawaharlal Nehru Technological University Hyderabad, during the year 2019-2020.

CH. Sandeep
Mr. CH. SANDEEP
Assistant Professor
Supervisor

G. Srinivasa Rao
Mr. G. SRINIVASA RAO
Associate Professor
M. Tech Project Coordinator

K. Vasanth
Dr. K. VASANTH
HOD, ECE

A. Prasad
EXTERNAL EXAMINER

A. Prasad
PRINCIPAL

Vidya Jyothi Institute of Technology
Himayatnagar (VIII), C. B. Post,
Hyderabad-75.