### **REGISTRATION & FEE**

For Faculty & Research Scholars: Rs. 1000/-For Industry Professionals: Rs. 2000/-

Registration fee includes Registration Kit, one Lunch and two high teas/snacks per day and Participation certificate. Interested candidates may register the program in one of the ways: (i). by submitting the filled registration form to the Organising Chairman along with a Demand Draft drawn in favor of VJIT payable at Hyderabad (ii). by filling the online registration form at <a href="http://vjit.ac.in/blog/online-registration-for-fdp-hamdsp/">http://vjit.ac.in/blog/online-registration-for-fdp-hamdsp/</a> and later paying the fee at registration counter (iii). by sending an email to ecehod@vjit.ac.in confirming the participation and later paying the fee at registration counter. The spot registration on 22-2-2016 is allowed based on the availability of seats.

### TRANSPORTATION

Our College Buses start from different locations of Hyderabad-Secunderabad: Uppal, Secunderabad Via Begumpet, Dilsukhnagar, Vidyanagar, Ameerpet, Kukatpally, Miyapur and Mehdipatnam. Participants can board these buses on the days of programme.

### **IMPORTANT DATES**

Last date for Online Registration: 20.2.2016

Last date for Spot Registration: 22.2.2016 (9.30-10.00AM)

# For further details please contact:

Mr. Sridhar - Mobile: **08522832445**Ms Lalitha Sowmya - Mobile: **09885642983**Mr Nagendra - Mobile: **09989542300**Mr Hari Babu - **09292606422** 

Or

Dr M V Krishna Rao ecehod@vjit.ac.in, mvk rao@hotmail.com

### CHIEF PATRONS

Dr. P. Rajeshwar Reddy.MLC, Secretary&Correspondent,
VJIT and Chairman, Anurag Group of Institutions
Mrs. S. Neelima, Joint Secretary, BoG-VJIT and Secretary&
Correspondent, Anurag Group of Institutions

### **PATRONS**

Dr. P. Venugopal Reddy, Director, VJIT Dr. A. Padmaja, Principal, VJIT

### **COORDINATORS**

Dr V. Usha Shree, Professor, ECE Dept Mr. M. Rajendra Prasad (Ph.D), Assoc. Professor, ECE Dept Mr. Shaik Mazunu (Ph.D), Assoc. Professor, ECE Dept

Mr. S. Upendra (Ph.D), Assoc. Professor, ECE Dept

### **ORGANIZING COMMITTEE**

Mr T. Venugopal, Assoc. Professor, ECE Dept

Ms A.Prasanna Lakshmi, Assoc. Professor, ECE Dept

Mr G. Ravi Kishore, Assoc. Professor, ECE Dept

Ms G. Annapurna, Assoc. Professor, ECE Dept

Mr G. Nagendra, Assoc. Professor, ECE Dept

Mr G. Srinivasa Rao, Assoc. Professor, ECE Dept

Mr. V. Sridhar, Assistant Professor, ECE Dept

Ms M. Lalitha Sowmya, Assistant Professor, ECE Dept

Ms A. Jaya Lakshmi, Assistant Professor, ECE Dept

Ms C.H.S.N Sireesha Devi, Assistant Professor, ECE Dept

Ms G. Ravikumar, Assistant Professor, ECE Dept

### ABOUT THE PROGRAMME

Digital Signal Processing addresses the challenges encountered in Detection, Estimation, Identification, Classification, Restoration, Enhancement, Separation, Modeling, Compression, Analysis and Synthesis of digital signals. Signals includes Speech, Audio, Voice (1-D), Image (2-D) and Video(3-D). Almost all the application domains benefit from signal processing, to name a few: Electronic Warfare, Radar, Communications, Multimedia, Consumer Electronics, Biomedical systems, etc. MATLAB is a highperformance language which integrates computation, visualization and programming in a user friendly environment. This training program enables the participants to get hands-on experience in simulating the theoretical concepts of Signal Processing, analyzing and interpreting the results. The program is framed to have 20% Theory and 80% Laboratory Sessions.

# Vidya Jyothi Institute of Technology

Aziznagar Gate, Chilukur Road, Hyderabad-500075
(An Autonomous Institution)

A Faculty Development Program
"HAnds-on-training on MATLAB
for Digital Signal Processing
(HAMDSP)"

22<sup>nd</sup> - 27<sup>th</sup> February 2016



Organizing Chairman
Dr. M.V. Krishna Rao
Professor & Head, ECE Dept., VJIT



Organized by

**Department of Electronics and Communication Engineering** 

### **MAJOR TOPICS**

Fundamentals of MATLAB programming: A MATLAB session - Data Types: Constants, Variables, Operators, Expressions – Working with Arrays & Matrices – Flow Control - Matlab Script files - Working with Functions Signal Generation: Generation of synthetic signals & images. Signal Visualization: Plotting of 1-D signals-Displaying of Multidimensional signals. Signal Storage & **Retrieval**: Storing of 1-D and 2-D signals- storage formats -Retrieving of 1-D and 2-D signals - File Operations. Signal Fourier Spectrum- Amplitude and Phase, **Analysis:** Power/Energy Spectrum - Correlation of Signals-Convolution & Filtering - Digital Filtering - Filter impulse and frequency responses. Open Problem Implementation: Guidelines for implementing selective modules; as requested by the participants on the spot. The algorithm is to be provided by the participants.

### INTENDED AUDIENCE

The Faculty/Research scholars from Engineering colleges, Engineering professionals from Industry, and all those who are actively involved or intended to involve in Teaching, Research & Development, Academic Projects and Product Development in the Application Areas of Signal Processing: Speech, Audio, Image, Video, Radar and Biomedical.

### ABOUT THE INSTITUTION

Founded in 1998, by a group of committed academicians and enterprising educationists, VJIT quickly won the confidence of the parent community. Under the dynamic leadership and vision of Dr. Palla Rajeshwar Reddy, Secretary & Correspondent, VJIT has emerged a prestigious institute in the region both in infrastructure and in quality of education. The institution has ECE, CSE, EEE, IT, ME, CE and MBA departments. All the engineering departments are NBA accredited and going for next cycle of accreditation. Starting with an intake of 220 in 1999, the institution has grown to have a current intake of more than 1000. The institution has full-fledged Classrooms and Laboratories, Seminar Halls, Central Library, Transportation and Sports facilities, Dispensary, Canteen, R&D Cell, Training and Placement Cell and a Green campus.

### ABOUT E.C.E DEPARTMENT

Department of Electronics and Communication Engineering was established in the year 1999 with an UG intake of 40, current intake being 240. The department also has M.Tech

(VLSISD) and M.Tech(ES). The department has spacious Laboratories, Lecture Halls, Department Library and Experienced faculty. The department has IETE Student Forum, IEEE Student Branch, IEEE Signal Processing Society Chapter and IEEE Woman in Engineering (WIE) Affinity Group Chapter. The department regularly conducts Technical Seminars, Expert Lectures, Industrial Tours and Faculty Orientation/ Development Programs. The department is NBA accredited and going for next cycle of accreditation based on Outcome Based Education (OBE) philosophy.

### RESOURCE PERSONS

Theory sessions from eminent professors from Academic Institutions. Practical sessions from the Organizing Chairman.

M V Krishna Rao received his B.Tech (ECE) degree from JNTU college of Engg, Kakinada in 1983, M.Tech. from IIT, Kharagpur and Ph.D from the Osmania University, Hyderabad. Dr Krishna Rao has Hands-on Design Experience of more than 20 Product Prototypes using both Analog and Embedded Digital Technologies including the Algorithms, the Hardware, the Firmware and the Software. He worked in industry/academics for 30 years at different levels: Scientist, GM(R&D), Director, Principal, Professor and HOD. His product designs include VHF/UHF tuners, Telephony Terminal Equipment, Transaction Automation, DSP sections of Radar Seeker, Radio Altimeter and Tactical Air Navigation Receiver. He executed consultancy projects in the areas of Radar Image Processing and Missile Terminal Guidance. He also designed/implemented software modules for VOIP, Vector Graphics Library, ITU Data & Fax Modems, MP3 & WMV Audio Codecs, JPEG & JPEG2000 Image Codecs, MPEG-4, H264 Video Codecs and Flash Player Codecs on embedded platforms. Currently he is with VJIT as Professor and Head ECE dept. His current research interests are Multimedia Signal Processing and Data Mining.

Though we provide the desktop computers for Lab sessions, it is strongly recommended that the participants bring their own laptops loaded with trial version of Matlab with Signal processing and Image processing toolboxes. This is for the ease of participants to carry their programs along with them after the Lab sessions.

### **REGISTRATION FORM**

(Please use photo copies if required)

# Vidya Jyothi Institute of Technology Hyderabad

(An Autonomous Institution)

A Faculty Development Program
"HAnds-on-training on MATLAB for Digital
Signal Processing (HAMDSP)"

22<sup>nd</sup> - 27<sup>th</sup> February 2016

Name:				
Designation:				
Experience:				
Organization:				
Address for correspondence:				
Mobile:				
E-mail:				
Payment Mode: DD/ Local cheque /Cash:				
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Applicant's Signature