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CONVENOR

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VIDYA JYOTHI INSTITUTE OF TECHNOLOGY
(An Autonomous Institution)

THREE DAY
FACULTY DEVELOPMENT
PROGRAM (FDP)

ON

FINITE LANGUAGE AND
AUTOMATA THEORY

Feb 22nd – 24th 2016

Vidya Jyothi
Institute of Technology

Organized by
DEPARTMENT OF CSE

**VIDYA JYOTHI INSTITUTE OF
TECHNOLOGY**
Aziz Nagar, C.B. Post, Hyderabad,
Telangana, INDIA
Visit us at www.vjit.ac.in

Vidya Jyothi Institute of Technology:

Founded in 1998, by a group of committed academicians & enterprising educationists, VJIT quickly won the confidence of the parent community and the students to become one of the select destinations for future engineers. Soon the lamp of knowledge began to spread its radiance far and wide. The college is accredited by NBA of AICTE. The college prides on the fact that it has a very senior and highly accomplished faculty- one of the best in the region. VJIT is rated as one of the best engineering colleges in the region. The model room which houses models created of live projects by its student's is an example of VJIT philosophy of innovation driven education. The high quality of VJIT's academics is mirror in the 95% academic results and the institution registers an 75% placements for eligible students. The College got **AUTONOMOUS** status by **UGC** from this Academic Year.

About Department

The department of Computer Science and Engineering at VJIT was established in 1999. The Department offers B. Tech. with an intake of 240 students and M. Tech. (CSE) with an intake of 36 students. The Department has state- of the art infrastructure and computing equipment supported by high speed internet and wireless networks. VJIT's department of Computer Science and Engineering is known for research and a highly useful computer education.

The department have committed and experienced faculty members who help in enriching the students' technical abilities and programming skills. The Department has undertaken many research projects funded both by multinationals and government agencies.

About Workshop

Automata theory is the study of abstract machines and automata, as well as the computational problems that can be solved using them. It is a theory in theoretical computer science, under discrete mathematics.

Automata theory is closely related to formal language theory. An automaton is a finite representation of a formal language that may be an infinite set. Automata are often classified by the class of formal languages they can recognize.

By attending the course the faculty can gain knowledge about the theory of computation, compiler construction, artificial intelligence, parsing and formal verification.

Resource Persons

1) Dr. V. V. Vijay Kumar, Dean CSE, AGI,HYD
2) Dr. U.S.N. Raju, NIT Warangal
3) Dr. K. Srinivas Reddy, BVRIT(W),HYD
4) Dr. P. Suneetha, BVRIT(W),HYD
5) Dr. A. Sirisha, CBIT, HYD.

ELIGIBILITY

Faculty members and M.Tech Students of various colleges and people from Industry.

REGISTRATION

NO REGISTRATION FEE

Faculty & Industry Participants:

A filled in application form in the prescribed format duly signed by appropriate authorities should reach to the convener by E-mail.

Last Date for Submission: 22.02.2016

Number of Participants limited, Registration is based on First come First basis.

MAILING ADDRESS:

Applications should be sent to:
csefdp@vjit.ac.in

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