

VIDYA JYOTHI

INSTITUTE OF TECHNOLOGY

(An Autonomous Institution)

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Certificate

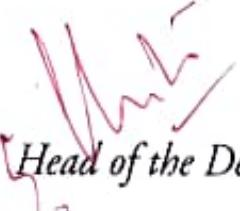
This is to certify that the bonafide record of the practical work carried out by

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of ClassECE-C.....in theEDC.....

laboratory during the academic year2021-2022.....


Faculty In-charge


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INDEX

S. No.	Date	Name of the Experiment	Page No.	Remarks
1.	20/10/21	Forward and Reverse Bias Characteristics of PN Junction Diode.	1-3	G 5 22/10/2021
2.	20/10/21	Zener Diode Characteristics .	4-5	G 5 22/10/2021
3.	27/10/21	Half Wave Rectifier Without & with filter.	6-9	G 5 21/11/2021
4.	3/11/21	Full Wave Rectifier Without & with filter.	10-12	G 5 10/11/2021
5.	10/11/21	Input and Output Characteristics of Transistor CB configuration.	13-15	G 5 16/11/2021
6.	17/11/21	Input and Output characteristics Of Transistor CE Configuration.	16-19	G 5 8/12/2021
7.	1/12/21	clippers	20-22	G 5 1/12/2021
8.	8/12/21	clampers	23-25	G 5 15/12/21
9.	15/12/21	characteristics of UJT	26-27	G 5 22/12/2021
10.	22/12/21	Frequency Response of CE amplifier	28-30	G 5 25/12/2021
11.	29/12/21	Frequency Response of CS amplifier	31-33	G 5 31/12/2021
12.	5/1/22	characteristics of FET	34-36	G 5 7/1/2022

V-I Characteristics of PN Junction Diode

Aim:- To plot the VI Characteristics of a PN junction diode in both forward and reverse biased condition. Also, calculate its cut in voltage, forward resistance and reverse resistance.

Apparatus Required :-

Sl.No.	Description	Range / Number / Value	Quantity
1.	Regulated Power Supply	(0-30)V	1
2.	Diode	1N4007	1
3.	DC Ammeter	(0-200)mA, (0-200)µA	1 of each category
4.	DC Voltmeter	(0-20)V	1
5.	Resistor	1KΩ	1
6.	Breadboard and connecting wires.		

Theory:-

A PN Junction diode conducts only in one direction. The VI characteristics of the diode are curve between voltage across the diode and current through the diode.

When the external voltage is zero, circuit is open and the potential barrier does not allow the current to flow. Therefore, the circuit current is zero.

When P-type (Anode) is connected to +ve terminal and n-type (cathode) is connected to -ve terminal of the supply voltage is known as forward bias. The potential barrier is

reduced when diode is in the forward biased condition. At some forward voltage, the potential barrier altogether eliminated and current starts flowing through the diode and also in the circuit. The diode is said to be in ON state. The current increases with increasing forward voltage.

When N-type (Cathode) is connected to +ve terminal and P-type (Anode) is connected to -ve terminal of the supply voltage is known as reverse bias and the potential barrier across the junction increases. Therefore, the junction resistance becomes very high and a very small current (reverse saturation current) flows in the circuit. The diode is said to be in OFF state. The reverse bias current is due to minority charge carriers.

Procedure :

A) Forward Bias :

1. Connections are made as per the circuit diagram.
2. Switch ON the power supply and increases the input voltage (supply voltage) in steps of 0.1V.
3. The forward current (I_f), and the forward voltage (V_f) are observed and then noted in the tabular form.
4. A graph is plotted between Forward Current (I_f) on X-axis and the forward voltage (V_f) on Y-axis.

B) Reverse Bias :

1. Connections are made as per the circuit diagram.
2. Switch ON the power supply and increase the input voltage

(supply voltage) in steps of 1V.

3. The Reverse Current (I_r) and the Reverse Voltage (V_r) are observed and then noted in the tabular form.
4. A graph is plotted between Reverse Current (I_r) on x-axis and Reverse Voltage (V_r) on Y-axis.

Calculations :-

Calculation of Static and Dynamic Resistance for a given diode.

In forward bias Condition :-

$$\text{static Resistance } R_s = \frac{V_f}{I_f}$$

$$\text{Dynamic Resistance } R_d = \frac{\Delta V_f}{\Delta I_f}$$

In Reverse bias Condition :-

~~$$\text{static Resistance } R_s = \frac{V_R}{I_R}$$~~

~~$$\text{Dynamic Resistance } R_d = \frac{\Delta V_R}{\Delta I_R}$$~~

Result :

The forward and reverse characteristics of the PN Junction diode has been plotted.

The static forward resistance of the diode = $0.139130 \text{ k}\Omega$

The Dynamic forward resistance of the diode = $0.1 \text{ k}\Omega$

The static reverse resistance of the diode = $-1.0095652 \text{ M}\Omega$

The Dynamic reverse resistance of the diode = $-1.02 \text{ M}\Omega$

The cut-in voltage of the diode = 0.45 V

27/10/2021

(supply voltage) in steps of 1V.

3. The Reverse Current (I_r) and the Reverse Voltage (V_r) are observed and then noted in the tabular form.
4. A graph is plotted between Reverse Current (I_r) on x-axis and Reverse Voltage (V_r) on y-axis.

Calculations :-

Calculation of Static and Dynamic Resistance for a given diode.

In forward bias Condition :-

$$\text{static Resistance } R_s = \frac{V_f}{I_f}$$

$$\text{Dynamic Resistance } R_d = \frac{\Delta V_f}{\Delta I_f}$$

In Reverse bias Condition :-

~~$$\text{static Resistance } R_s = \frac{V_r}{I_r}$$~~

~~$$\text{Dynamic Resistance } R_d = \frac{\Delta V_r}{\Delta I_r}$$~~

Result :

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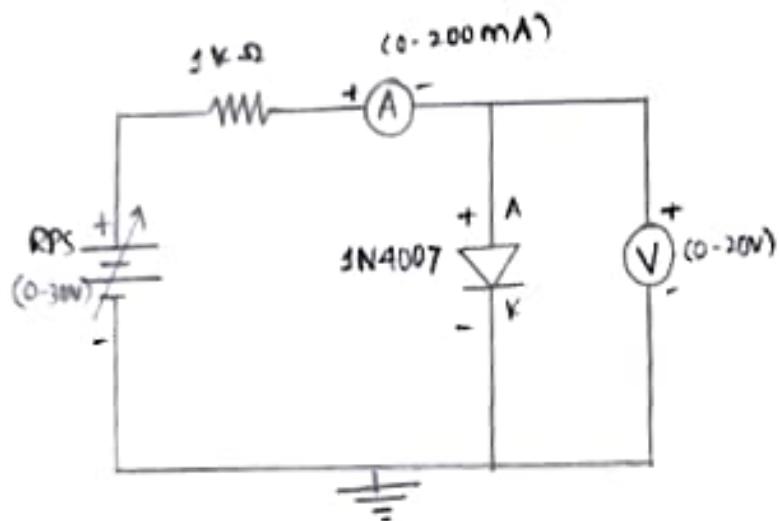
The Dynamic reverse resistance of the diode = $-1.02 \text{ M}\Omega$

The cut-in voltage of the diode = 0.45 V

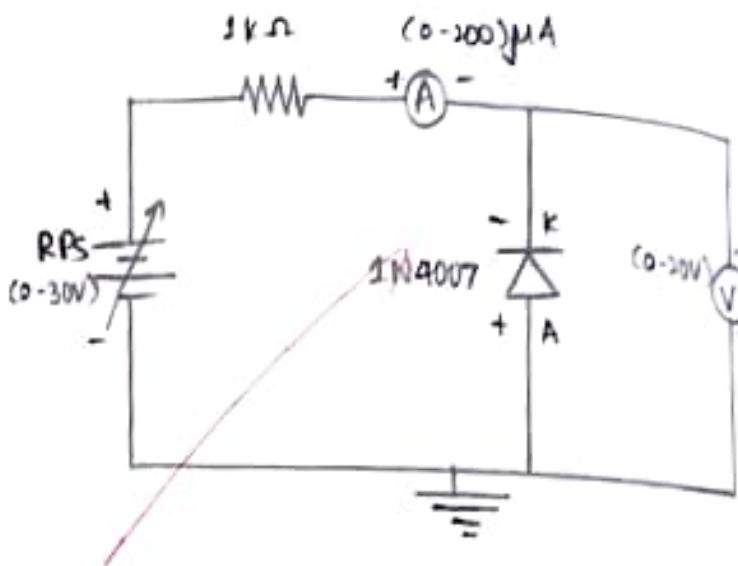
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Circuit Diagram :-

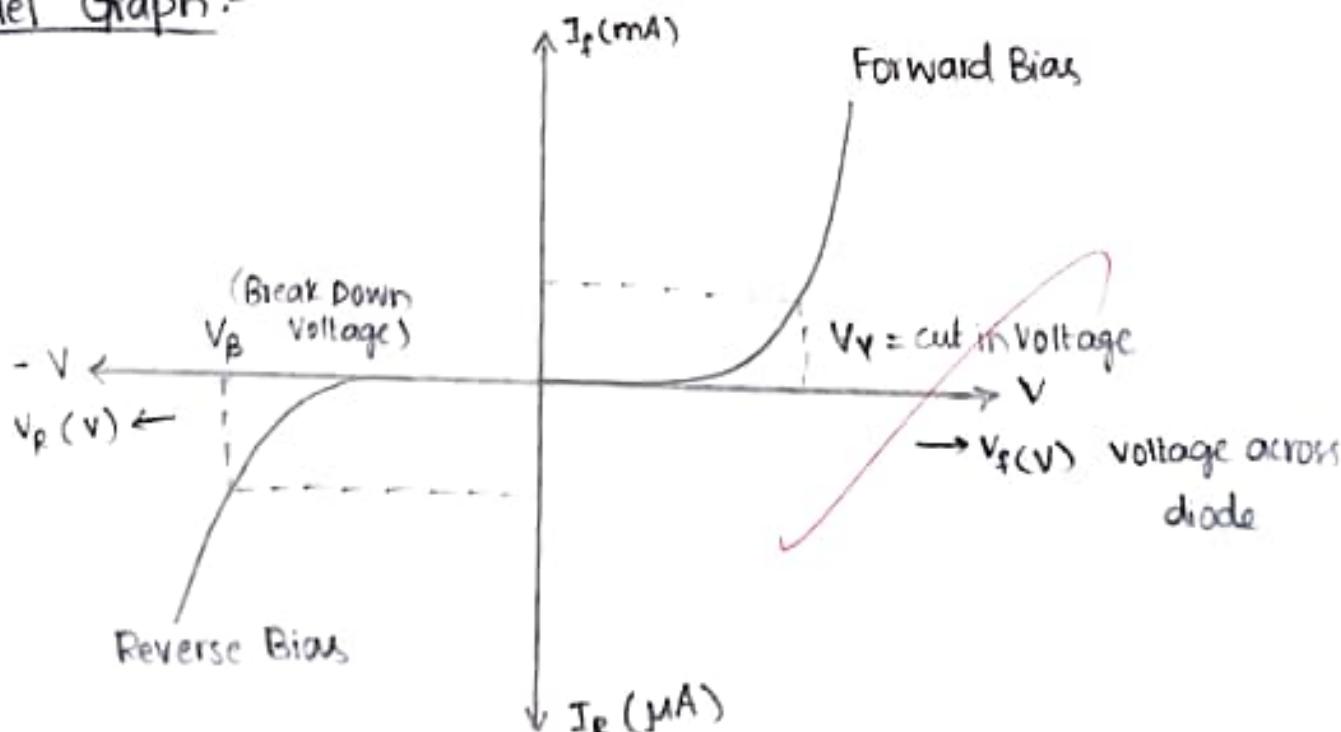
A) Forward Bias



B) Reverse Bias



Model Graph :-



Observations :-

A) Forward Bias:

S.No.	Applied Voltage(V) in Volts	Forward Voltage(V _f) in Volts	Forward current(I _f) in mA
1.	0.1	0.19	0
2.	0.2	0.31	0
3.	0.3	0.37	0
4.	0.4	0.45	0.1
5.	0.5	0.48	0.2
6.	0.6	0.49	0.28
7.	0.7	0.51	0.3
8.	0.8	0.52	0.4
9.	0.9	0.53	0.5
10.	1.0	0.54	0.6
11.	1.5	0.57	1.1
12.	2	0.59	1.6
13.	2.5	0.6	2.1
14.	3	0.61	2.6
15.	3	0.64	4.6

B) Reverse Bias:

S.No.	Applied Voltage(-V) in Volts	Preverse voltage(-V _r) in Volts	Reverse current(I _r) in mA
1.	0	1.09	1.1
2.	2	2.15	0.1
3.	3	3.17	3.1
4.	4	4.13	9.1
5.	5	5.15	3.1
6.	5.5	5.58	5.5
7.	6	6.12	6.1
8.	6.5	6.63	6.6
9.	7	7.12	7
10.	7.5	7.65	7.6
11.	8	8.08	8
12.	8.5	8.65	8.6
13.	9	9.14	9
14.	9.5	9.59	9.5
15.	11.5	11.61	11.5

Calculations :- Forward Bias

$$R_S = \frac{V_F}{I_F} = \frac{0.64}{4.6 \times 10^{-3}} = 0.139130 \text{ k}\Omega$$

$$R_D = \frac{\Delta V_F}{\Delta I_F} = \frac{V_2 - V_1}{I_2 - I_1} = \frac{0.53 - 0.52}{(0.5 - 0.4) \times 10^3} = \frac{0.01}{0.1 \times 10^3} = 0.1 \text{ k}\Omega$$

= 100 Ω

$$R_S = \frac{V_R}{I_R} = \frac{11.61}{11.5 \times 10^{-3}} = 1.00956 \text{ M}\Omega$$

$$R_D = \frac{\Delta V_R}{\Delta I_R} = \frac{V_2 - V_1}{I_2 - I_1} = \frac{6.63 - 6.12}{(6.6 - 6.1) \times 10^3} = \frac{0.51}{0.5 \times 10^3} = 1.02 \text{ M}\Omega$$

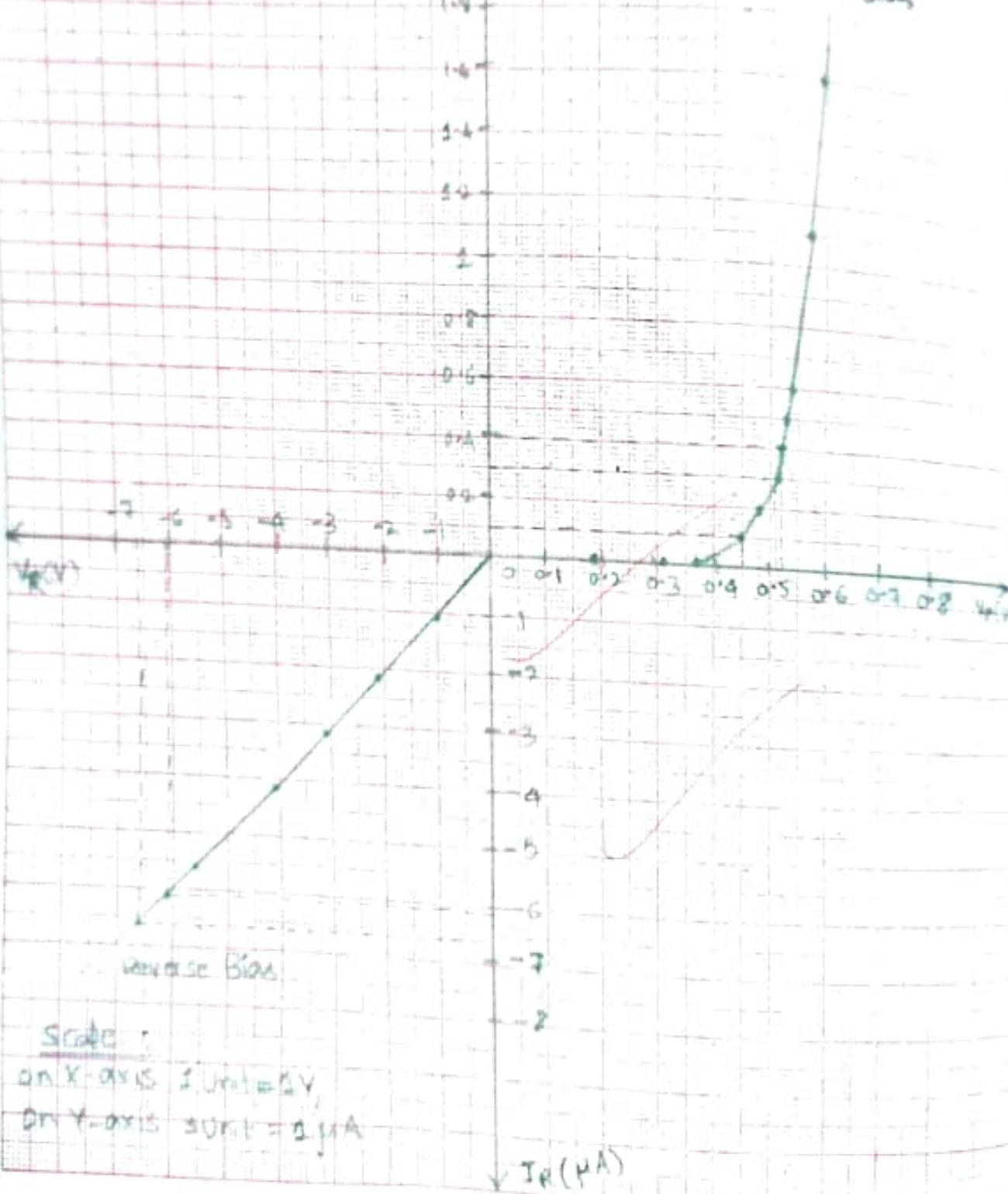
PN Junction Diode

I_f (mA)

Scale

On X-axis 1 unit = 0.1 mA
On Y-axis 1 unit = 0.1 A

Forward Bias



Reverse Bias

Scale :

on X-axis 1 unit = 0.1 mA

on Y-axis 1 unit = 0.1 A

$I_r(\mu A)$

V-I Characteristics of Zener Diode.

Aim :- To plot the VI characteristics of a zener diode in both forward and reverse biased condition and find out the zener breakdown voltage.

Apparatus Required :-

Sl.No.	Description	Range/Number/ Value	Quantity
1.	Regulated Power Supply	(0-30) V	1
2.	Zener diode	FZ 5.1 V	1
3.	DC ammeters	(0-200) mA	1
4.	DC Voltmeter	(0-1) V, (0-20) V	1 of each category
5.	Resistor	1 K Ω	1
6.	Bread board and connecting wires		

Theory:-

A zener diode is heavily doped P-N junction diode , specially made to operate in the sharp breakdown voltage . A P-N junction diode normally does not conduct when reverse biased. But zener diodes have ~~a~~ good conduction when reverse biased.

When forward biased , its characteristics are just those of ordinary diode.

In reverse bias , if the reverse bias is increased , at a particular voltage it starts conducting heavily . This voltage is called Breakdown Voltage . The zener diode is not immediately

burnt just because it has entered the breakdown region.

It is mainly used in voltage regulators.

Procedure :-

A) Forward Bias :-

1. Connections are made as per the circuit diagram.
2. The regulated power supply voltage is increased in steps.
3. The forward current (I_F) and the forward voltage (V_F) are observed and then noted in the tabular form.
4. A graph is plotted between Forward Current (I_F) on X-axis and the forward voltage (V_F) on Y-axis.

B) Reverse Bias :-

1. Connections are made as per the ~~circuit~~ diagram.
2. Switch ON the power supply and increase the input voltage (Supply voltage) ~~in~~ steps of 1V.
3. The Reverse Current (I_R) and the Reverse Voltage (V_R) are observed and then noted in the tabular form.
4. A graph is plotted between Reverse Current (I_R) on X-axis and the Reverse Voltage (V_R) on Y-axis.

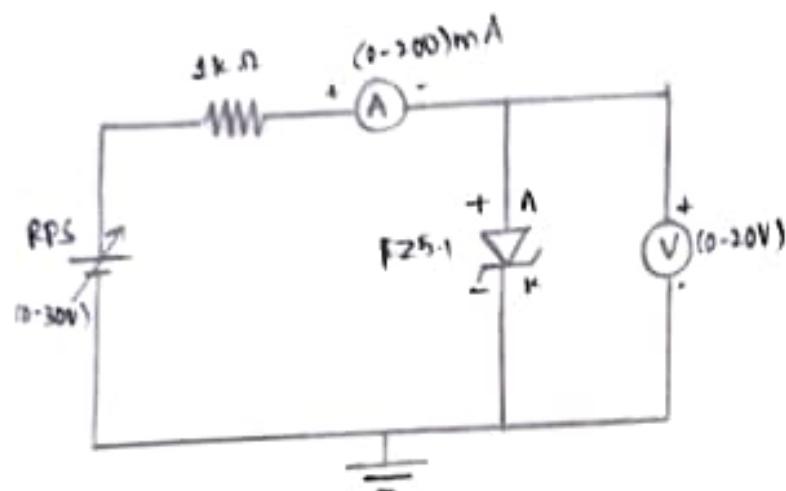
Result :- The forward and ~~reverse~~ characteristics of the zener diode has been plotted.

The breakdown voltage of the zener diode = -7.37 V.

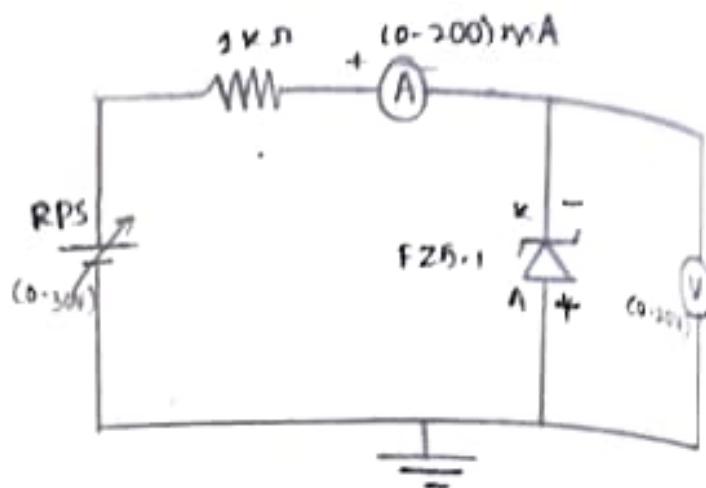
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Circuit Diagram :-

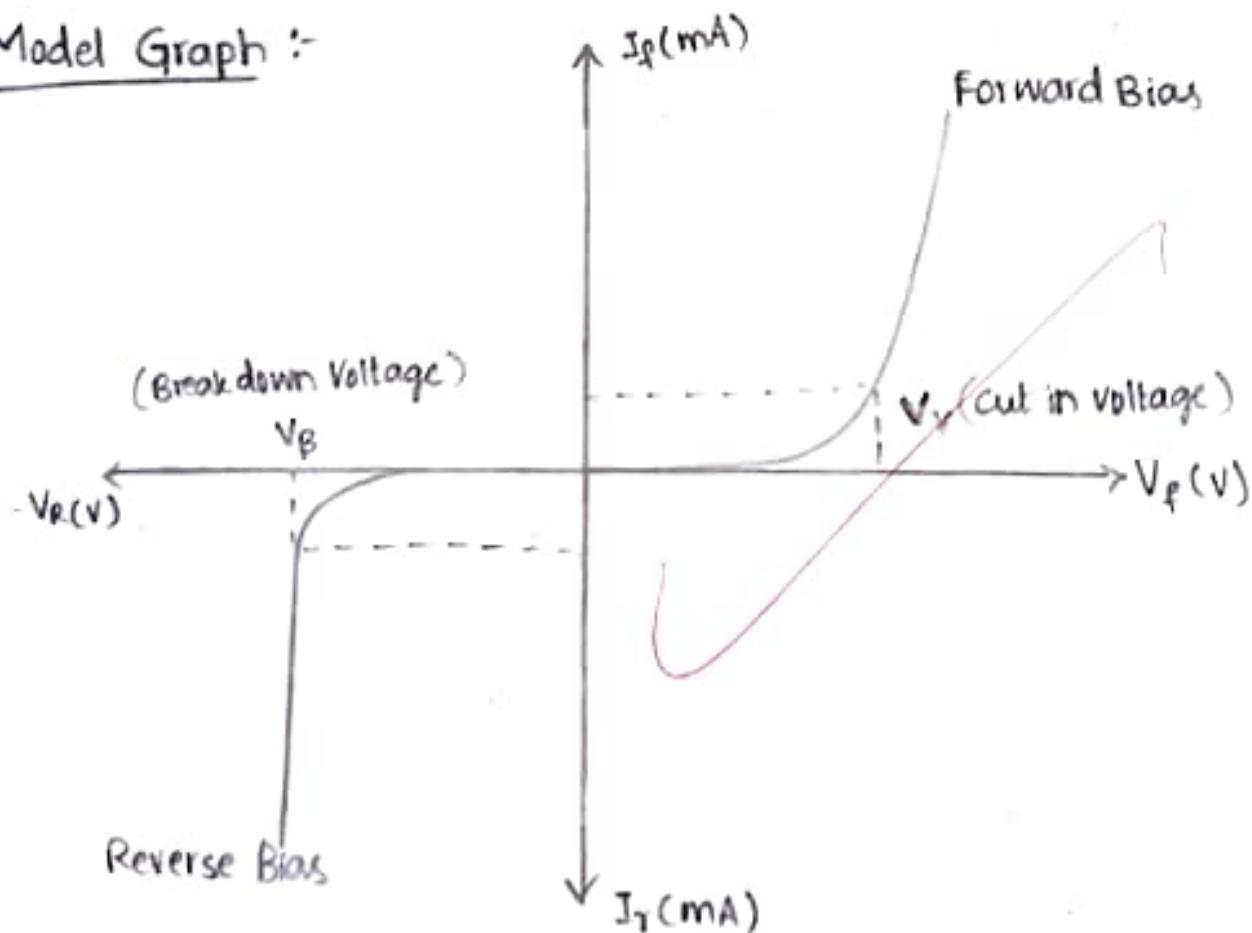
A) Forward Bias :



B) Reverse Bias :



Model Graph :-



Observations :-

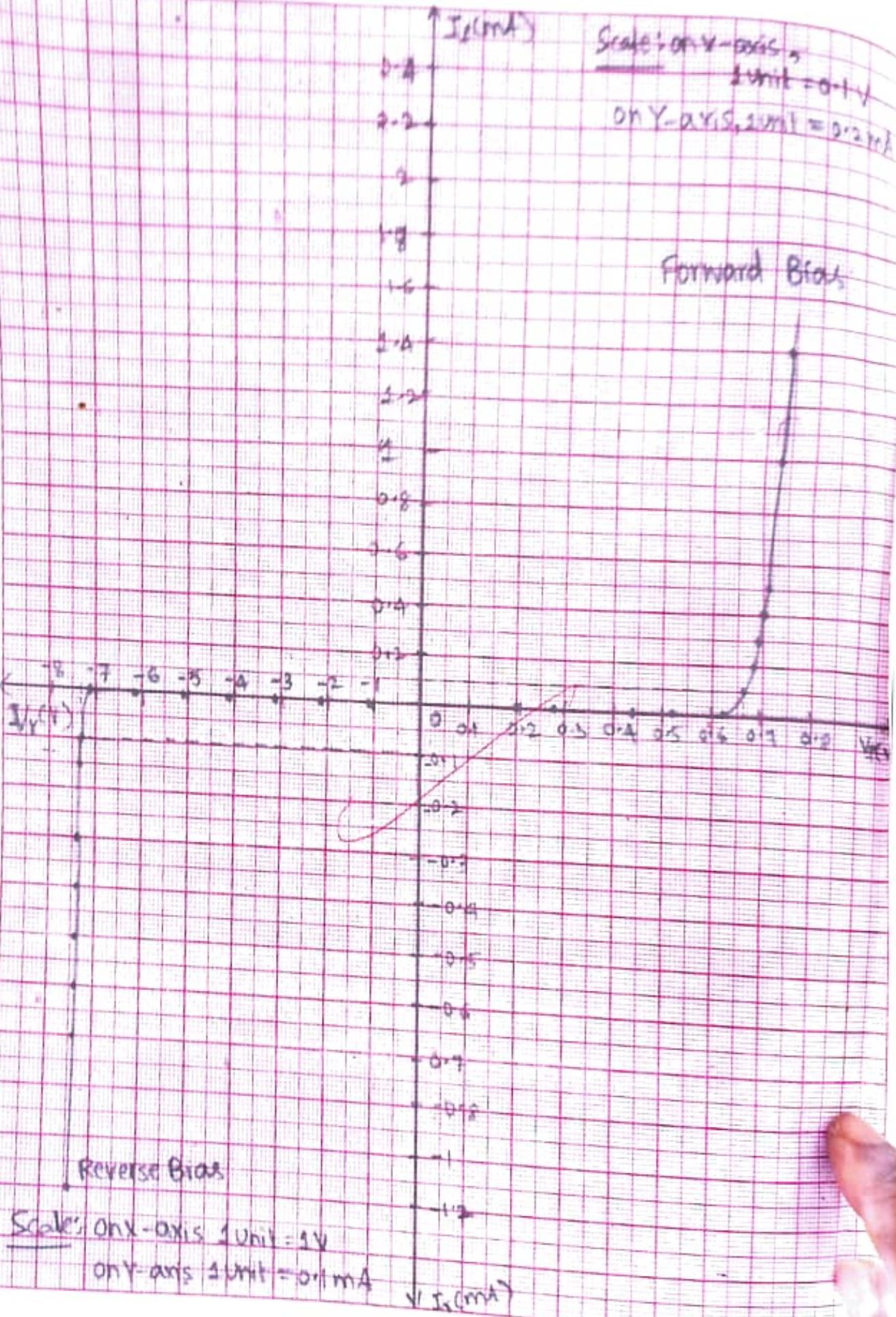
A) Forward Bias:

Sl. No.	Applied Voltage(V) in Volts	Forward Voltage (V_F) in Volts	Forward Current (I_F) in mA
1.	0.1	0.2	0
2.	0.2	0.28	0
3.	0.3	0.44	0
4.	0.4	0.52	0
5.	0.5	0.62	0
6.	0.6	0.66	0.1
7.	0.7	0.68	0.2
8.	0.8	0.69	0.3
9.	0.9	0.7	0.4
10.	1	0.71	0.5
11.	1.5	0.73	1
12.	2	0.74	1.4
13.	2.5	0.75	1.9
14.	3	0.76	2.4
15.	3.5	0.77	2.9

B) Reverse Bias:

Sl. No.	Applied Voltage(-V) in Volts	Reverse Voltage (- V_R) in Volts	Reverse Current (- I_R) in mA
1.	1	1.14	0
2.	2	2.18	0
3.	3	3.15	0
4.	4	4.16	0
5.	5	5.16	0
6.	6	6.15	0
7.	7	7.14	0
8.	7.4	7.37	0.1
9.	7.5	7.39	0.2
10.	7.6	7.39	0.3
11.	7.7	7.40	0.4
12.	7.8	7.40	0.5
13.	7.9	7.40	0.6
14.	8	7.4	0.7
15.	8.5	7.43	1.2

Zener Diode



Half Wave Rectifier With And Without Filter

Aim :- To examine the input and output waveforms of half wave rectifier using without and with filter and also calculate its ripple factor.

Apparatus Required :-

Sl.No.	Description	Range / Number / Value	Quantity.
1.	Regulated Power Supply	(0-30) V	1
2.	Diode	1N4007	1
3.	Transformer	0-12V	1
4.	Decade Resistor Box		1
5.	Capacitor	100 μ F	1
6.	CRO		1
7.	Bread board and Connecting wires		

Theory :-

When AC Supply is applied at the input, only positive Half Cycle appears across the load. whereas, the negative Half Cycle is suppressed.

During positive Half Cycle of the input voltage, the diode D is in forward bias and current flows through the load resistor R_L . Hence the current produces an output voltage across the load resistor R_L , which has

the same shape as the +ve half cycle of the input voltage.

During the negative half Cycle of the input voltage, the diode is reverse biased and there is no current through the load resistor R_L . so the voltage across R_L is zero.

The net result is that only the +ve half cycle of the input voltage appears across the load.

Procedure :-

A) Without filter :-

1. Connections are made as per the circuit diagram.
2. Switch ON the transformer and CRO.
3. Vary the load resistance R_L and notedown the value of V_m and find the value of ripple factor.
4. Measure the input at secondary terminal of transformer and output across load resistance R_L .
5. Plot the Input and Output waveforms on graph sheet.

B) With filter :-

1. Connections are made as per the circuit diagram.
2. Switch ON the transformer and CRO.
3. Vary the load resistance R_L and notedown the value of V_m and find the value of ripple factor.
4. Measure the input at secondary terminal of transformer

and output across load resistance R_L .

5. Plot the Input and Output waveforms on Graph Sheet.

Result :-

The input and output waveforms of half wave rectifier are measured using without and with filter and also calculated its ripple factor.

The ripple factor of half wave rectifier using without filter

is 1.2.

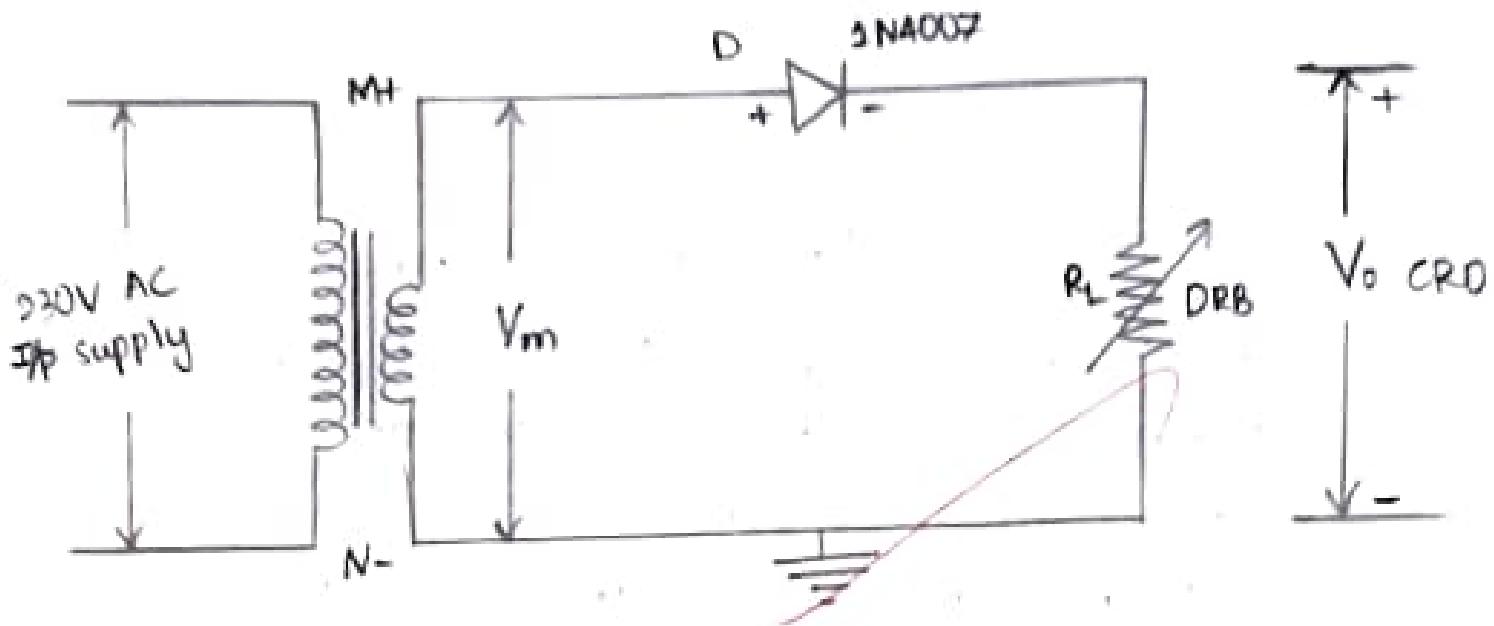
The ripple factor of half wave rectifier using with filter

is 0.2.

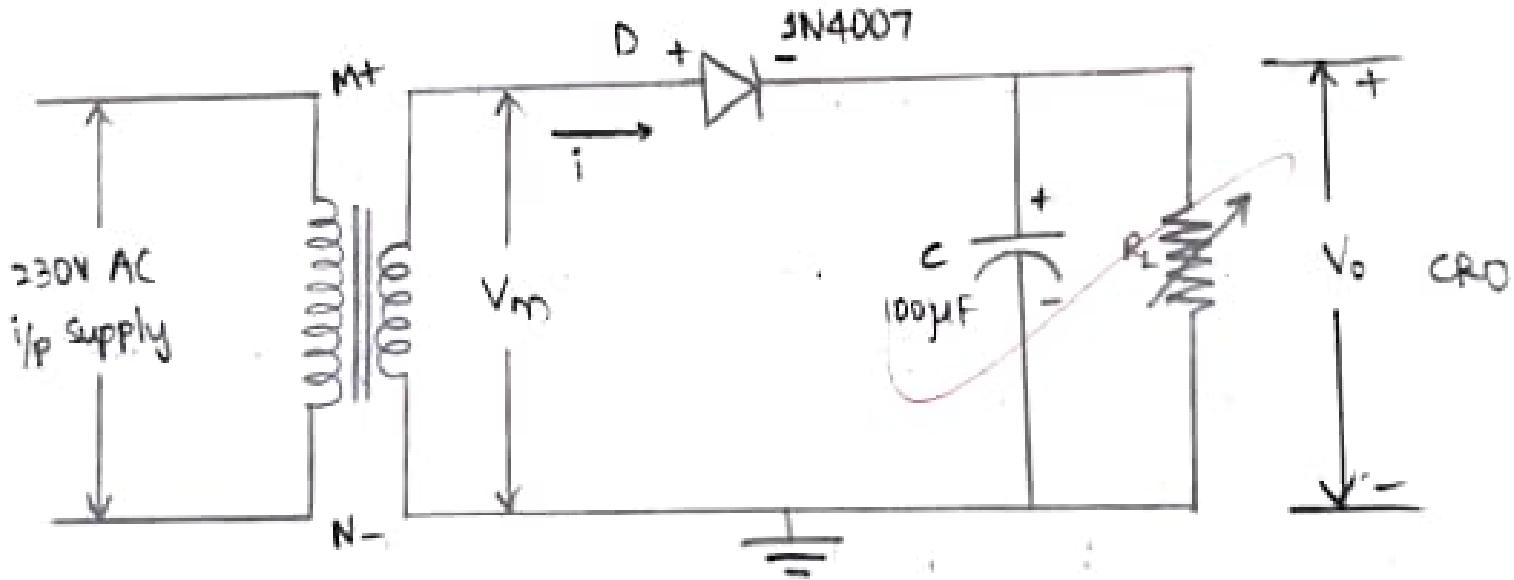
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Circuit Diagram :-

A) Without Filter :



B) With Filter :



Observations :

a) Without Filter

S.I. No.	Load resistance eR_L (Ω)	V_m (volts)	$V_{dc} = \frac{V_m}{\pi}$	$V_{rms} = \frac{V_m}{2}$	$\gamma = \sqrt{\left(\frac{V_{rms}}{V_{dc}}\right)^2 - 1}$
1.	0	0.6×10	1.9	3	1.2
2.	100	1.4×10	4.45	4	1.2
3.	200	1.6×10	5.09	8	1.2
4.	500	1.7×10	5.41	8.5	1.2

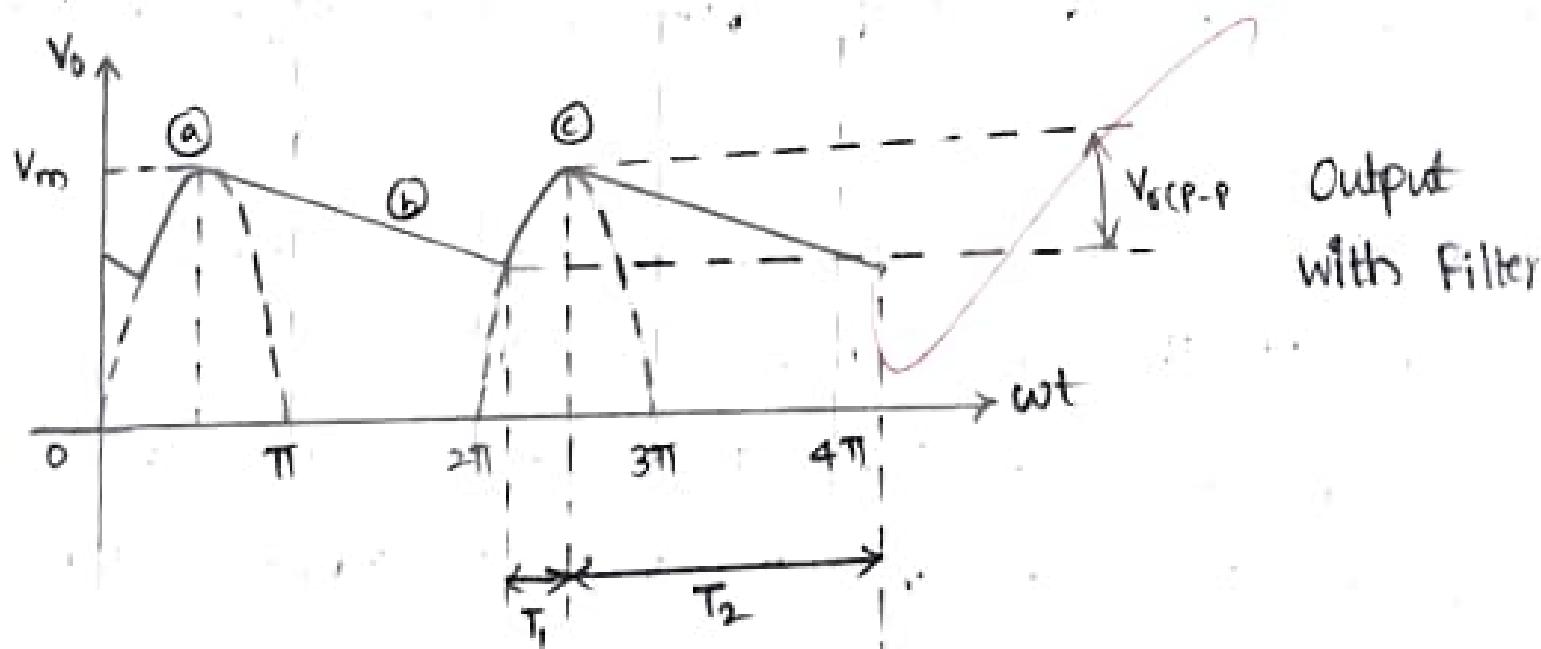
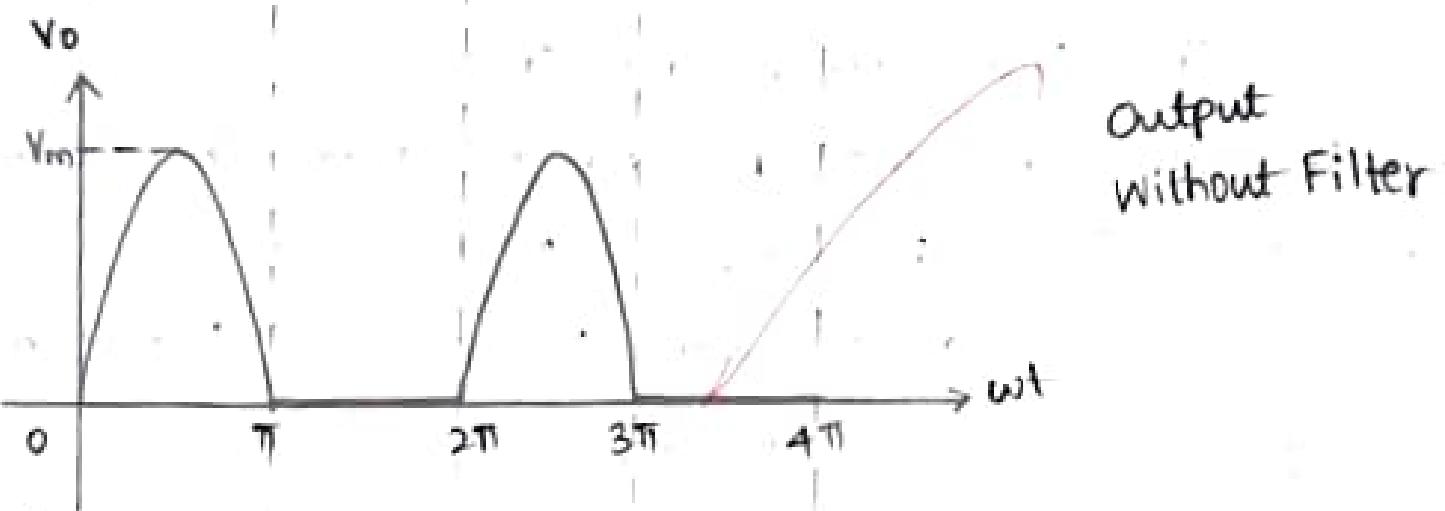
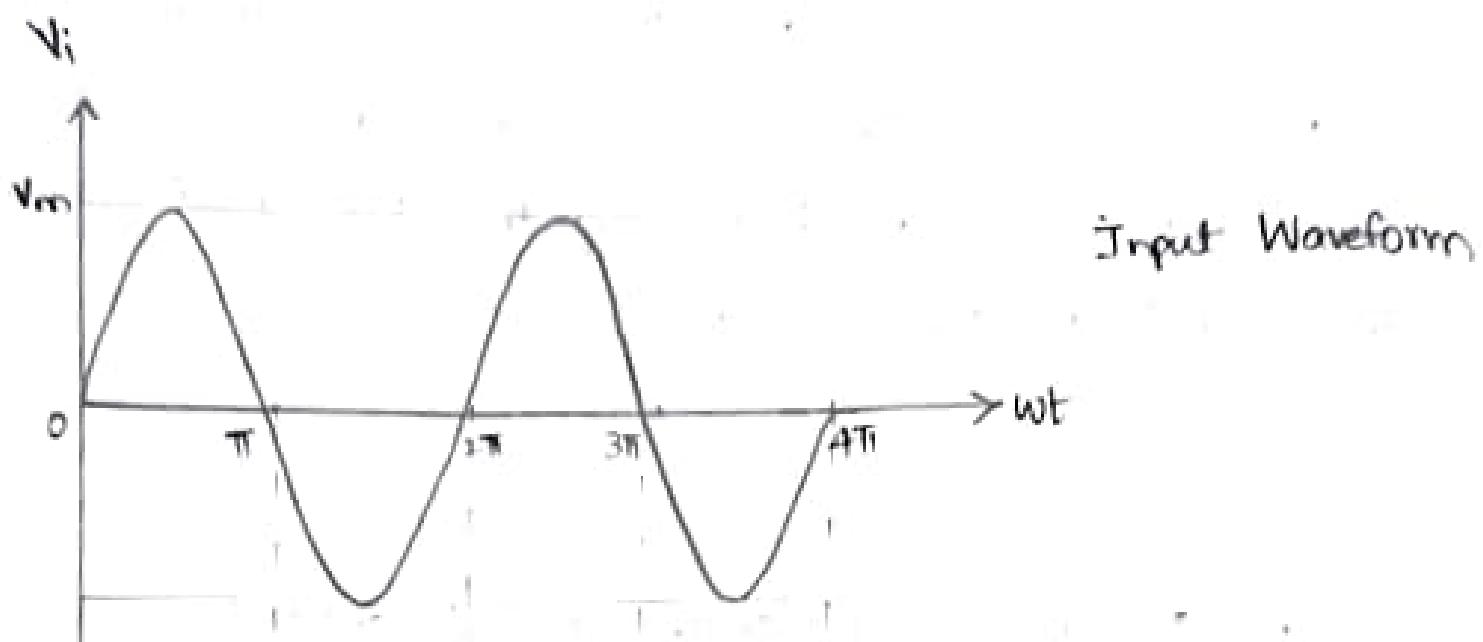
b) With Filter

S.I. No.	Load resistance R_L (Ω)	V_m (volts)	$V_{(P-P)}$ (volts)	$V_{dc} = V_m - \frac{V_{(P-P)}}{2}$	$V_{rms} = \frac{V_{(P-P)}}{2\sqrt{3}}$	$\gamma = \frac{V_{rms}}{V_{dc}}$
1.	0	5	3	3.5	0.86	0.24
2.	100	13	9	8.5	2.59	0.305
3.	500	15	11	9.5	3.17	0.334
4.	1000	16	12	10	3.46	0.345

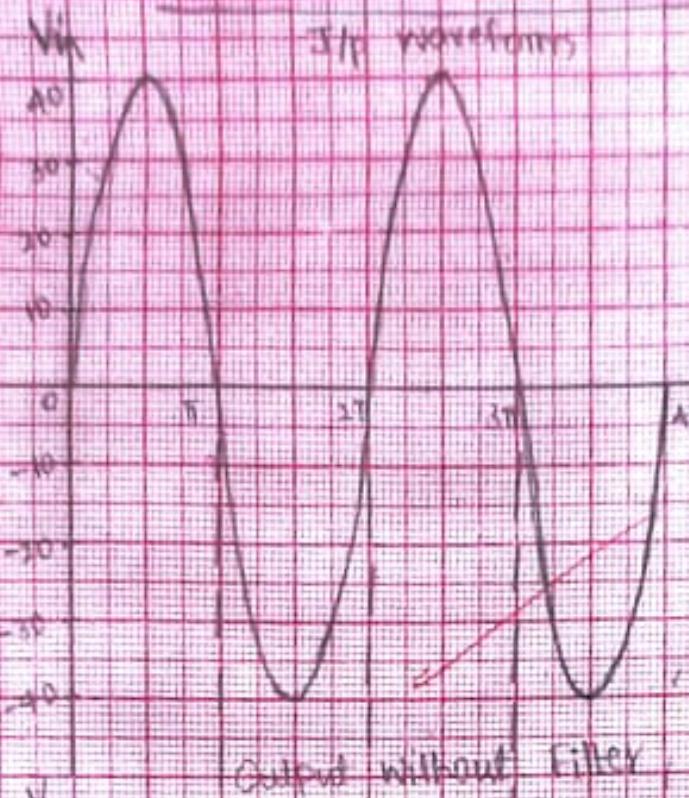
c) Measuring Waveforms

Type	Time period	Amplitude
Input Waveform	$4 \times 5 = 20$ milli sec	$3.9 \times 10 = 39$
Output Waveform without filter	.	$0.6 \times 10 = 6$
Output Waveform with filter		5

Input & Output Waveforms:

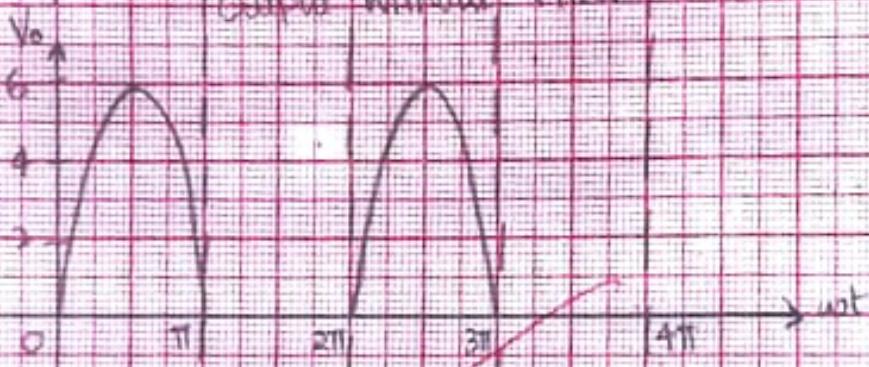


Half Wave Rectifier With & Without Filter



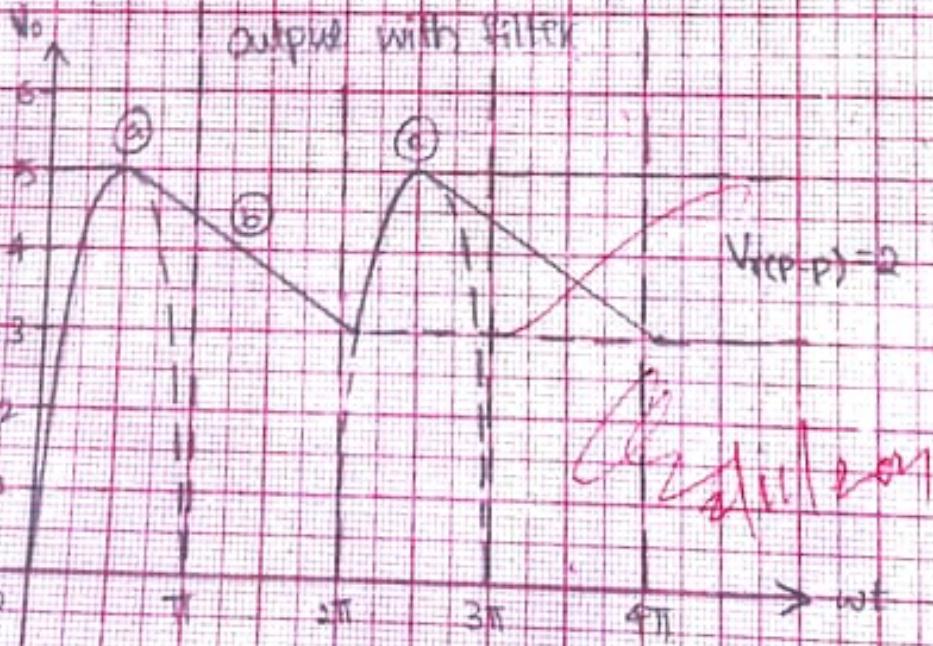
Scale -
on x-axis,
 $1 \text{ unit} = 90^\circ$
on y-axis,
 $1 \text{ unit} = 10V$

Output without filter



Scale:
on x-axis,
 $1 \text{ unit} = 90^\circ$
on y-axis,
 $1 \text{ unit} = 2V$

Output with filter



Scale -
on x-axis,
 $1 \text{ unit} = 90^\circ$
on y-axis,
 $1 \text{ unit} = 1V$

Full Wave Rectifier With and Without Filter.

Aim :- To examine the input and output waveforms of full wave rectifier using without and with filter and also calculate its ripple factor.

Apparatus Required :-

S.I.No	Description	Range/Number/Value	Quantity
1.	Regulated Power Supply	(0-30)V	1
2.	Diode	1N4007	2
3.	Transformer	12 - 0 - 12 V	1
4.	Decade Resistance Box		1
5.	Capacitor	100 μF	1
6.	CRO		1
7.	Breadboard and Connecting wires		

Theory :-

When AC supply is applied at the ~~input~~, output appears across the load for both the ~~cycles~~ of input.

During positive half cycle of secondary voltage the diode D₁ is forward biased and D₂ is reverse biased. So, the diode D₁ conducts and current flows through

Load resistance R_L .

During negative half cycle of secondary voltage, the diode D_2 becomes forward biased and D_1 reverse biased. Now D_2 conducts and current flows through load resistor R_L in the same direction. There is a continuous current flow through the load resistor R_L , during the both the half cycles and will get unidirectional current.

A Full Wave Rectifier allows unidirectional current to the load during the entire 360° degrees of the input signal.

Procedure :-

A) Without Filter :-

1. Connections are made as per the circuit diagram.
2. Switch on the transformer and CRO.
3. Vary the load resistance R_L and note down the value of V_m and find the value of ripple factor.
4. Measure the input of secondary terminal of transformer and output across load terminal R_L .
5. Plot the Input and Output waveforms on graph sheet.

B) With Filter :-

1. Connections are made as per the circuit diagram
2. Switch on the transformer and CRO.
3. Vary the load resistance R_L and notedown the value of V_m and find the value of ripple factor.
4. Measure the input at secondary terminal of transformer and output across load resistance R_L .
5. Plot the Input and Output waveforms on Graph Sheet.

Result :-

The input and output waveforms of full wave rectifier are measured using without and with filter and also calculated its ripple factor.

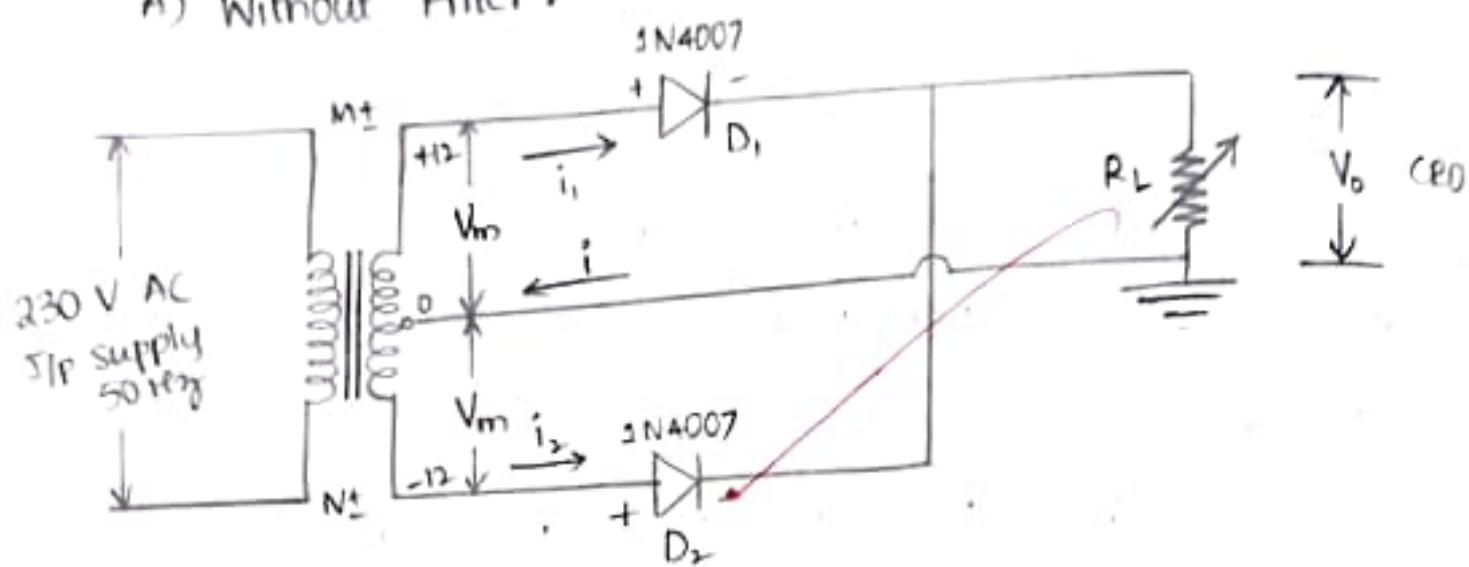
The ripple factor of full wave rectifier using without filter is 0.48

The ripple factor of full wave rectifier using with filter is 0.27.

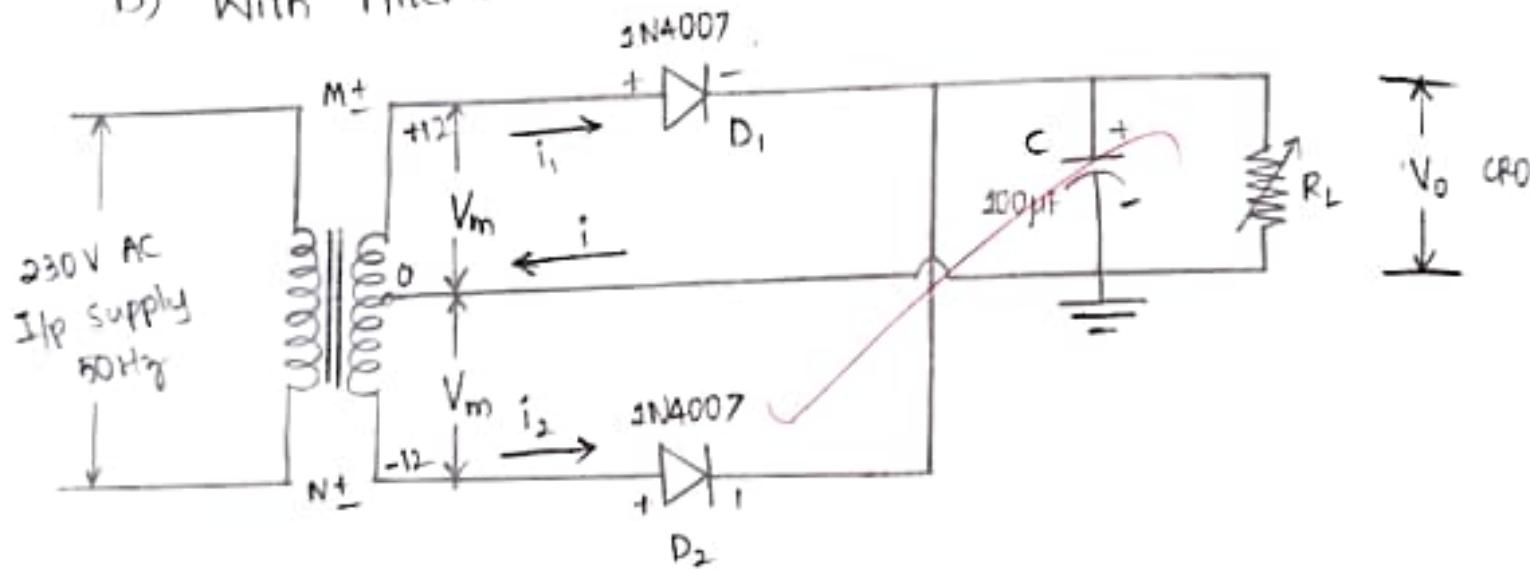
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10/11/2021

Circuit Diagram:

A) Without Filter:



B) With Filter:



Observations :-

A) Without filter :

S.No.	Load Resistance $\times R_L (\Omega)$	V_m (Volts)	$V_{dc} = \frac{2V_m}{\pi}$	$V_{rms} = \frac{V_m}{\sqrt{2}}$	$\beta = \frac{V_{rms}}{V_{dc}} - 1$
1.	0	$0.6 \times 10 = 6$	3.82	4.24	0.48
2.	100	$1.4 \times 10 = 14$	8.9	9.89	0.48
3.	500	$1.6 \times 10 = 16$	10.19	11.31	0.48
4.	1K	$1.6 \times 10 = 16$	10.19	11.31	0.48
5.	10K	$1.6 \times 10 = 16$	10.19	11.31	0.48

B) With Filter :

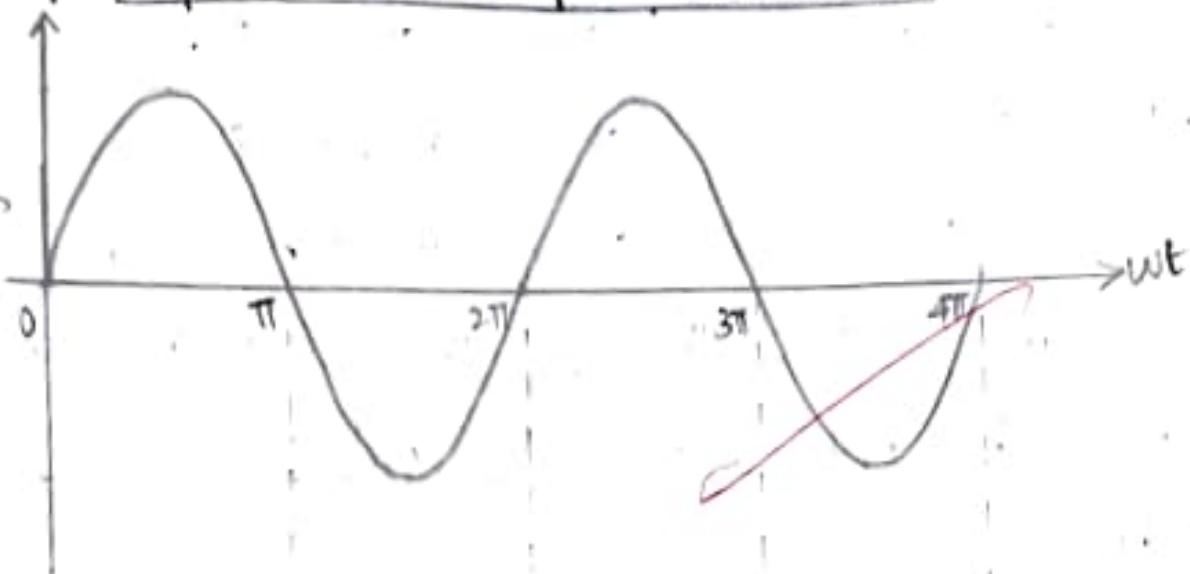
S.No.	Load resistance $\times R_L (\Omega)$	V_m (Volts)	$V_{r(p-p)}$ (Volts)	$V_{dc} = V_m - \frac{V_{r(p-p)}}{2}$	$V_{rms} = \frac{V_{r(p-p)}}{2}$	$\beta = \frac{V_{rms}}{V_{dc}}$
1.	100	$1.4 \times 10 = 14$	$0.6 \times 10 = 6$	11	$\frac{6}{11} = 0.545$	0.27
2.	500	$1.6 \times 10 = 16$	$0.2 \times 10 = 2$	16	$\frac{2}{16} = 0.125$	0.062
3.	1K	$1.2 \times 10 = 12$	$0.2 \times 10 = 2$	12	$\frac{2}{12} = 0.167$	0.083
4.	10K	$1.2 \times 10 = 12$	0	12	0	0

c) Measuring Waveforms :

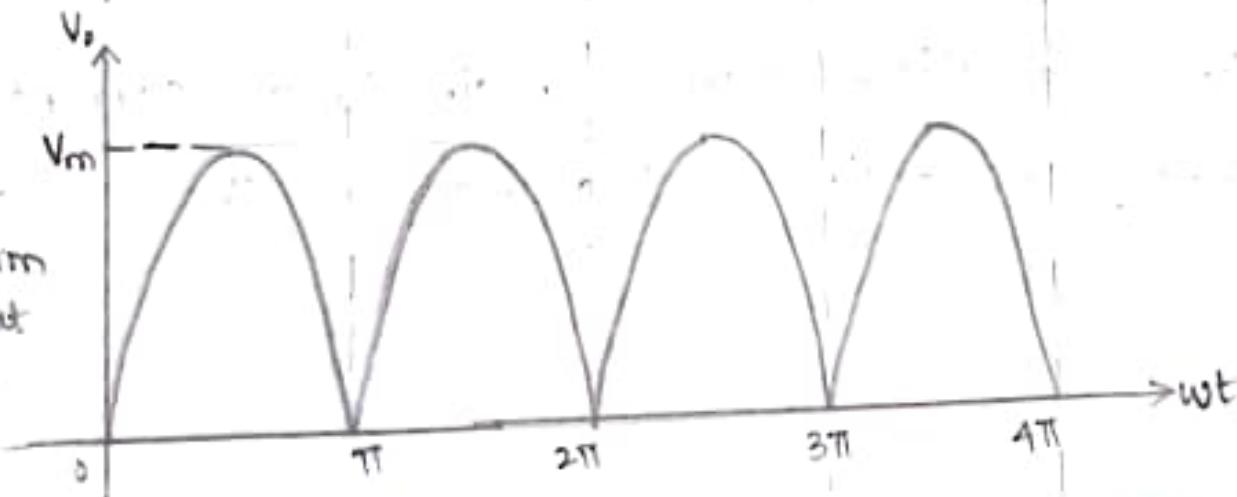
Type	Time period	Amplitude
Input Waveform	$4 \times 5 \text{ ms} = 20 \text{ ms}$	$1 \times 10 = 10 \text{ V}$
O/p waveform Without Filter		$0.6 \times 10 = 6 \text{ V}$
O/p waveform With filter		$1.4 \times 10 = 14 \text{ V}$

V_i: Input and Output Waveforms:

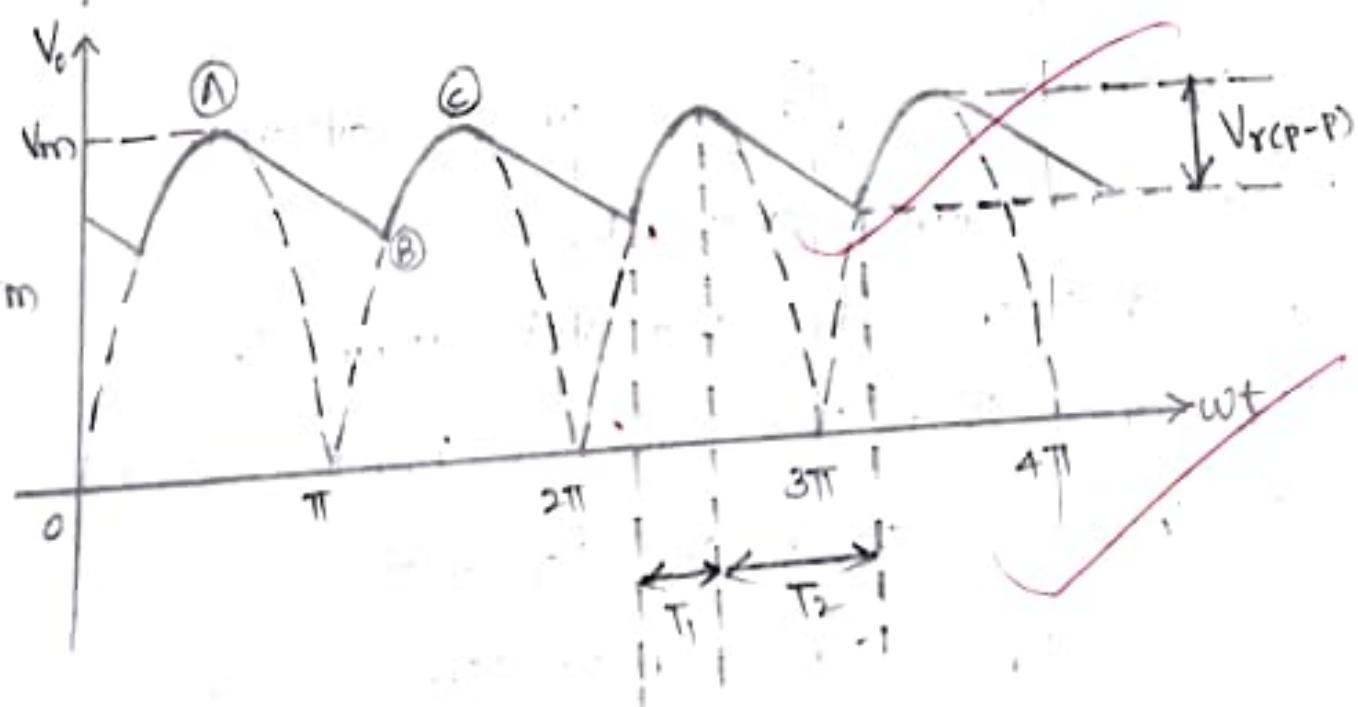
Input
Waveform



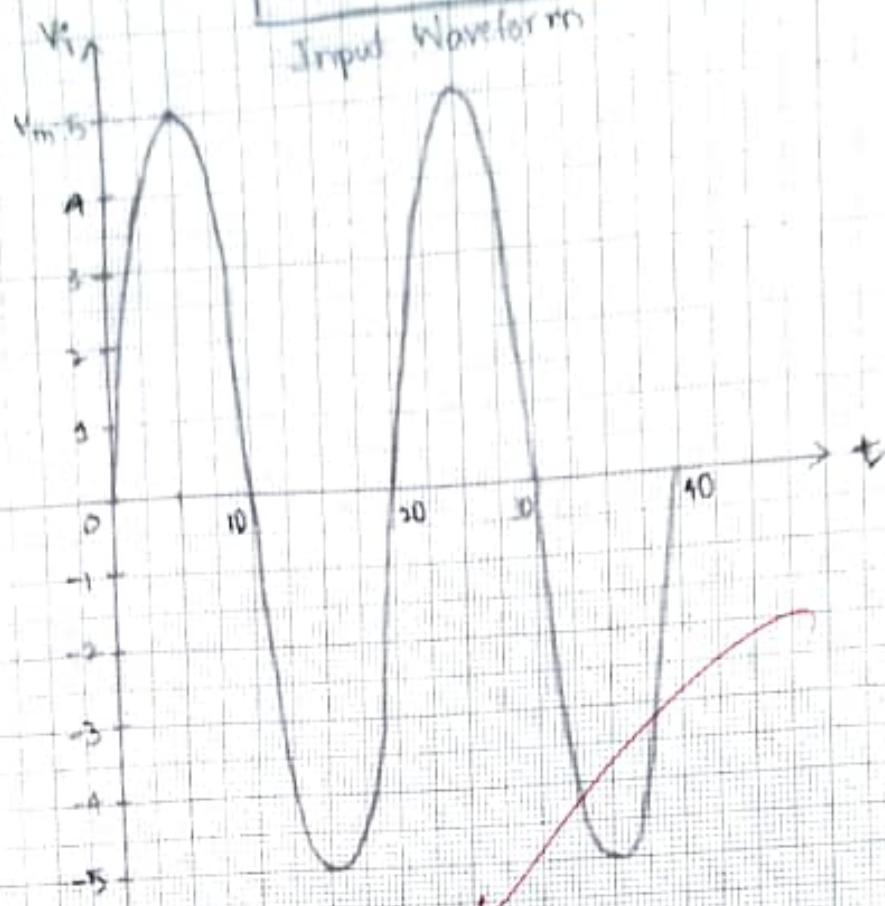
Output
Waveform
without
Filter



Output
Waveform
with
filter

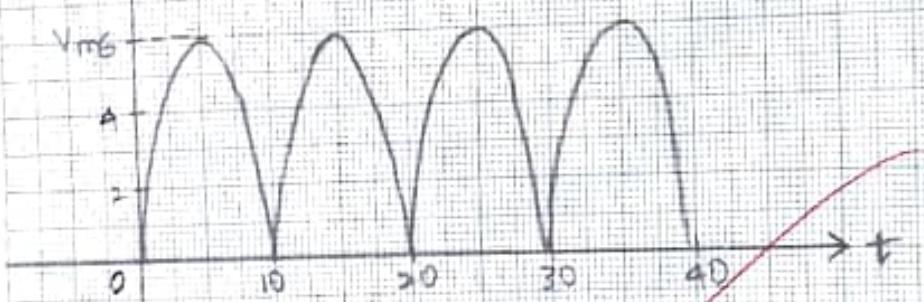


Full Wave Rectifier With & Without Filter



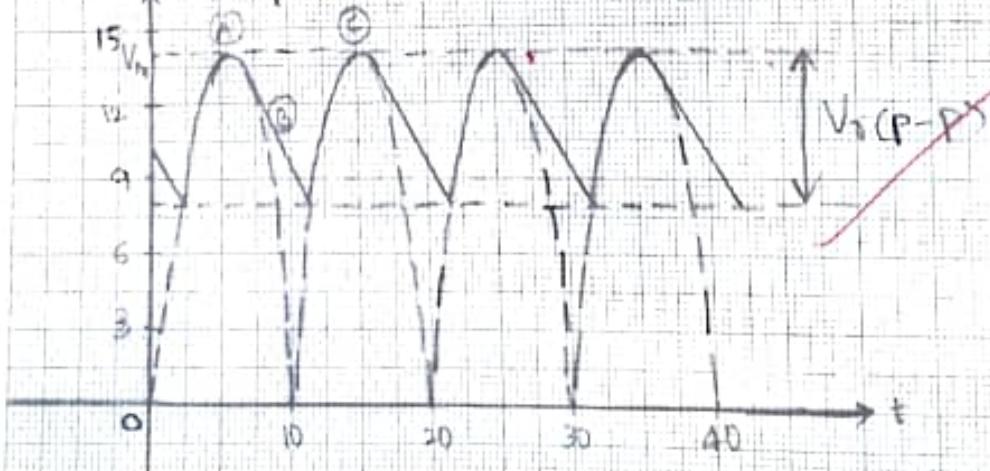
Scale :-
on X-axis,
1 unit = 10 msec
on Y-axis,
1 unit = 1V

Output waveform with filter



Scale :-
on X-axis,
1 unit = 5 msec
on Y-axis,
1 unit = 2V

Output waveform without filter



Scale :-
on X-axis,
1 unit = 5 msec
on Y-axis,
1 unit = 3V

Input and Output Characteristics of Transistor in CB Configuration

Aim :- To plot the input and output characteristics of a transistor connected in common base configuration and to find the h-parameters from the characteristics.

Apparatus Required :-

Sl.No.	Description	Range / Number / Value	Quantity
1.	Regulated Power Supply.	(0-30)V	1
2.	Transistor	BC107	1
3.	DC Ammeter	(0-200) mA	2
4.	DC Voltmeter	(0-20)V	2
5.	Resistor.	1K Ω	2
6.	Breadboard and connecting wires.		

Theory :-

A transistor is a three terminal active device. The terminals are emitter, base, collector. For normal operation, In CB configuration the base is common to both input (emitter) and output (collector). For normal operation, the E-B junction is forward biased and C-B junction is reverse biased.

This transistor configuration provides low input impedance while offering high output impedance. Although the voltage is

high, the current gain is low and the overall power gain is also low when compared to the other transistor configurations. The other salient feature of this configuration is that the input and output are in phase.

Procedure :-

A) Input Characteristics :-

1. Connections are made as per the circuit diagram.
2. Keep V_{CE} and V_C knob in minimum position before turn on the regulated power supply.
3. Set $V_{CB} = 1V$ by varying V_{CC} .
4. Then vary the V_{CE} smoothly with fine control and noted down the value of emitter current I_E & base emitter voltage V_{EB} and put it in tabular form.
5. Repeat the experiment by keeping $V_{CB} = 2V$ and $4V$.
6. Draw a graph between V_{EB} (V_s) V_E against $V_{CB} = \text{constant}$.

B) Output Characteristics :-

1. Connections are made as per the circuit diagram.
2. Keep V_{CE} and V_C knob in minimum position before turn on the regulated power supply.
3. Set $I_E = 5\text{ mA}$ by varying V_{CE} .
4. Then vary the V_{CC} smoothly with fine control and noted down the value of emitter current I_C and base collector voltage V_{CB} and put it in tabular form.
5. Repeat the experiment by keeping $I_E = 10\text{ mA}$ and 20 mA .

6. Draw a graph between V_{CB} Vs I_C against $I_E = \text{constant}$.

To find the h-parameters:

calculation of h_{ib} :

Mark two points on the Jinput characteristics for constant V_{CB} .

Let the coordinates of these two points be (V_{BE2}, I_{E2}) & (V_{BE1}, I_{E1})

$$h_{ib} = \frac{V_{EB2} - V_{EB1}}{I_{E2} - I_{E1}}$$

Calculations of h_{ib} :

~~Draw a horizontal line at some constant I_E value on the input characteristics. Find V_{CB2} , V_{CB1} & V_{EB2} , V_{EB1} .~~

$$h_{ib} = \frac{V_{EB2} - V_{EB1}}{V_{CB2} - V_{CB1}}$$

Calculations of h_{fb} :

Draw a vertical line on the output characteristics at some constant V_{CB} value. Find I_{C2} , I_{C1} & I_{E2} , I_{E1}

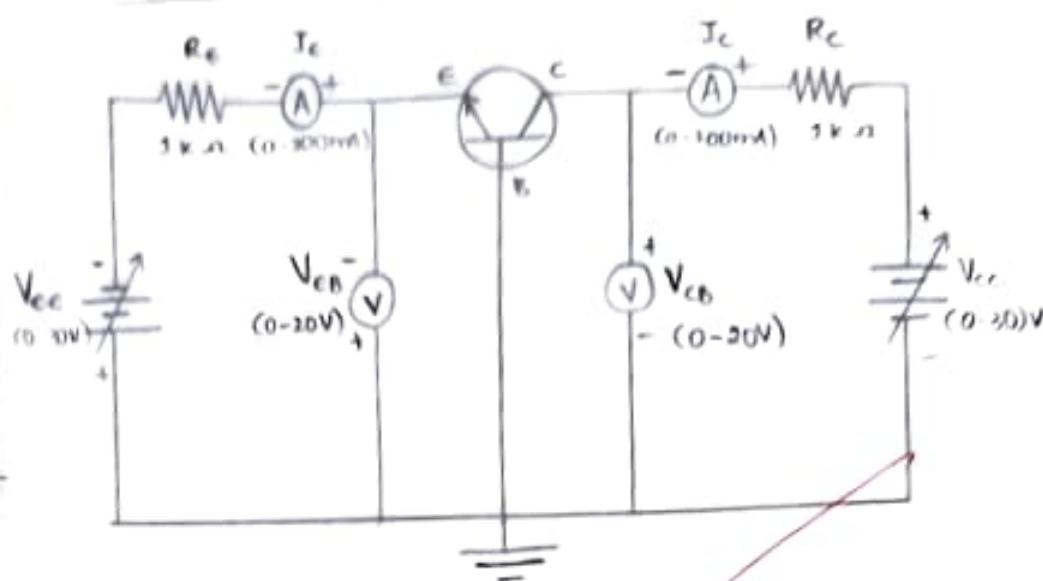
$$h_{fb} = \frac{I_{C2} - I_{C1}}{I_{E2} - I_{E1}}$$

calculations of h_{ob} :- Mark two points on input characteristics for constant I_E . Let coordinates of these points be (V_{CB2}, I_C2) & (V_{CB1}, I_C1)

$$h_{ob} = \frac{I_{C2} - I_{C1}}{V_{CB2} - V_{CB1}}$$

Result :- The input and output characteristics of transistor in CB configuration are drawn on the graph and h parameters are calculated: $h_{ib} = 48 \Omega$ $h_{ib} = -0.06$
 $h_{ob} = 1$ $h_{fb} = 0.495 \text{ m}^{-2}$

Circuit Diagram :-



Pin Assignment

- 1 : Emitter
- 2 : Base
- 3 : Collector

Observation:-

A) Input Characteristics :-

PPS V_{EE} (V)	$V_{CB} = 1V$		$V_{CB} = 5V$	
	V_{EB} (V)	I_E (mA)	V_{EB} (V)	I_E (mA)
0.1	0.2	0	0.18	0
0.2	0.3	0	0.32	0
0.3	0.45	0	0.44	0
0.4	0.52	0	0.49	0
0.5	0.55	0.1	0.54	0.1
0.6	0.56	0.1	0.56	0.1
0.7	0.58	0.3	0.58	0.2
0.8	0.59	0.3	0.59	0.3
0.9	0.60	0.4	0.6	0.4
1	0.61	0.5	0.61	0.5
1.5	0.62	1.0	0.62	1.0
2	0.64	1.5	0.62	1.5
2.5	0.66	1.9	0.64	2.0
3	0.69	2.4	0.65	2.5
3.5	0.72	2.9	0.66	2.9

b) Output Characteristics :-

RPS $V_{CE}(V)$	$I_E = 3\text{mA}$		$I_E = 2\text{mA}$	
	$V_{CB}(V)$	$I_C(\text{mA})$	$V_{CB}(V)$	$I_C(\text{mA})$
0.1	-0.59	0.8	-0.69	0.9
0.2	-0.57	0.9	-0.68	1.0
0.3	-0.55	1.0	-0.68	1.1
0.4	-0.46	1.0	-0.67	1.2
0.5	-0.37	1.0	-0.67	1.3
0.6	-0.29	1.0	-0.66	1.4
0.7	-0.19	1.1	-0.65	1.5
0.8	-0.15	1.1	-0.64	1.6
0.9	-0.01	1.1	-0.63	1.7
1	0.11	1.1	-0.62	1.8
1.5	0.56	1.1	-0.39	2.1
2	1.04	1.1	0.08	2.1
2.5	1.57	1.1	0.56	2.1
3	2.06	1.1	1.04	2.1
3.5	2.57	1.1	1.56	2.1

Calculation of $h_{ib} \Rightarrow$

$$(V_{EB1}, I_{E1}) \& (V_{EB2}, I_{E2}) \\ (0.6, 0.4) \quad (0.72, 2.9)$$

$$h_{ib} = \frac{V_{EB2} - V_{EB1}}{I_{E2} - I_{E1}} = \frac{0.72 - 0.6}{(2.9 - 0.4) \times 10^{-3}} \\ = \frac{0.12 \times 10^3}{2.5} = 0.048 \times 10^3 = 48 \Omega$$

Calculation of $h_{rb} \Rightarrow$

$$(V_{EB2}, V_{CB1}), (V_{EB2}, V_{EB1}) \\ (1, 2) \quad (0.72, 0.66)$$

$$h_{rb} = \frac{V_{EB2} - V_{EB1}}{V_{CB2} - V_{CB1}}$$

$$= \frac{0.72 - 0.66}{1 - 2} = \frac{0.06}{-1} = -0.06$$

Calculation of $h_{fb} \Rightarrow$

$$(I_{C2}, I_{C1}), (I_{E2}, I_{E1})$$

$$h_{fb} = \frac{I_{C2} - I_{C1}}{I_{E2} - I_{E1}} = \frac{(2.1 - 1.1) \times 10^{-3}}{(2 - 1) \times 10^{-3}} = 1$$

Calculation of $h_{ob} \Rightarrow$

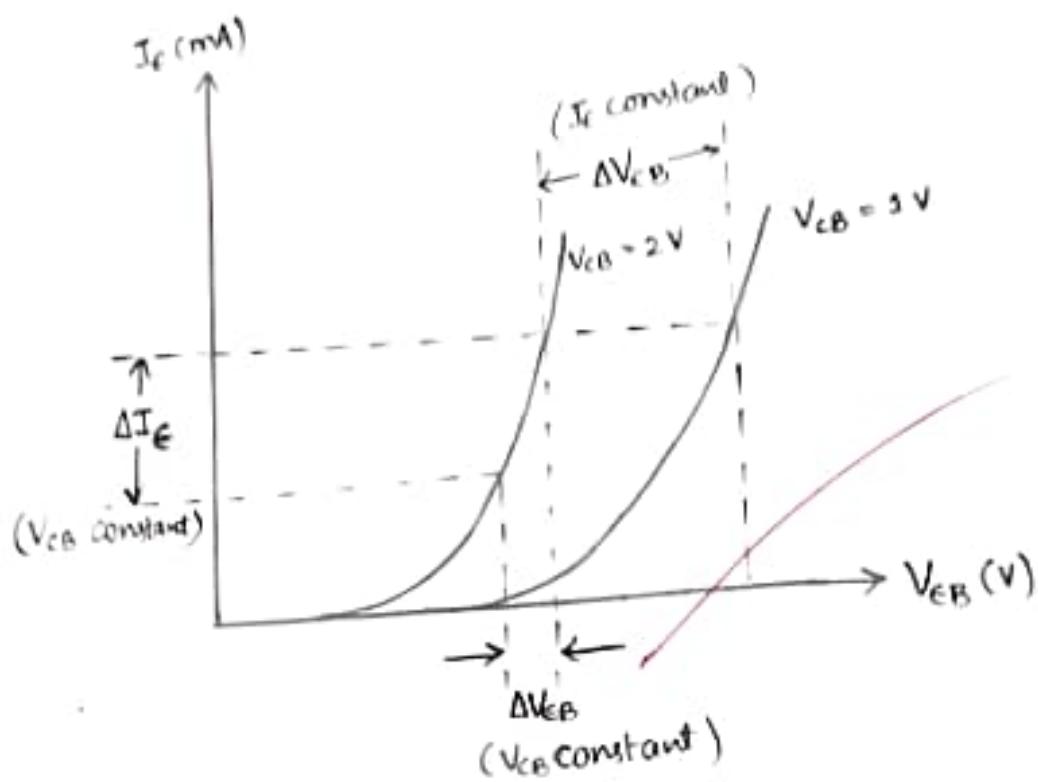
$$(V_{CB2}, I_{C2}), (V_{CB1}, I_{C1}) \\ 0.56 \quad 2.1 \quad -0.65 \quad 1.1$$

$$h_{ob} = \frac{I_{C2} - I_{C1}}{V_{CB2} - V_{CB1}}$$

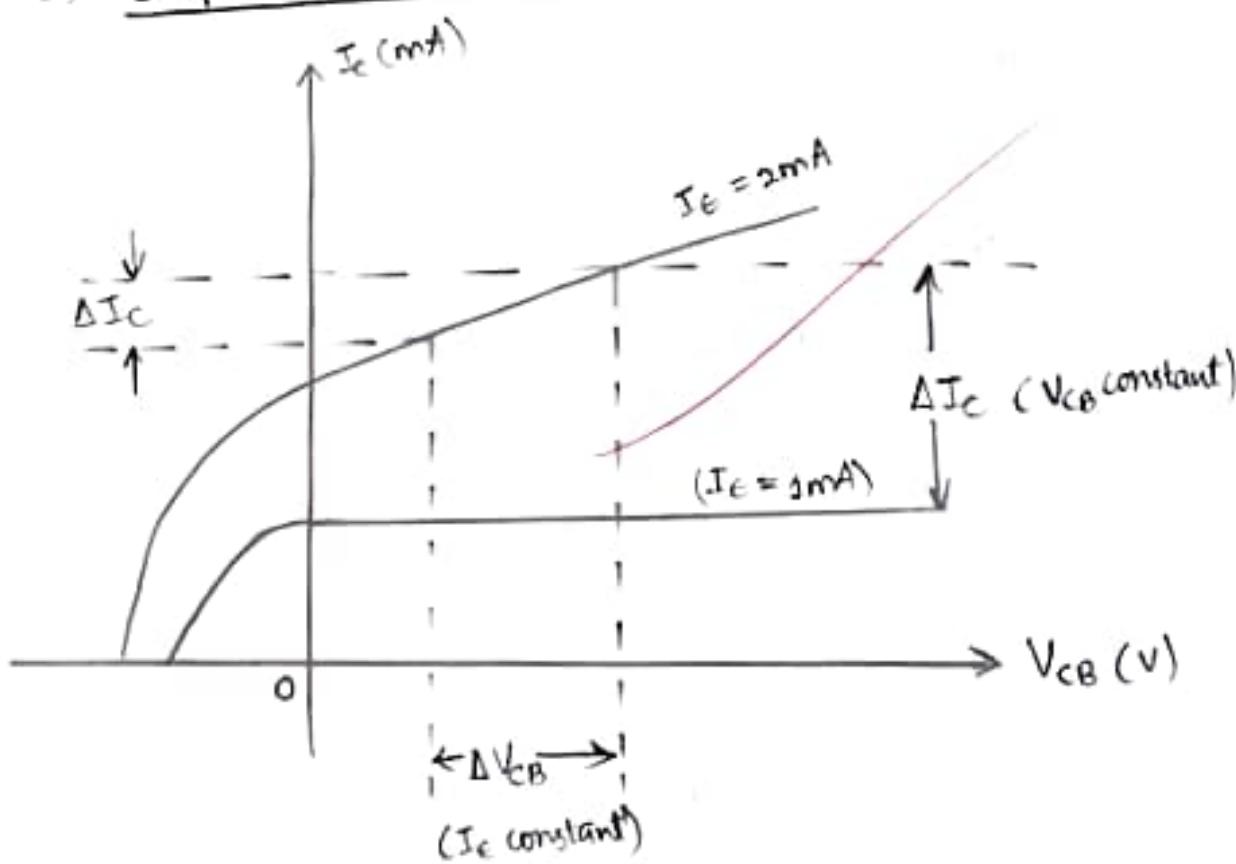
$$= \frac{(2.1 - 1.1) \times 10^{-3}}{(0.56) - (-0.65)} = \frac{0.6 \times 10^{-3}}{1.21} = 0.495 \text{ mV}$$

Model Graph :-

a) Input Characteristics :-

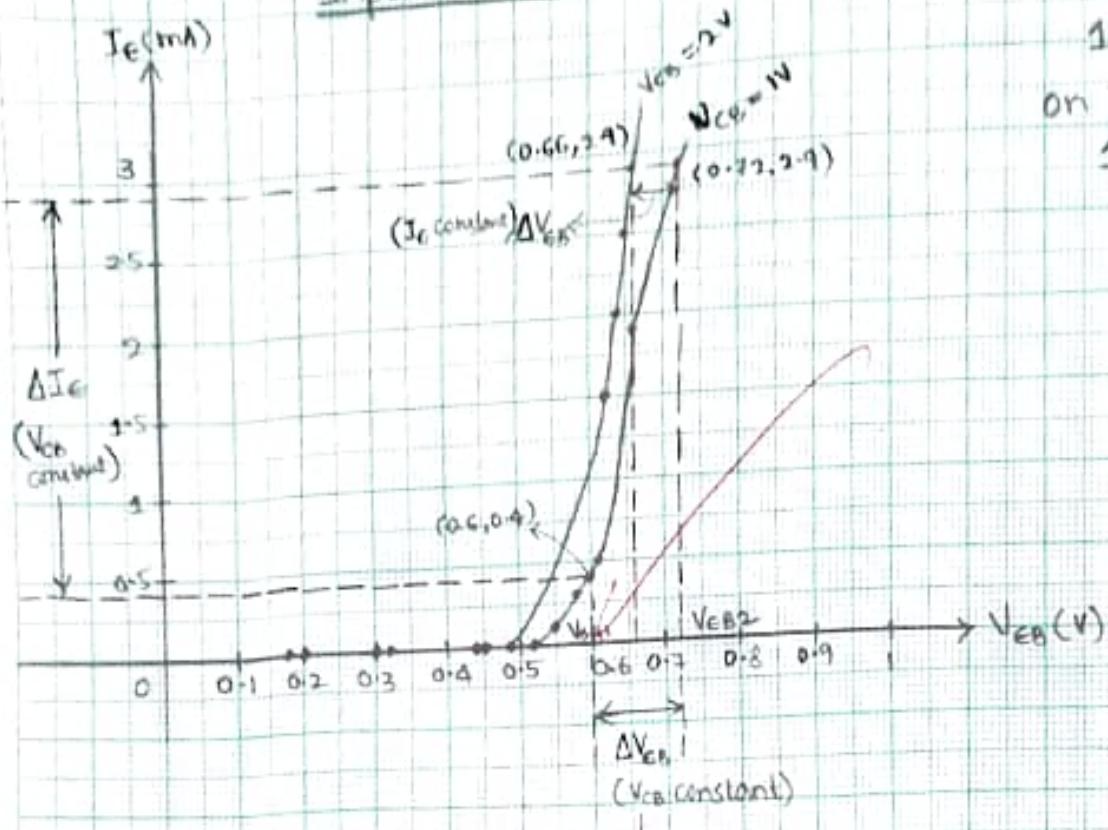


b) Output Characteristics :-

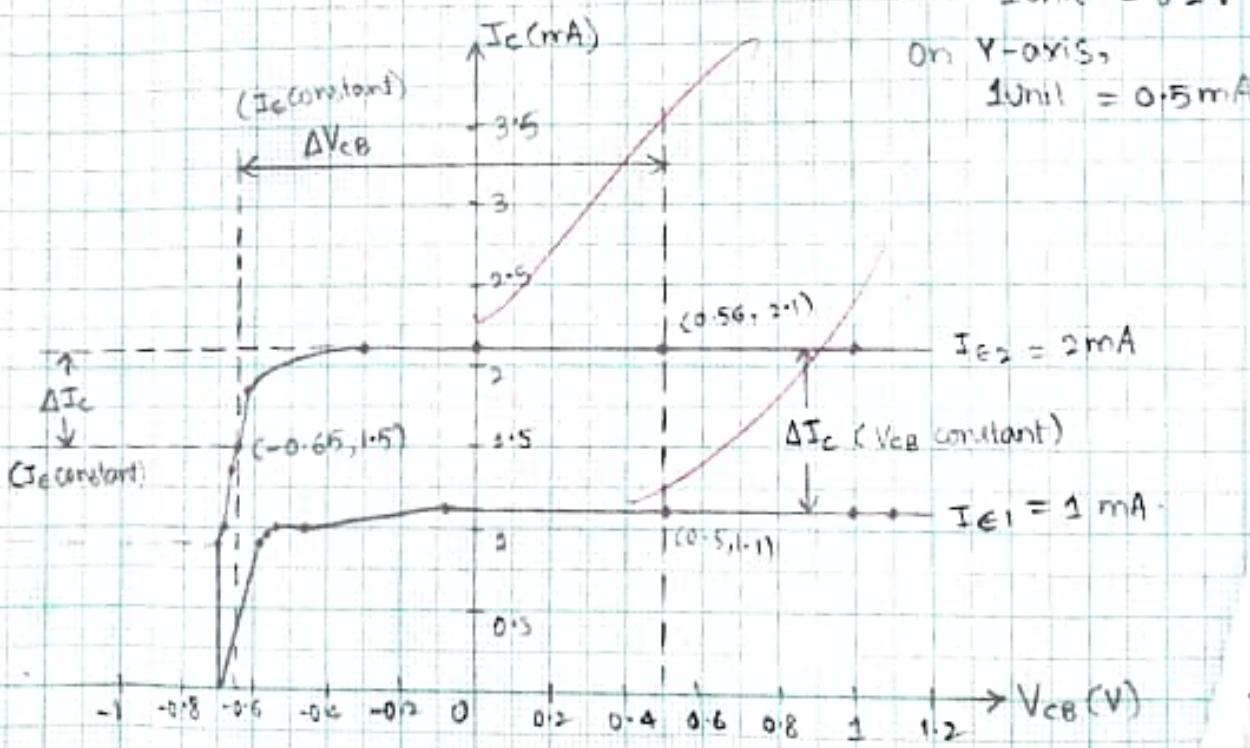


Common Base Configuration

Input Characteristics



Output Characteristics



Scale:-

on X-axis:

1 unit = 0.1V

on Y-axis:

1 unit = 0.5 mA

Scale:-

on X-axis,

1 unit = 0.2V

on Y-axis,

1 unit = 0.5 mA

Input and Output Characteristics of Transistor

CE Configuration

Aim:- To plot the Input and output characteristics of a transistor connected in common Emitter configuration and to find the h-parameters from the characteristics.

Apparatus Required :-

Sl.No.	Description	Range / Value	Quantity
1.	Regulated Power Supply	(0-30)V	1
2.	Transistor	BC107	1
3.	DC Ammeter	(0-200)mA, (0-200)μA	1 of each
4.	DC Voltmeter	(0-20)V	2
5.	Resistor	100 kΩ, 1 kΩ	1 of each.
6.	Bread board and connecting wires		

Theory :-

A transistor is a three terminal active device. The terminals are emitter, base, collector. In CE configuration, the emitter is common to both input (base) and output (collector). For normal operation, the E-B junction is forward biased and C-B junction is reverse biased.

The output characteristics resemble that of a forward biased diode curve due to the Base-Emitter junction of the transistor is forward biased. The input resistance of CE

circuit is higher than that of CB circuit.

The Output Characteristics are drawn between I_C and V_{CE} at constant I_B . The collector current varies with V_{CE} in to few volts only. After this the collector current becomes almost constant, and independent of V_{CE} . The value of V_{CE} up to which the collector current changes with V_{CE} is known as Knee Voltage. The transistor always operated in the region above knee voltage, I_C is always constant and is approximately equal to I_B . The other salient feature of this configuration is that the input and output are out of phase.

Procedure :-

a) Input Characteristics :

1. Connections are made as per the circuit diagram.
2. Keep V_{BB} and V_{CC} Knob in minimum position before turn on the regulated power supply.
3. Set $V_{CE} = 1V$ by varying V_{CE} .
4. Then vary the V_{BB} smoothly with fine control and note down the value of base current I_B & base emitter voltage V_{BE} and put it in tabular form.
5. Repeat the experiment by keeping $V_{CE} = 2V \& 4V$.
6. Draw the graph between V_{CE} Vs I_B against V_{CE} equal to constant.

B) Output Characteristics

1. Connections are made as per the circuit diagram.
2. Keep V_{BB} and V_{CC} knob in minimum position before turn on the regulated power supply.
3. Set $I_B = 10 \mu A$ by varying V_{BB} .
4. Vary the V_{CC} smoothly with fine control and note down the value of collector current I_C & Emitter collector voltage V_{CE} and put it in tabular form.
5. Repeat the experiment by keeping $I_E = 20 \mu A$ and $30 \mu A$
6. Draw a graph between V_{CE} Vs I_C against $I_B = \text{constant}$.

To find the h-parameters :

Calculation of h_{ie} :

Mark two points on the Input characteristics for constant V_{CE} .
Let the coordinates of these 2 points be (V_{BE1}, I_{B1}) & (V_{BE2}, I_{B2})

$$h_{ie} = \frac{V_{BE2} - V_{BE1}}{I_{B2} - I_{B1}}$$

Calculation of h_{re} :

Draw the horizontal line at some constant I_B value on the input characteristics. Find V_{CE2} , V_{CE1} & V_{BE2} , V_{BE1}

$$h_{re} = \frac{V_{CE2} - V_{BE1}}{V_{CE2} - V_{CE1}}$$

Calculation of h_{fe} :

Draw a vertical line on the output characteristics at some constant V_{CE} value. Find I_C2 , I_C1 & I_{B2} , I_{B1}

$$h_{fe} = \frac{I_{C2} - I_{C1}}{I_{B2} - I_{B1}}$$

calculation of h_{fe} :-

Mark two points on the Input Characteristics for constant I_B .
 let the coordinates of these two points be (V_{ce2}, I_{c2}) and (V_{ce1}, I_{c1})

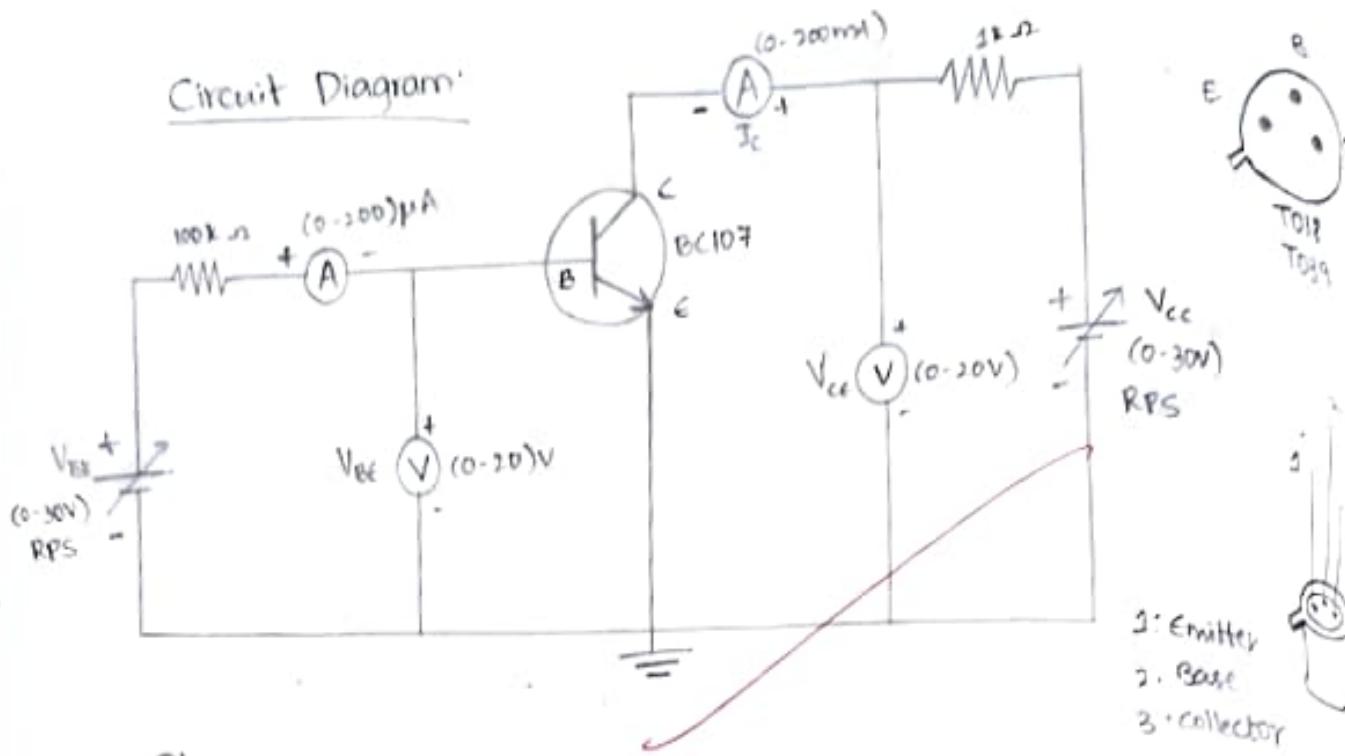
$$h_{fe} = \frac{I_{C2} - I_{C1}}{V_{ce2} - V_{ce1}}$$

Result :-

The Input and Output Characteristics of transistor in CE configuration are drawn on the graphs and the h-parameters are calculated: $h_{ie} = 1.3k\Omega$ $h_{re} = -0.01$
 ~~$h_{oe} = 1m\Omega$~~ $h_{fe} = 150$

ESt/12/2021

Circuit Diagram:



Observation :-

A) Input Characteristics

S.No.	RPS $V_{CE}(\text{V})$	$V_{CE} = 2\text{V}$		$V_{CE} = 4\text{V}$	
		$V_{BE}(\text{V})$	$I_B(\mu\text{A})$	$V_{BE}(\text{V})$	$I_B(\mu\text{A})$
1.	0.3	0.30	1.3	0.32	1.4
2.	0.5	0.4	2.0	0.41	2.3
3.	1.0	0.58	5.9	0.57	6.2
4.	1.5	0.62	10.3	0.62	10.2
5.	1.8	0.64	13.6	0.63	13.1
6.	2.0	0.64	15.3	0.64	15.5
7.	2.2	0.65	17.2	0.64	17.6
8.	2.5	0.65	20.2	0.64	20.0
9.	2.8	0.66	23.6	0.64	22.9
10.	3.0	0.66	25.7	0.65	25.3
11.	3.5	0.67	30.7	0.66	30.8
12.	4.0	0.67	35.1	0.66	35.7

1: Emitter
2: Base
3: Collector

B) Output Characteristics

s.No	PDS	$I_E = 10 \mu A$		$I_E = 20 \mu A$	
		$V_{CE}(V)$	$I_C(mA)$	$V_{CE}(V)$	$I_C(mA)$
1	0.3	0.173	0.3	0.05	0.4
2	0.5	0.183	0.6	0.06	0.6
3	1	0.25	1.0	0.08	1.1
4	1.5	0.36	1.4	0.12	1.7
5	1.8	0.47	1.6	0.13	2.0
6	2	0.56	1.8	0.15	2.3
7	2.2	0.62	2.1	0.18	2.5
8	2.5	0.80	2.4	0.19	2.8
9	2.8	1	2.7	0.20	3.1
10	3	1.12	2.9	0.22	3.3
11	3.2	1.3	2.2	0.23	3.5
12	3.5	1.56	2.2	0.28	3.6
13	4	1.96	2.2	0.34	4.3

Calculations :-

$$h_{ie} = \frac{0.63 - 0.65}{(35.1 - 20.2) \times 10^6} = 134.2 \times 10^{-6} = 134.2 \mu A/V$$

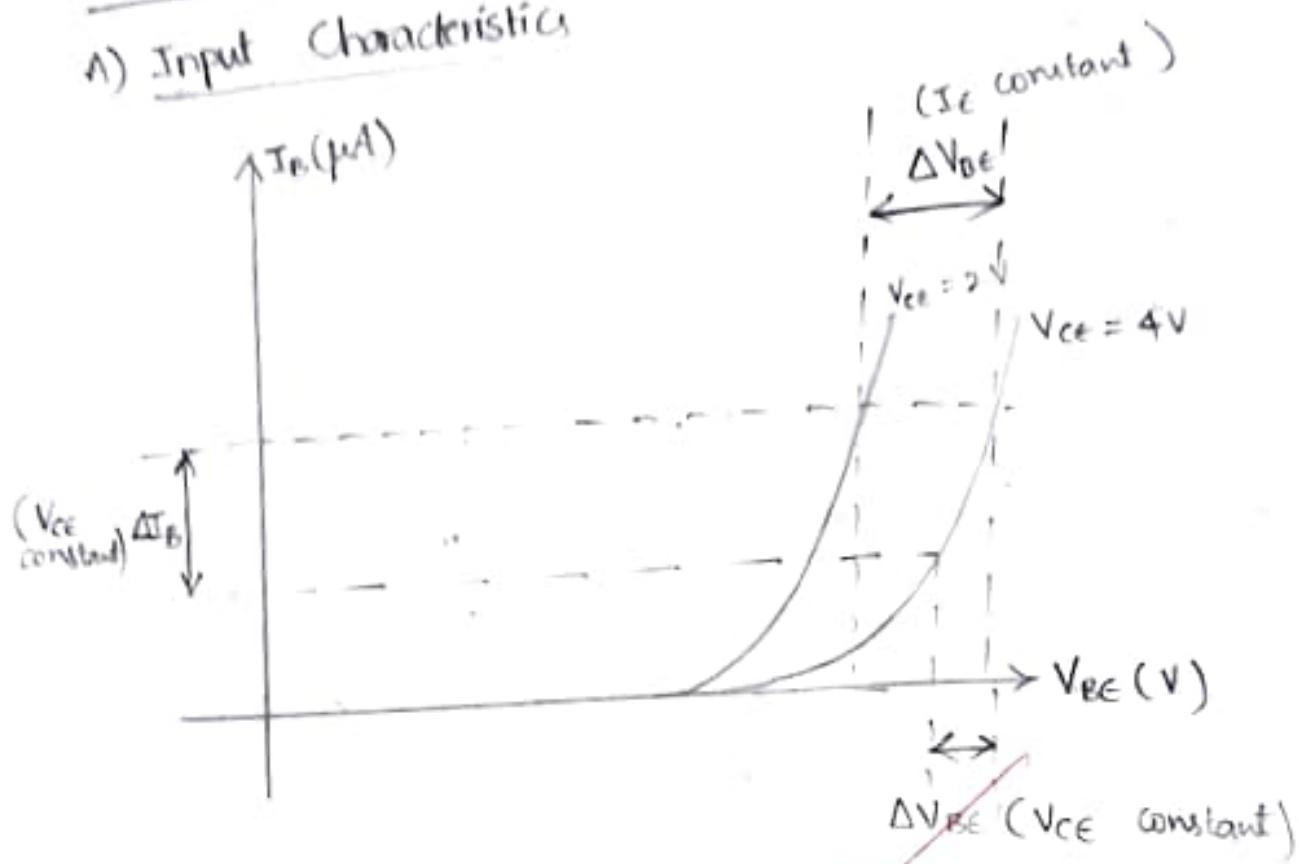
$$h_{re} = \frac{0.65 - 0.64}{1-2} = -0.01$$

$$h_{fe} = \frac{3.6 - 2.1}{20 - 10} \times \frac{10^3}{10^6} = 150$$

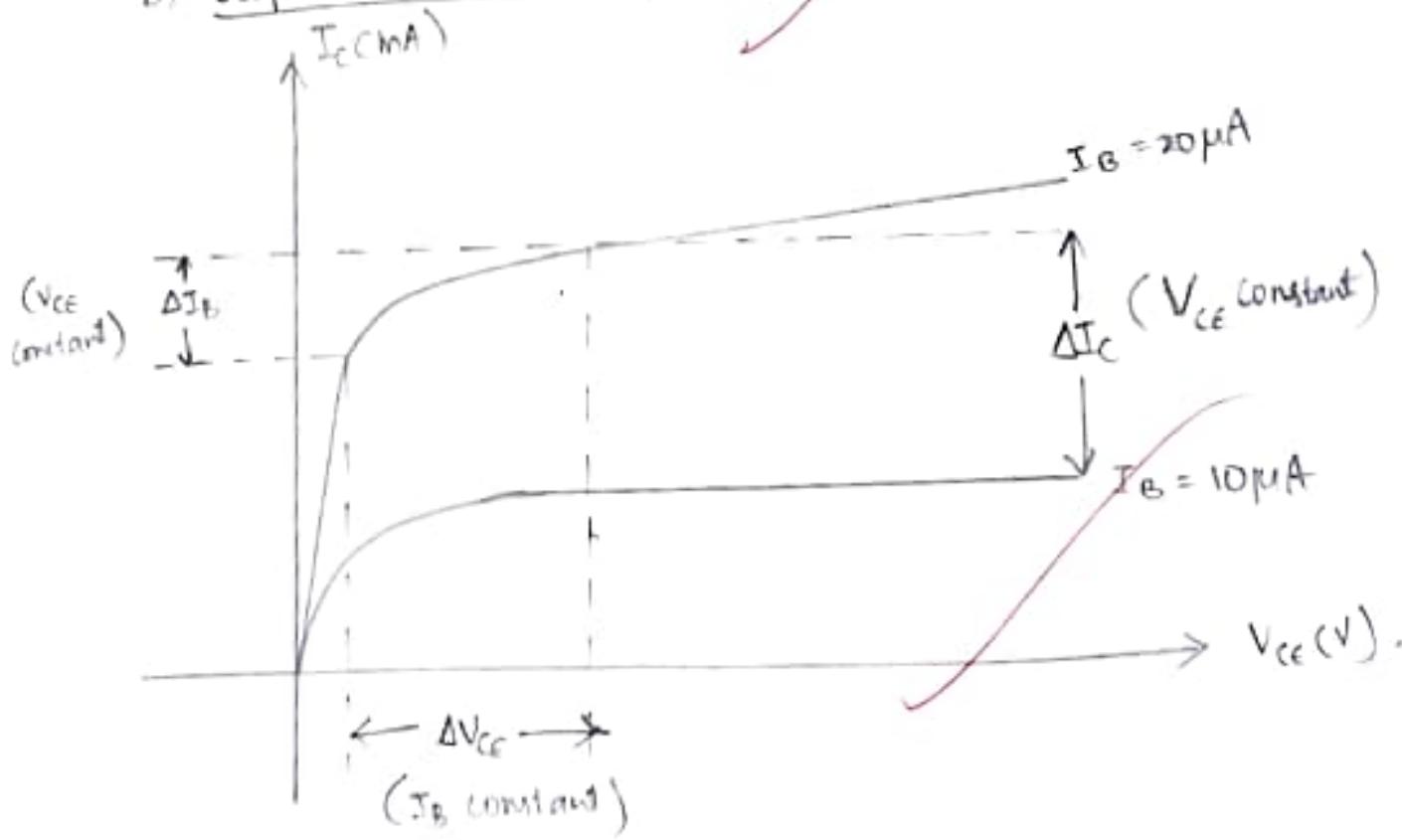
$$h_{oe} = \frac{(3.6 - 3.1) \times 10^3}{0.25 - 0.2} = 10 \times 10^3 = 10 m\Omega$$

Model Graph :-

A) Input Characteristics

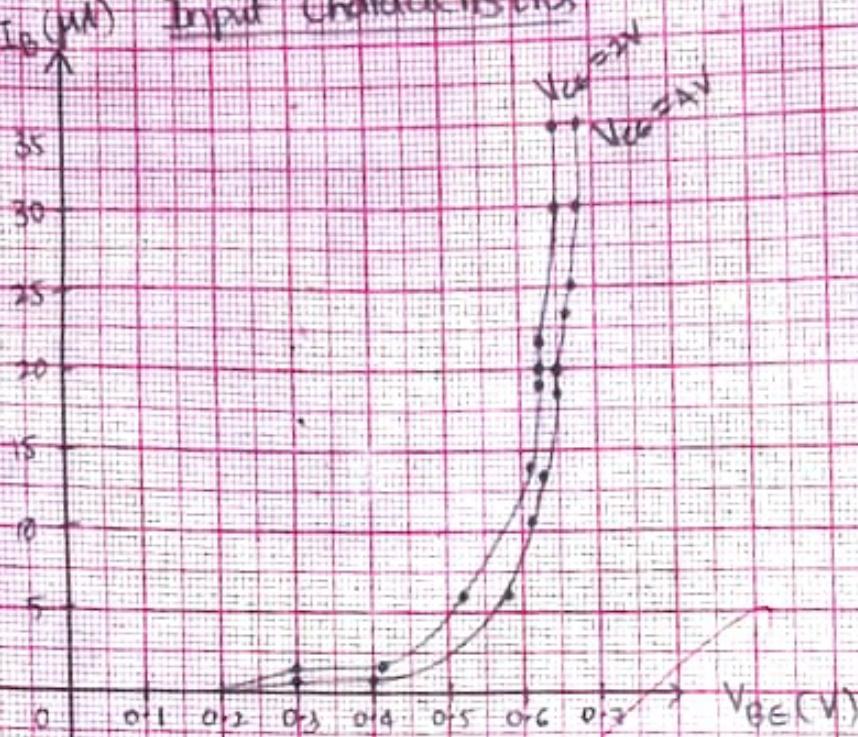


B) Output Characteristics



CE Configuration

Input Characteristics

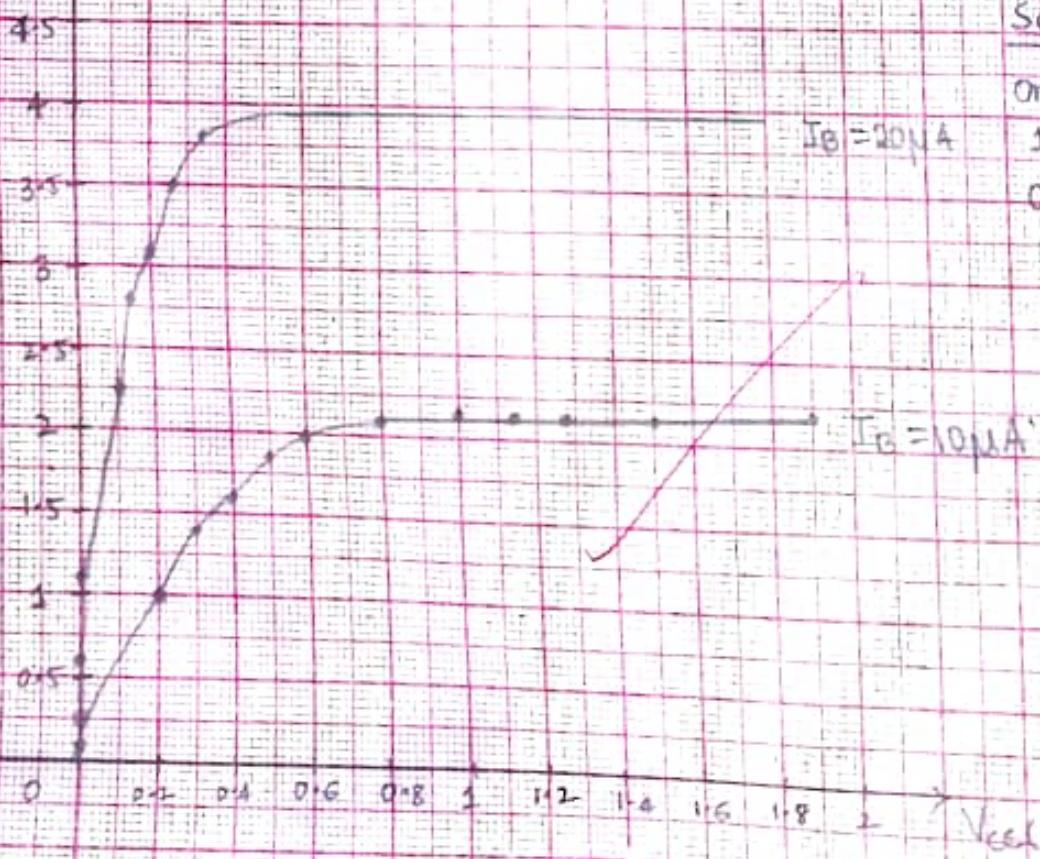


Scale:

On X-axis,
1 Unit = 0.1 V
on Y-axis,
1 Unit = 5 μA

$I_C (mA)$

Output Characteristics



Scale:

On X-axis,
1 Unit = 0.2 V
On Y-axis,
1 Unit = 0.5 mA

Clippers

Aim :- To construct and test the shunt and series clipping circuits and also verify the transfer characteristics of it.

Apparatus Required :-

Sl.No	Description	Range/Number/Value	Quantity
1.	Regulated Power Supply	(0-30)V	1
2.	Diode	1N4007	1
3.	Resistors	2.2K Ω	1
4.	Signal Generators	(0-1 MHz)	1
5.	Dual Trace CRO	20 MHz	1
6.	Breadboard and connecting Wires		

Theory :

clipping Circuits basically limit the amplitude of the input signal either below or above certain voltage level . They are referred to as voltage limiters , Amplitude selectors or slicers . A clipping circuits is one, in which a small section of input waveform is missing or cut or truncated at the output section .

Clipping circuits are constructed using a combination of resistors , diodes or transistor and reference voltage . Clipping circuits are classified based on the position of diode as

- i. Series diode Clipper
- ii. Shunt diode Clipper .

Procedure :-

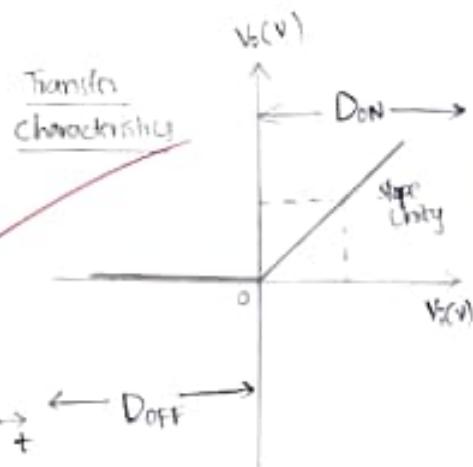
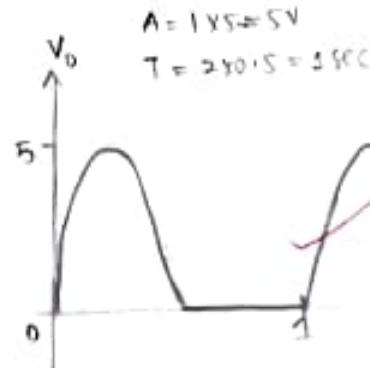
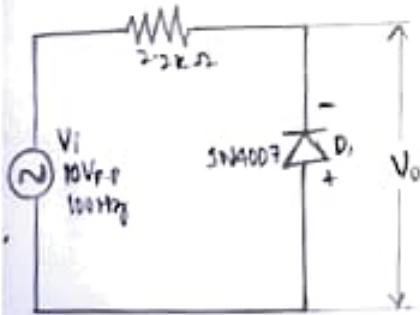
1. Connect the circuit as shown in the figures
2. In each case, apply $10 \text{ V}_{\text{p-p}}$, 1 KHz sine wave as Input using a signal generator.
3. Observe the Input and Output waveforms on CRO and plot the waveforms and mark the values without and with reference voltage V_c .
4. Obtain the transfer characteristics of clipper circuit, by keeping CRO in X-Y mode.
5. Repeat the above steps for all the clipping circuits.

Procedure for Transfer Characteristics:-

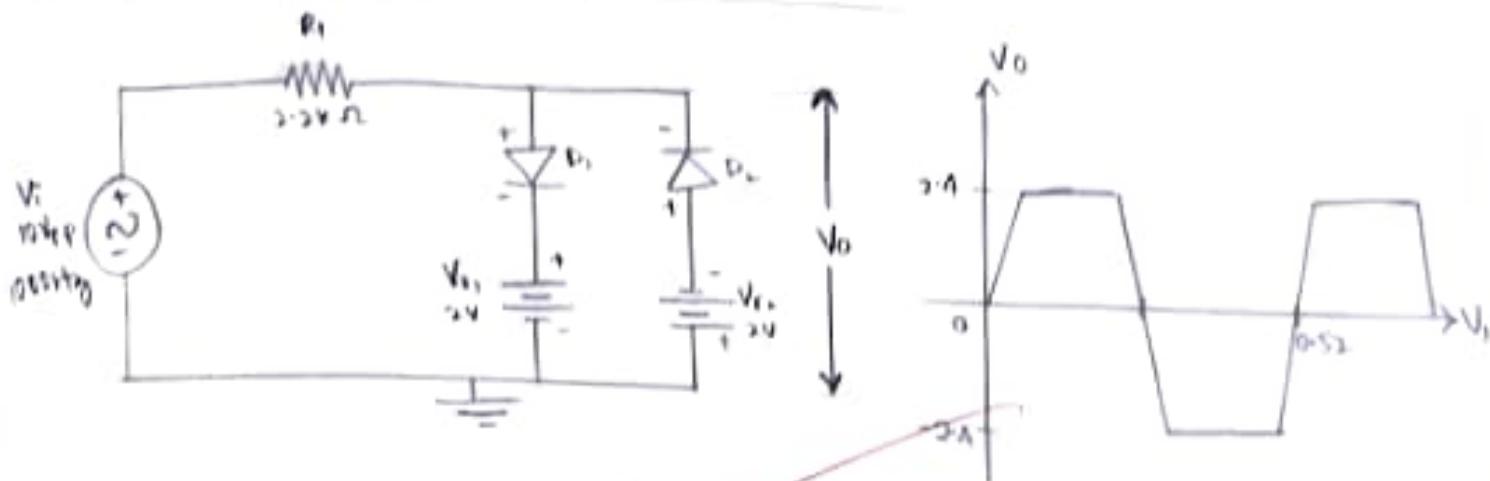
- To find the transfer characteristics, apply input to the X-channel, output to Y-channel, adjust the dot at the center of the screen when CRO is in X-Y mode. Both the channels must be in ground, then remove ground and plot the transfer characteristics.
- Set the CRO output channel in DC mode always.

B) shunt clipper:

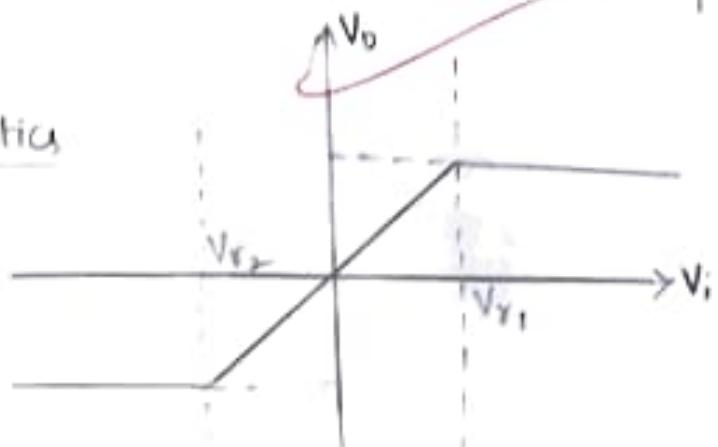
a) shunt Negative clipper:



e) Clipping at two independent levels:



Transfer characteristics



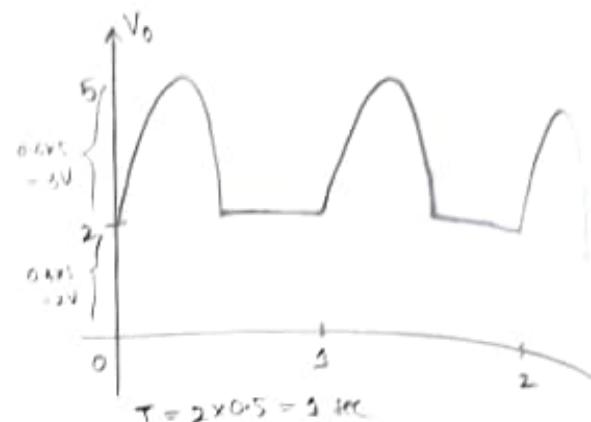
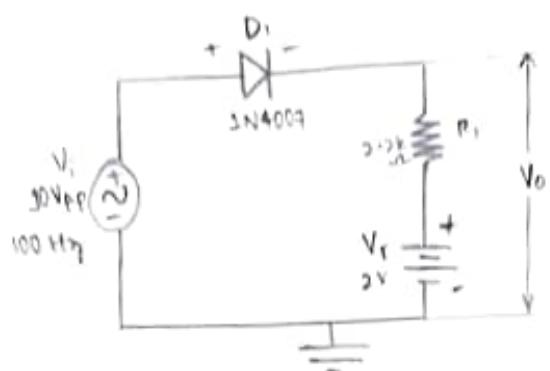
Result :- Different types of shunt and series clipping circuits are constructed and tested and also verified the transfer characteristics of it

Q8/12/2019

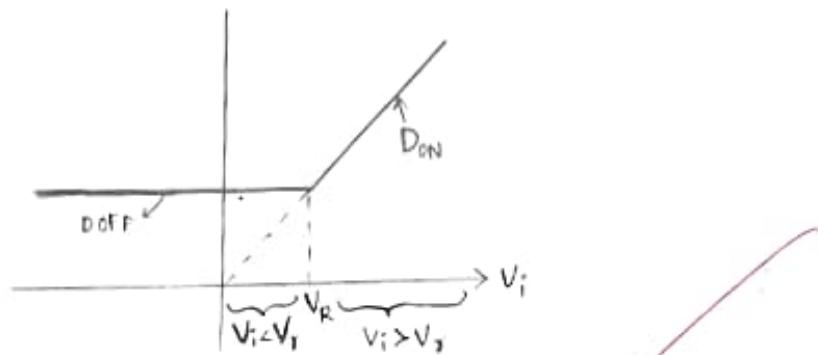
Circuit Diagram

A) Series Clipper

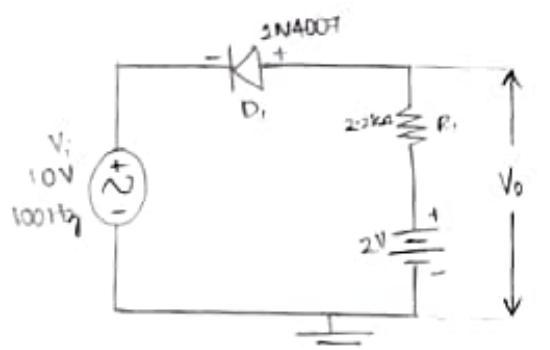
c) Series Negative Clipper with Positive Reference



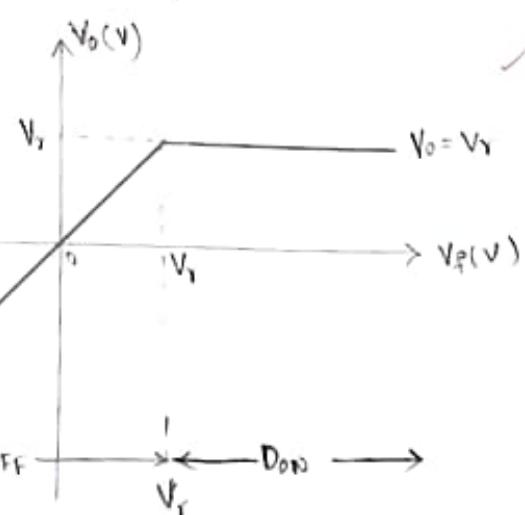
Transfer Characteristic



d) Series Positive Clipper with Positive Reference



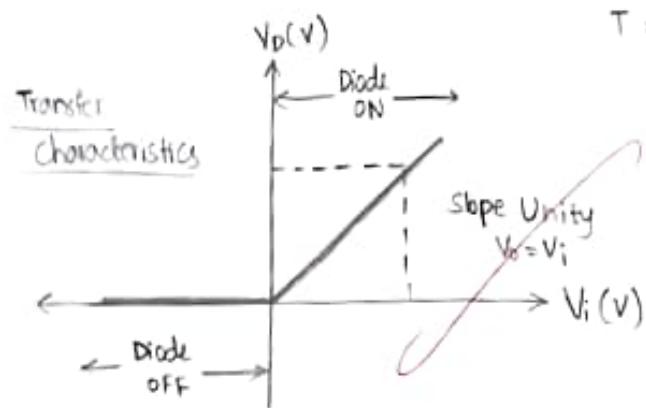
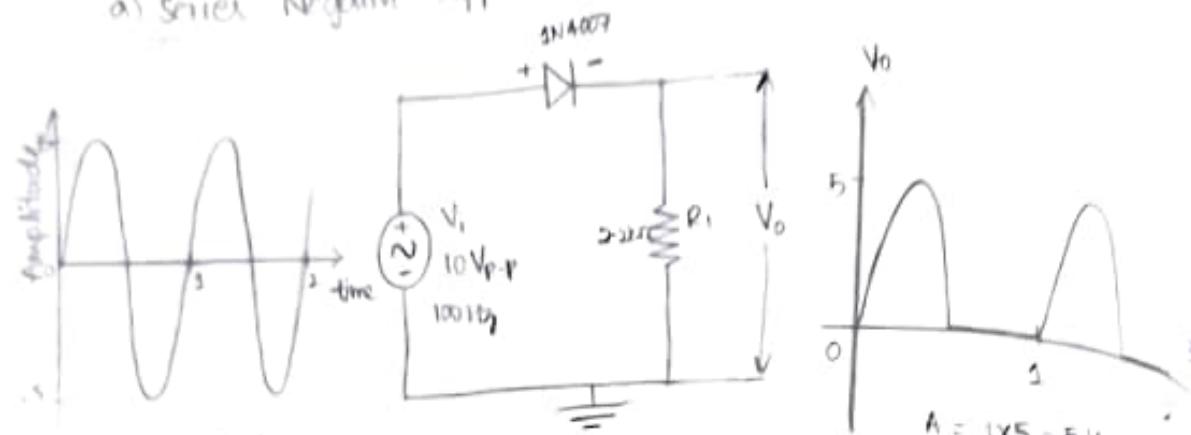
$T = 2 \times 0.5 = 1 \text{ sec}$



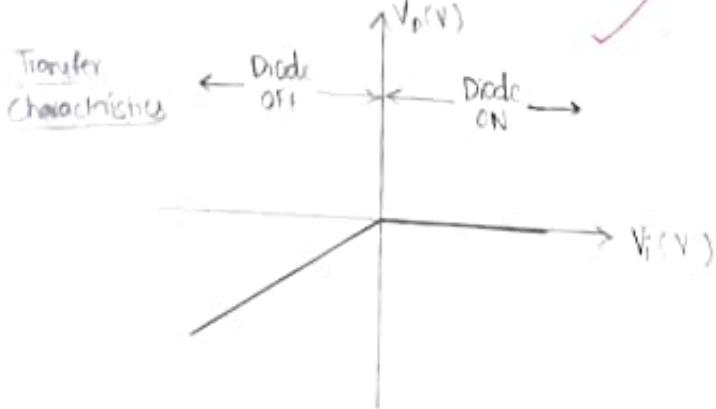
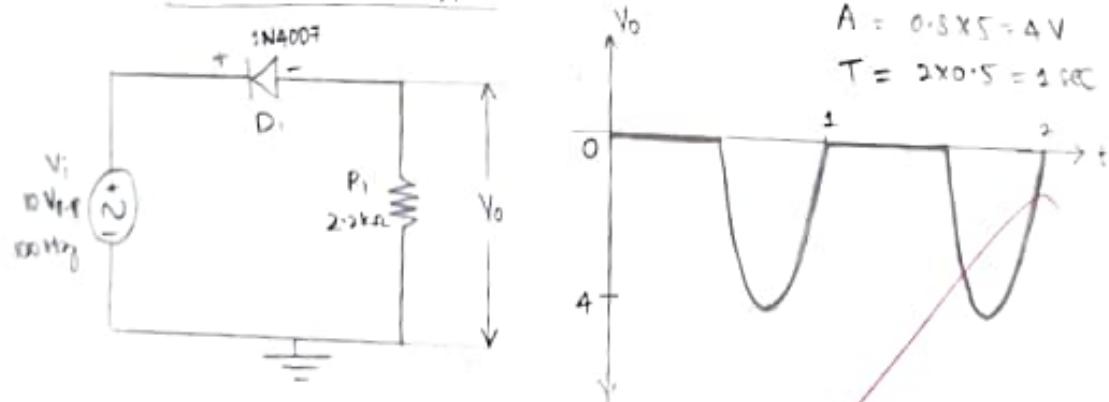
Circuit Diagram:

A) Series Clipper

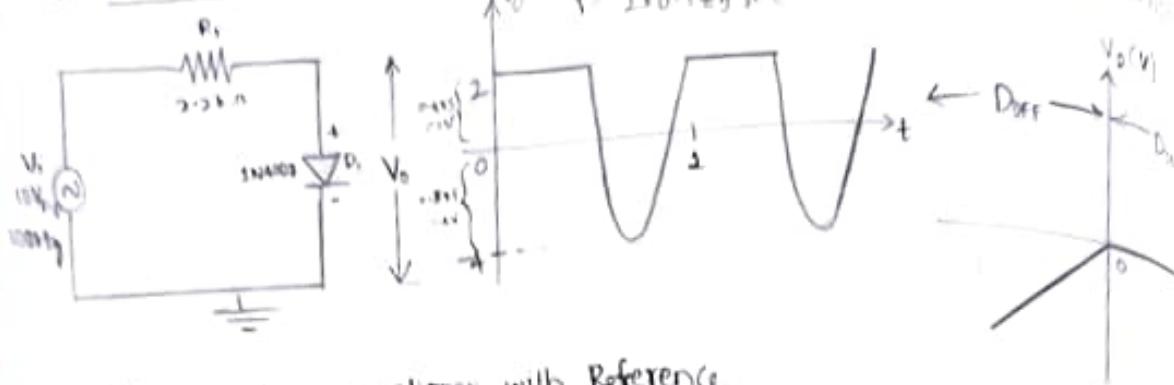
a) Series Negative Clipper



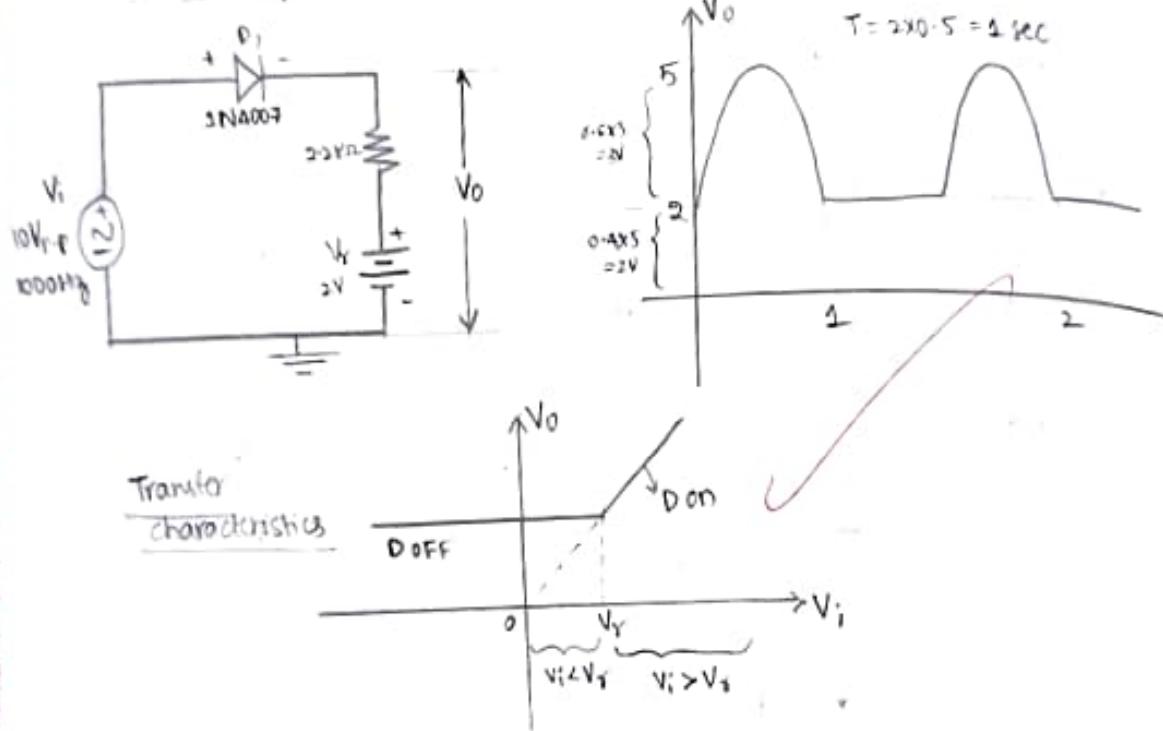
b) Series Positive Clipper



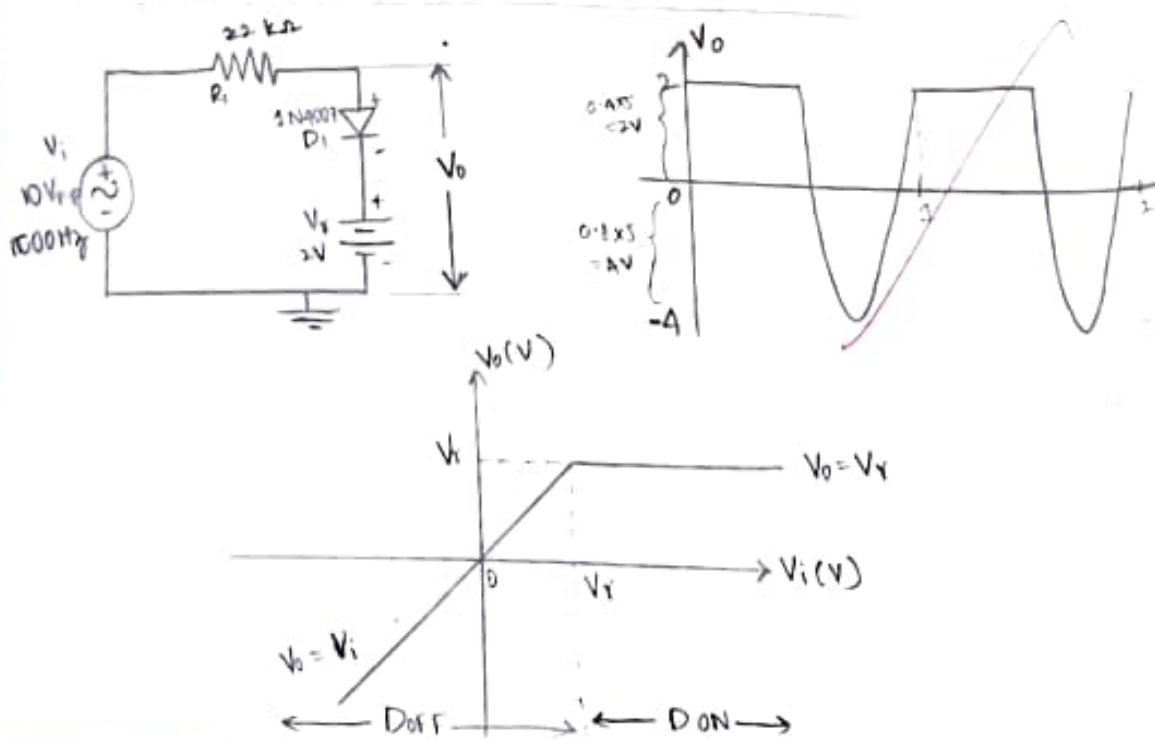
b) Shunt Positive Clipper



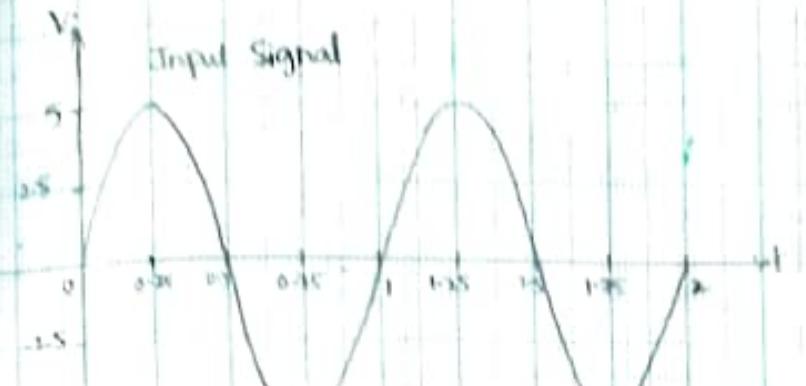
c) Shunt Negative clipper with Reference



d) Shunt Positive Clipper with Positive reference



Series Clipper



Scale :-

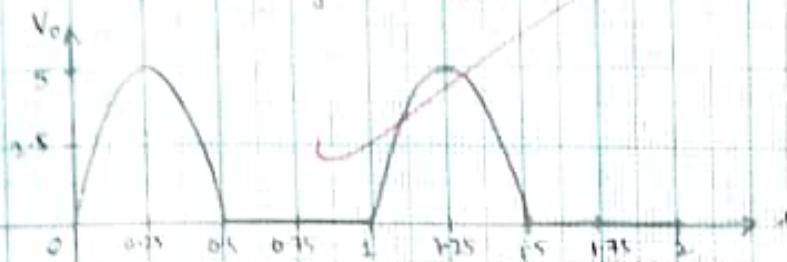
On X-axis;

1 Unit = 0.25 sec

On Y-axis,

1 Unit = 2.5 V

Series Negative Clipper



scale :-

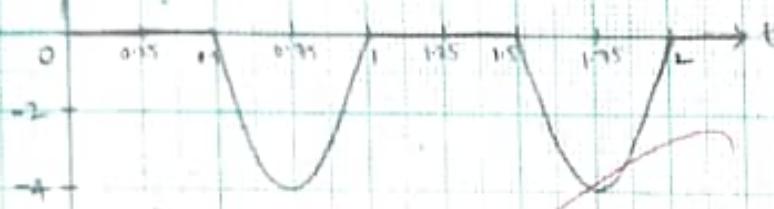
On X-axis,

1 Unit = 0.25 sec

On Y-axis,

1 Unit = 2.5 V

Series positive Clipper



scale :-

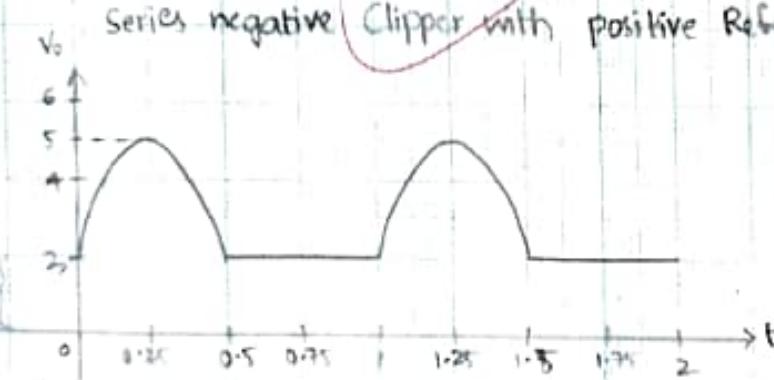
On X-axis,

1 Unit = 0.25 sec

On Y-axis,

1 Unit = 2 V

Series negative Clipper with positive Reference



scale :-

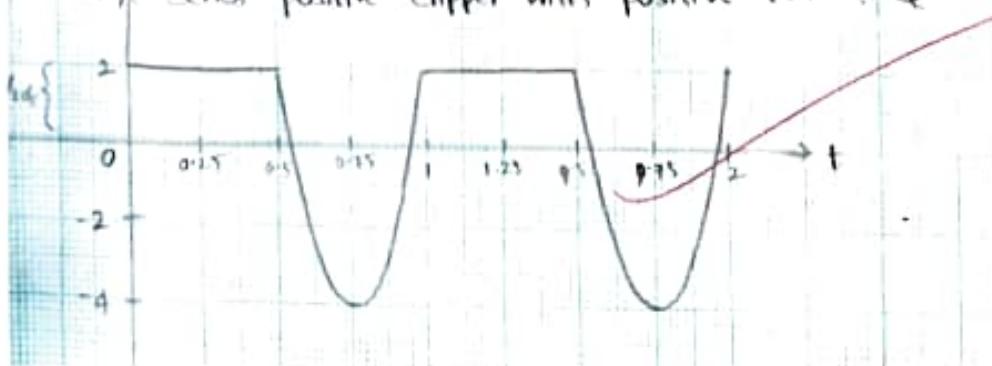
On X-axis,

1 Unit = 0.25 sec

On Y-axis,

1 Unit = 2 V

Series positive clipper with positive reference



scale :-

On X-axis,

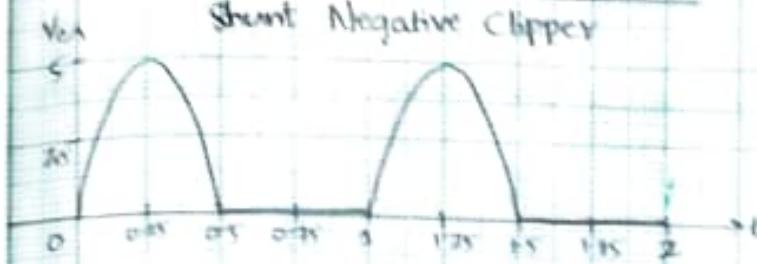
1 Unit = 0.25 sec

On Y-axis,

1 Unit = 2 V

SHUNT CLIPPER

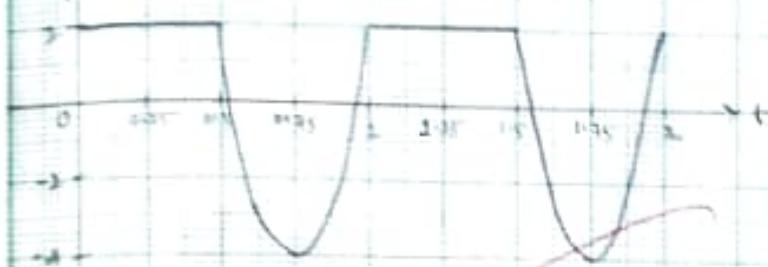
Shunt Negative clipper



Scale :-

On X-axis,
1 unit = 0.25 sec.
On Y-axis,
1 unit = 2.5 V

Shunt Positive Clipper



Scale :-

On X-axis,
1 unit = 0.25 sec.
On Y-axis,
1 unit = 2 V

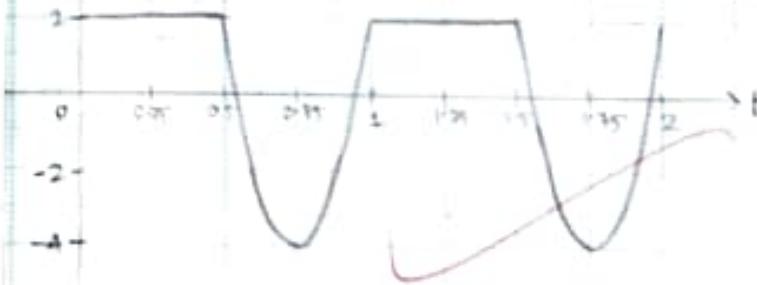
Shunt Negative Clipper with reference



Scale :-

On X-axis,
1 unit = 0.25 sec.
On Y-axis,
1 unit = 2 V

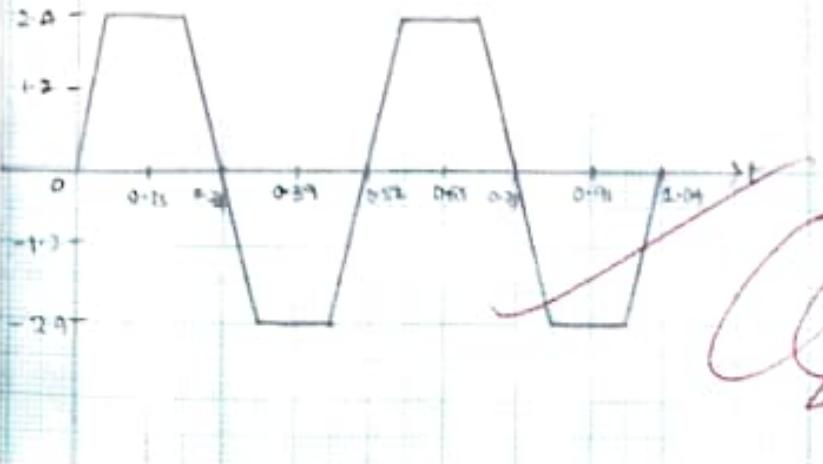
Shunt Positive clipper with positive reference



Scale :-

On X-axis,
1 unit = 0.25 sec.
On Y-axis,
1 unit = 2 V

Clipping at two independent levels



Scale :-

On X-axis,
1 unit = 0.13 sec.
On Y-axis,
1 unit = 1.2 V.

Clampers

Aim:- To construct and test the positive and negative clamping circuits

Apparatus Required :-

S.No.	Description	Range / value	Quantity
1.	Regulated Power Supply RPS	(0-30)V	1
2.	Diode	IN4007	1
3.	Resistor	100 K Ω	1
4.	Capacitor	0.1 μ F	1
5.	Signal Generator	0 - 1 MHz	1
6.	Dual Trace CRO	20 MHz	1
7.	Breadboard and Connecting wires		

Theory :-

Clamping circuits add a DC level to an AC signal.

A clumper is also referred to as DC restorer or DC inserter.

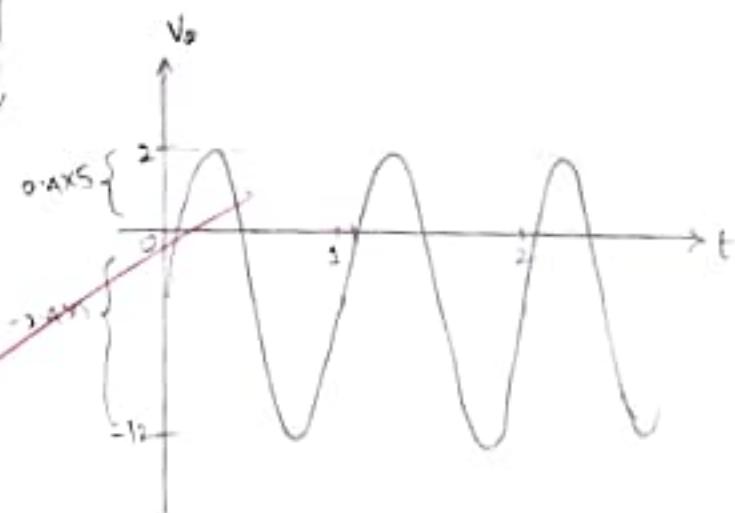
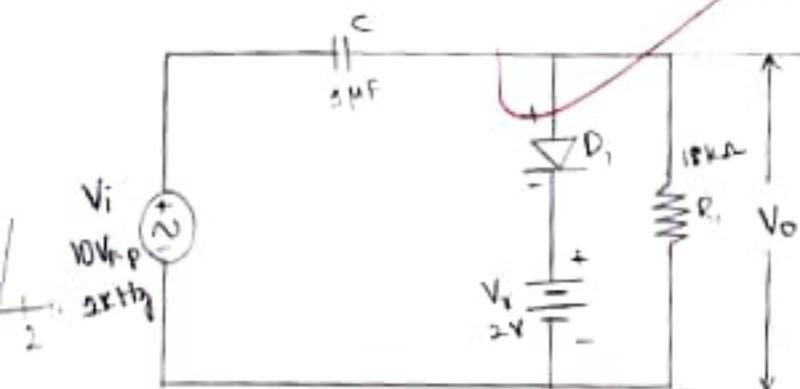
The clumper clamp the given waveform either above or below the reference level, which are known as positive or negative clumpers respectively. Clumping circuits are classified as 2 types : 1) Negative clumpers.

2) Positive clumpers

Procedure :-

1. Connect the circuit as shown in figure
2. Apply a sine wave of $10 \text{ V}_{\text{p-p}}, 1\text{KHz}$ at the input terminals with the help of signal generator.
3. Observe the Input and Output waveforms on CRO and plot the waveforms and mark the values without and with reference voltage.
4. Output is taken across the load R_L .
5. Repeat the above steps for all clamping circuits as shown in figure.

Negative clamper with positive reference voltage



Input Signal

$$\text{Amplitude} = 2 \times 5 \text{ V} = 10 \text{ V}$$

$$\text{Time period} = 2 \times 0.5 \text{ ms} = 1 \text{ ms}$$

Title :-

Date :-

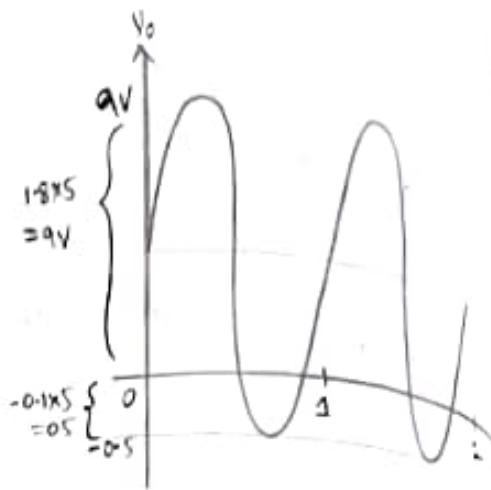
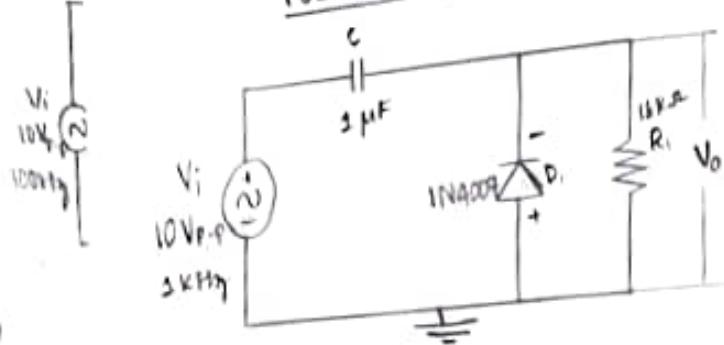
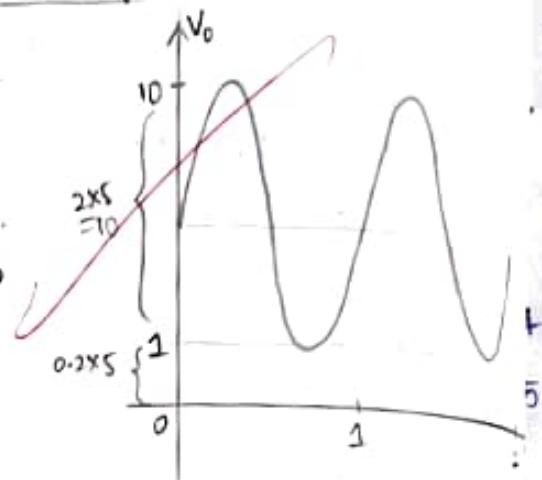
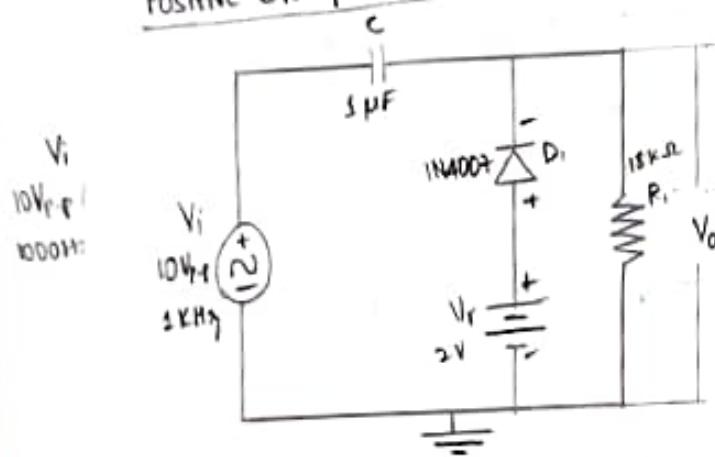
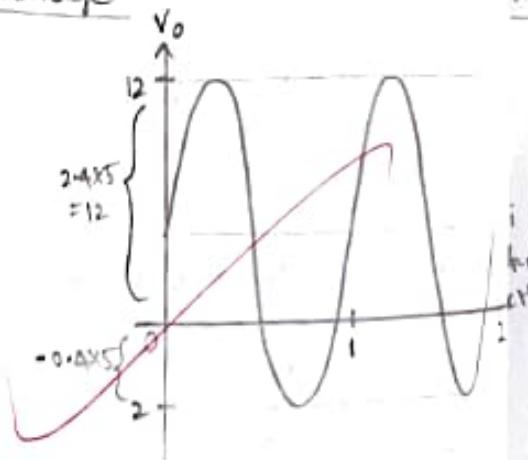
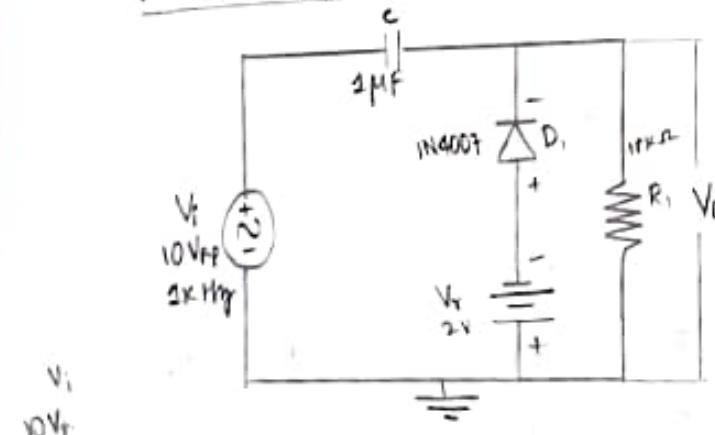
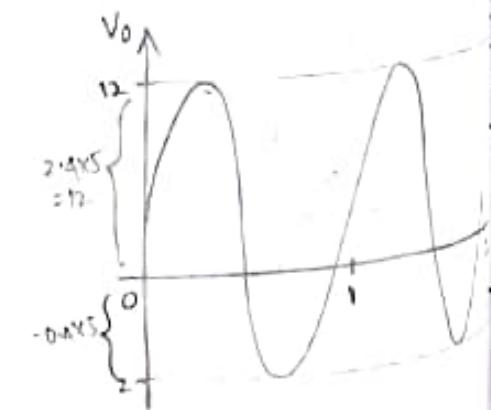
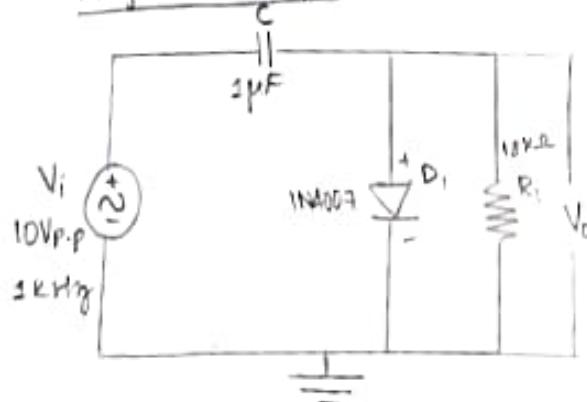
Page No.: 25

Result :-

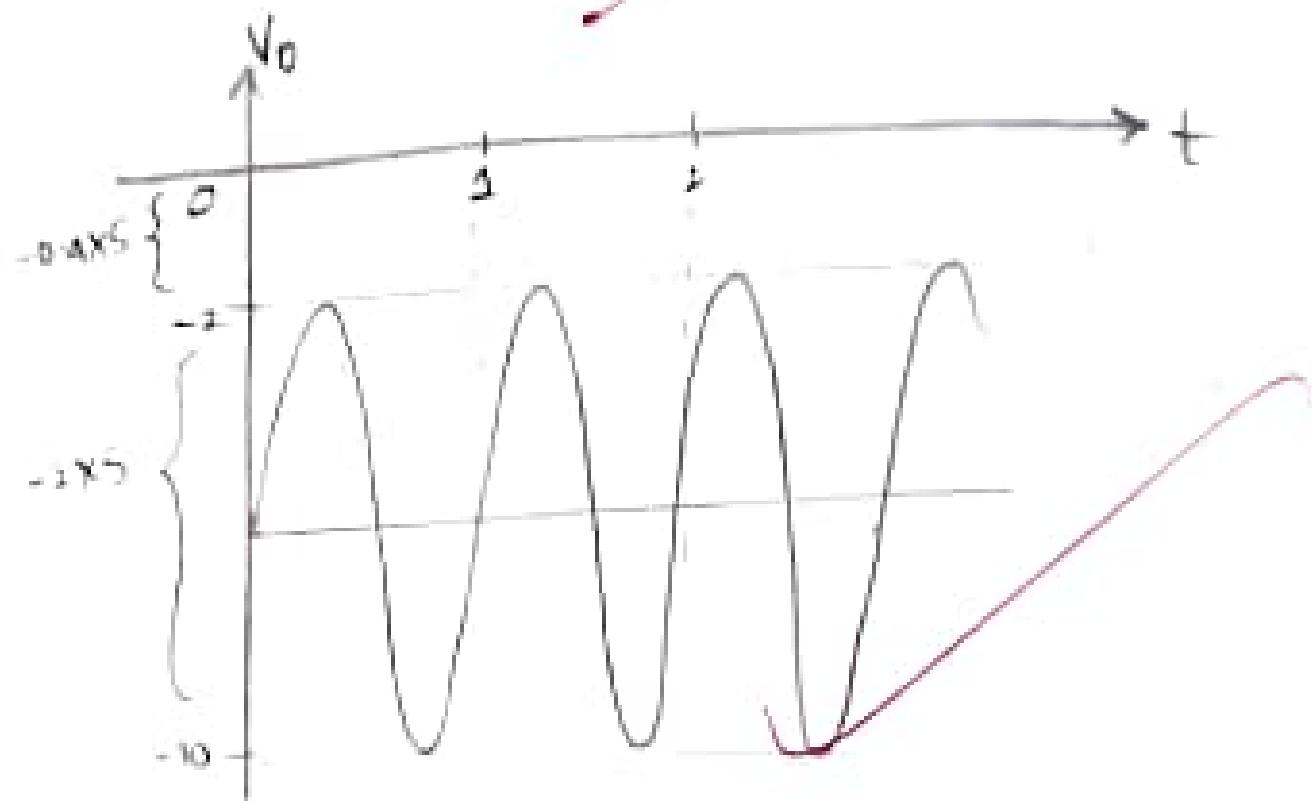
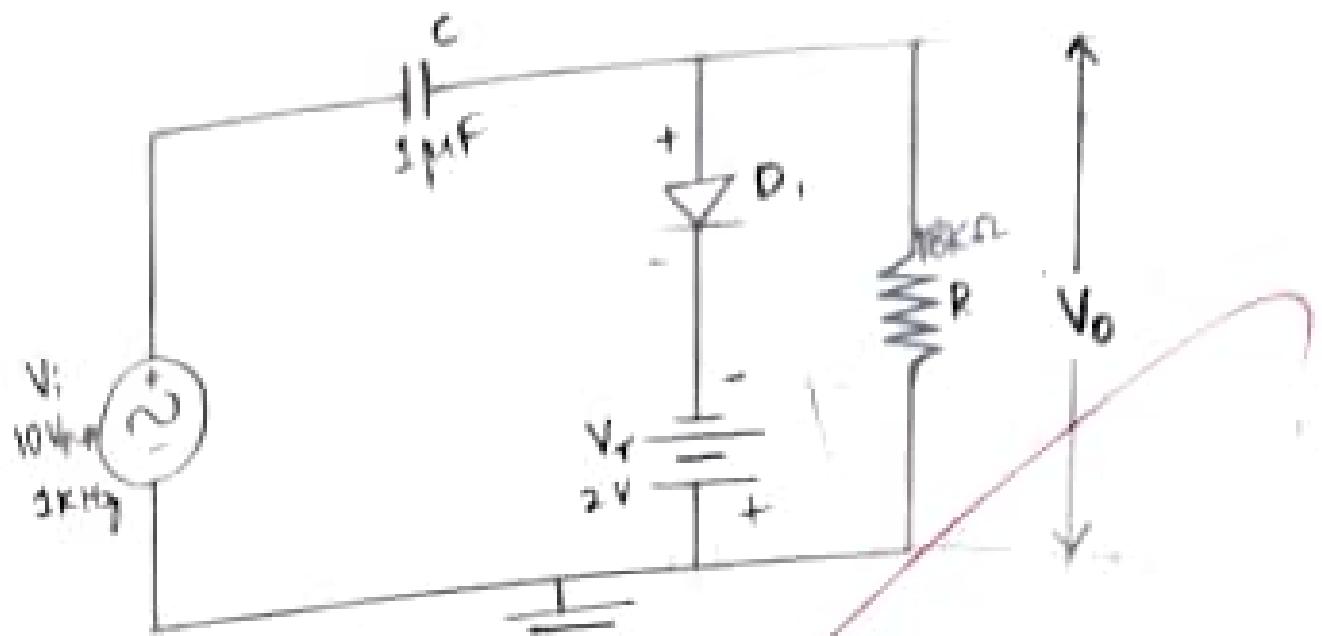
Different types of positive and negative clamper circuits are constructed and tested.

G 15/12/2021

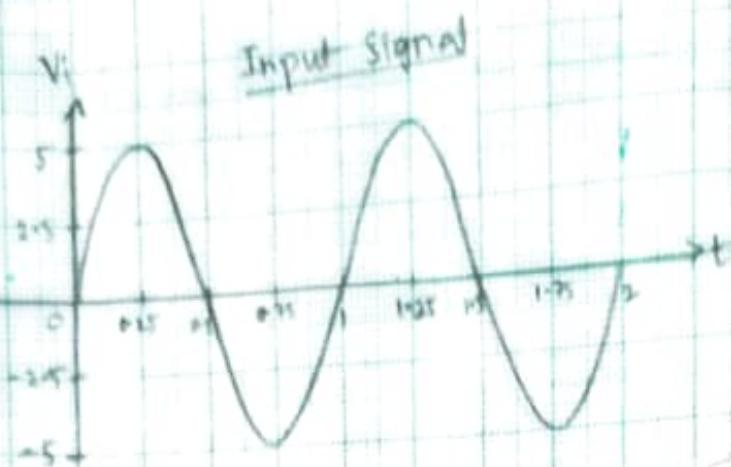
B

Circuit Diagram:-Positive ClamperPositive Clamper with Positive reference VoltagePositive Clamper with Negative reference VoltageNegative Clamper

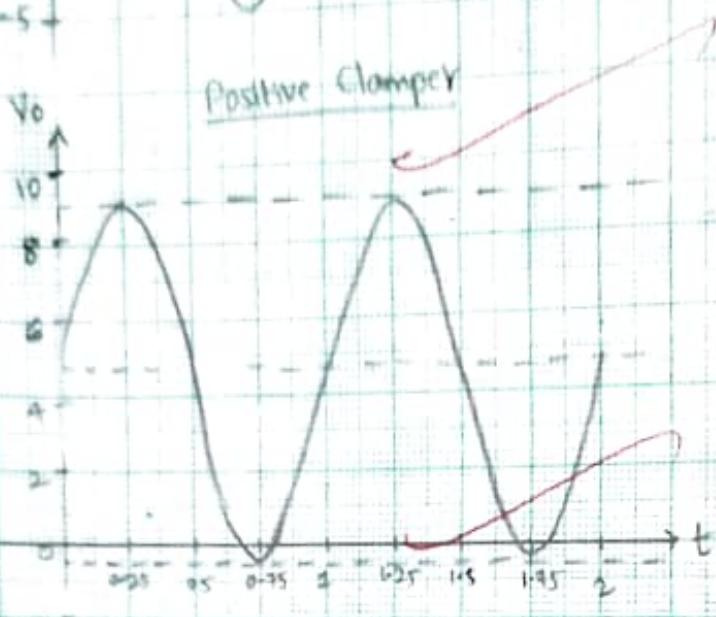
Negative clapper with negative reference voltage



CLAMPERS

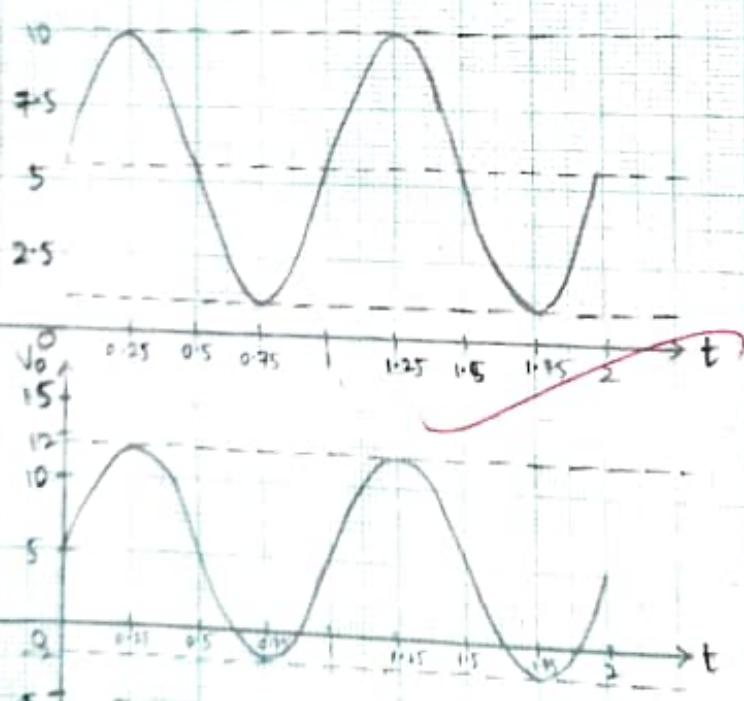


Scale :-
On X-axis,
1 unit = 0.25 sec
On Y-axis,
1 unit = 2.5 V



Scale :-
On X-axis,
1 unit = 0.25 sec
On Y-axis,
1 unit = 2 V

V_o , Positive Clamper with Positive reference Voltage

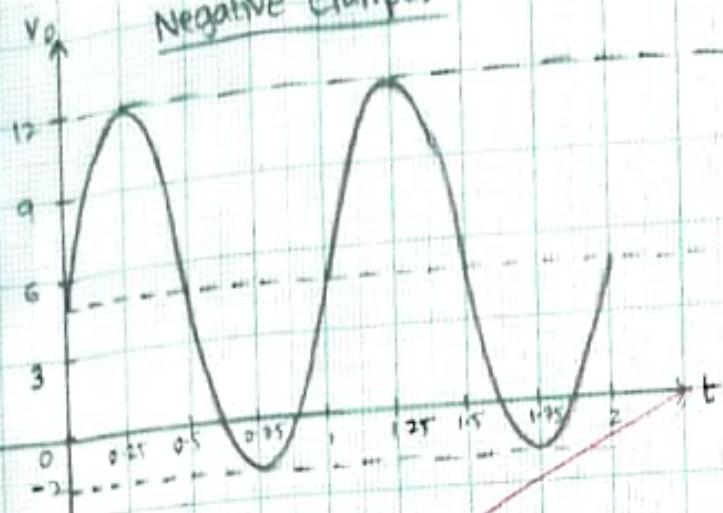


Scale :-
On X-axis,
1 unit = 0.25 sec
On Y-axis,
1 unit = 2.5 V

Positive Clamper with negative reference

Scale :-
On X-axis,
1 unit = 0.25 sec
On Y-axis,
1 unit = 5 V

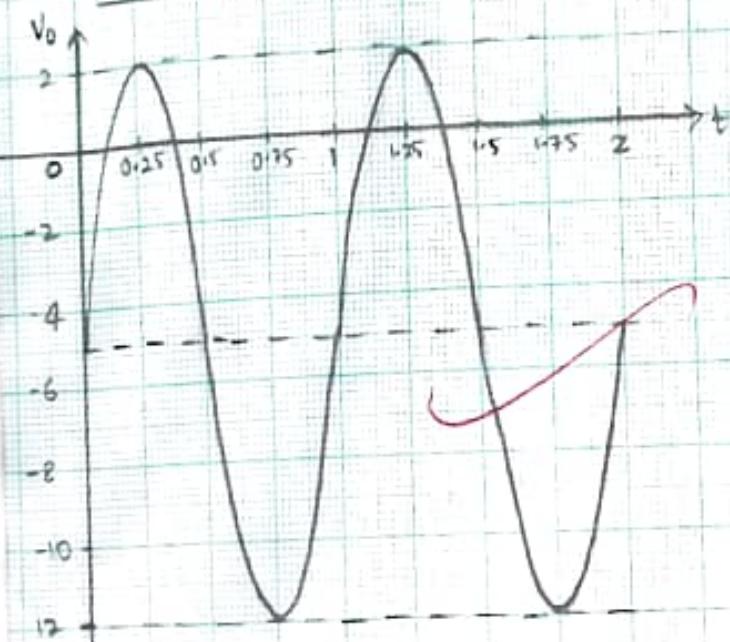
Negative Clamper



Scale:-

on x-axis,
1 unit = 0.25 sec
on y-axis,
1 unit = 3 V

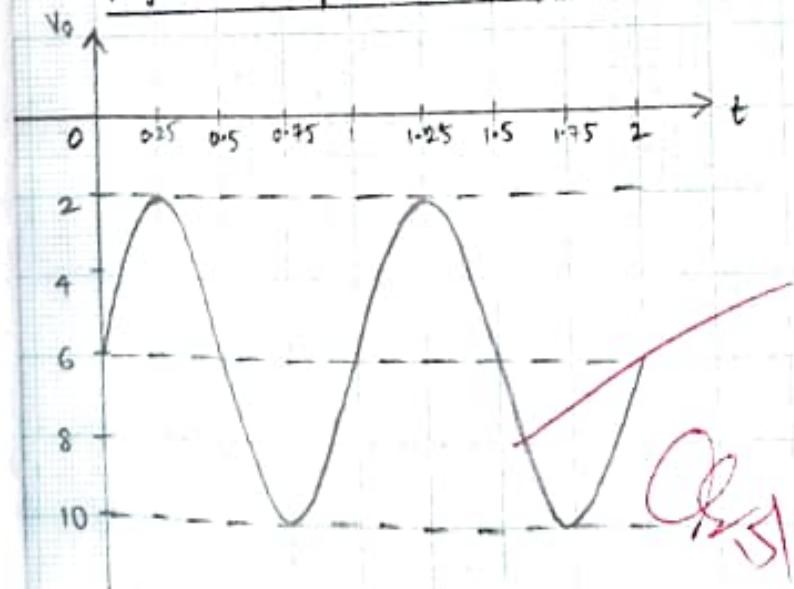
Negative Clamper with Positive reference



Scale :-

on x-axis,
1 unit = 0.25 sec
on y-axis,
1 unit = 2 V

Negative clamper with negative reference



Scale:-

on x-axis,
1 unit = 0.25 sec
on y-axis,
1 unit = 2 V

Q312/2021

Characteristics of UJT

Aim:- To obtain the V-I characteristics of UJT.

Apparatus Required :-

Sl.no.	Description	Range / Number / Value	Quantity
1.	Regulated Power Supply	(0 - 30) V	1
2.	UJT	2N2646	1
3.	DC ammeter	(0 - 200) mA	1
4.	DC Voltmeter	(0 - 20) V	2
5.	Resistor	2 k Ω	2
6.	Breadboard and connecting wires		

Theory :-

The UJT junction is a 3-terminal solid state device. The device has only one PN Junction and hence it is known as UNI-JUNCTION transistor.

When no voltage is applied between B₁ and B₂ with emitter open, the inter base resistance is given by $R_{BB} = R_{B1} + R_{B2}$.

When a voltage V_{BB} is applied between B₁ and B₂ with emitter open, voltage will divide up across R_{B1} and R_{B2} .

$$V_{RB1} = \frac{R_{B1}}{R_{B1} + R_{B2}} \times V_{BB}$$

$$\gamma = \text{the Intrinsic stand-off ratio} = \frac{V_{RB1}}{V_{BB}} = \frac{R_{B1}}{R_{B1} + R_{B2}}$$

The emitter conductivity characteristics are such that as I_E increases the emitter to base (B1) voltage decreases. At peak points to V_p and the valley point V_v , the slope of the emitter characteristics is 0.

Procedure :-

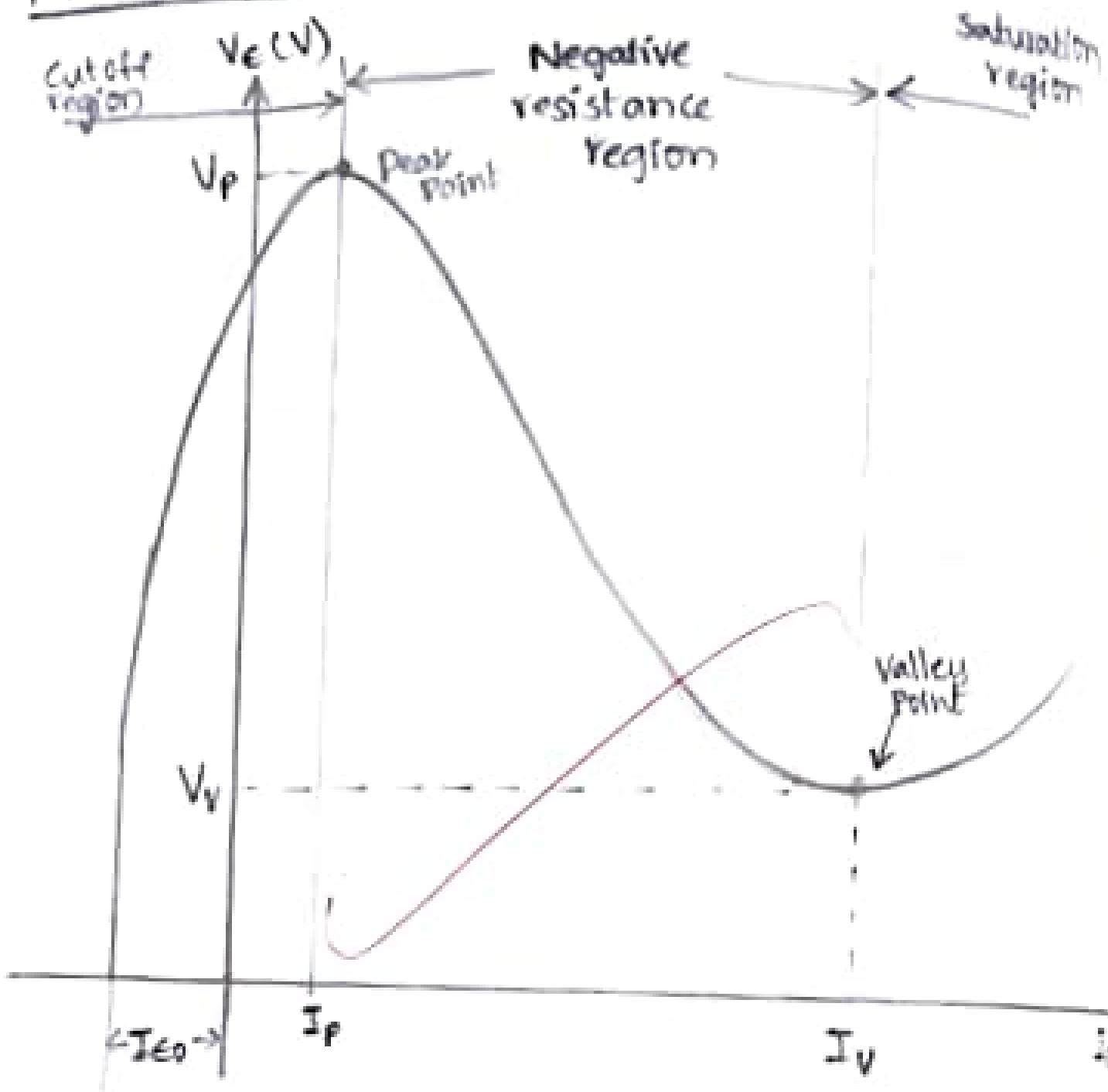
- 1) Connect the circuit as shown in the circuit diagram.
- 2) Set output voltage $V_{BB1} = 5V$ by varying V_{BB} .
- 3) Varying V_{CE} gradually, notedown both emitter current I_E and emitter voltage (V_E).
- 4) Step size is not fixed because of non-linear curve. Initially, vary V_{CE} in steps of 1V. Current I_E remains zero. As voltage is varied further, current starts increasing while voltage V_E drops. Notedown the readings V_E and I_E .
- 5) Repeat above procedure (step 3) for $V_{BB1} = 10V$.
- 6) Draw the graph between V_E Vs I_E by keeping V_{BB} constant.

Result :-

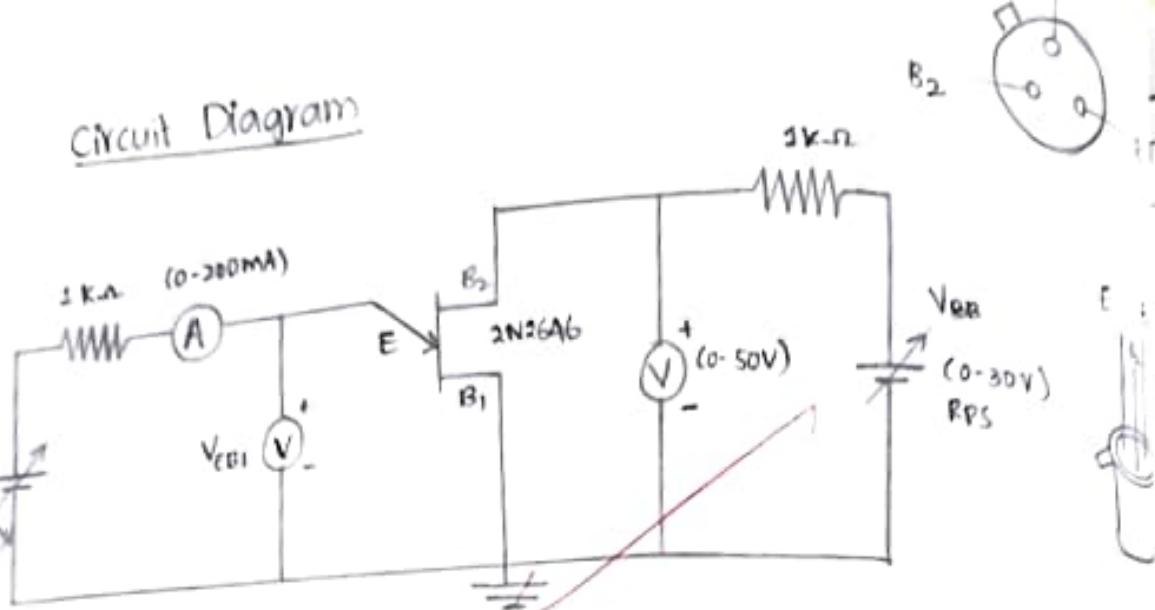
The V-I characteristics of UJT are obtained.

22/12/2017

Model Graph :-



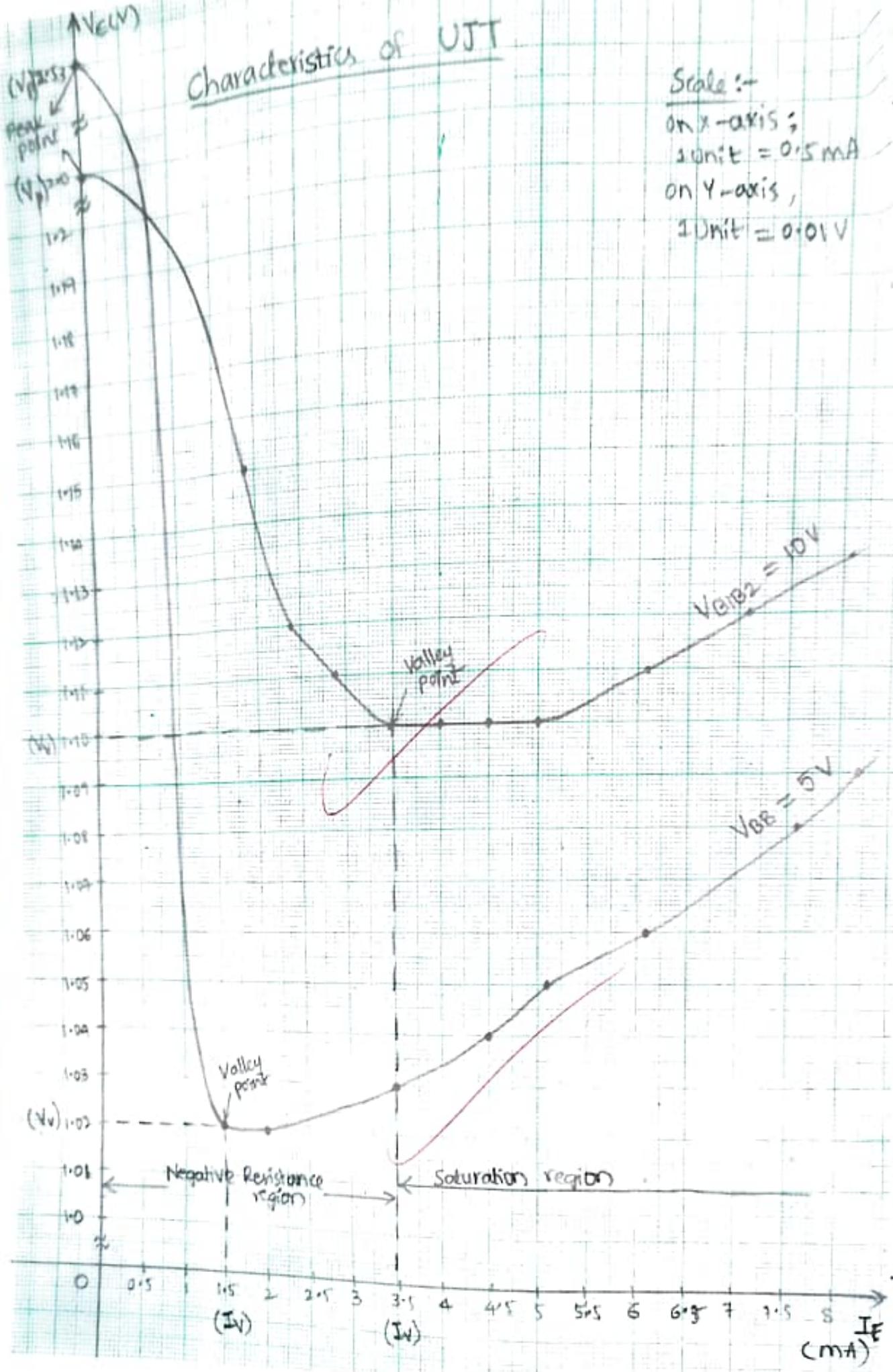
Circuit Diagram



Observations :-

RPS $V_{EE}(V)$	$V_{B1B2} = 5V$		$V_{B1B2} = 10V$	
	$V_E(V)$	$I_E(mA)$	$V_E(V)$	$I_E(mA)$
1	1.04	-0	1.04	0
1.5	1.51	0	1.51	0
2	2.07	0	2.02	0
2.5	1.02	1.5	2.53	0
3	1.02	2.0	1.15	1.9
3.5	1.02	2.5	1.12	2.4
4	1.02	3.0	1.11	2.9
4.5	1.03	3.5	1.10	3.5
5	1.04	4.1	1.10	4.0
5.5	1.04	4.5	1.10	4.5
6	1.05	5.1	1.10	5.0
6.5	1.06	5.6	1.11	5.6
7	1.06	6.1	1.11	6.1
7.5	1.07	6.6	1.12	6.6
8	1.08	7.1	1.12	7.1
8.5	1.08	7.6	1.12	7.6
9	1.09	8.2	1.13	8.1
9.5	1.09	8.7	1.13	8.6
10	1.10	9.2	1.14	9.1

Characteristics of UJT



Scale :-
 on x-axis ;
 1 unit = 0.5 mA
 on y-axis ,
 1 Unit = 0.01 V

Frequency Response of CE Amplifier

Aim:- To plot the frequency response of common Emitter amplifier and calculate its Bandwidth.

Apparatus Required :-

S.No	Description	Range/Value	Quantity
1.	Regulated Power Supply	(0-30)V	1
2.	Transistor	BC107	1
3.	Resistors	1K Ω	2
4.	Resistor	100K Ω , 33K Ω , 3.3K Ω	Each 1
5.	Capacitors	10 μ F	3
6.	Signal Generator	(0 - 1 MHz)	1
7.	Dual Trace CRO	20 MHz	1
8.	Breadboard & Connecting Wires		

Theory :-

The common emitter configuration is widely used as a basic amplifier as it has both voltage and current amplification. Resistors R_1 and R_2 form a voltage divider across the base of the transistor.

The function of this network is to provide necessary bias condition and ensure that emitter-base junction is operating in the proper region. In order to operate transistor as an amplifier,

biasing is done in such a way that the operating point is in the active region. For an amplifier the Q-point is placed so that the loadline is bisected.

The coupling and bypass capacitors cause the fall of the signal in the low frequency response of the amplifier because their impedance becomes large at low frequencies. The stray capacitances are effectively open circuits.

In the mid frequencies range large capacitors are effectively short circuits and the stray capacitors are open circuits, so that no capacitors appears in the mid frequency range. Hence, the mid band frequency gain is maximum.

At the high frequencies, the bypass and coupling capacitors are replaced by short circuits. The stray capacitors and the transistor determine the response.

Procedure :-

Connect the circuit as shown in the circuit diagram.

Set source voltage $V_s = 50 \text{ mV}$ (say) at 1 kHz frequency using the function generator. Observe the phase difference input and output by giving these two signals to the dual channels of CRO.

Keeping input voltage constant, vary the frequency from 50 Hz to 1 MHz in regular steps and note down the corresponding output voltage. Calculate gain in dB as

shown in the tabular column.

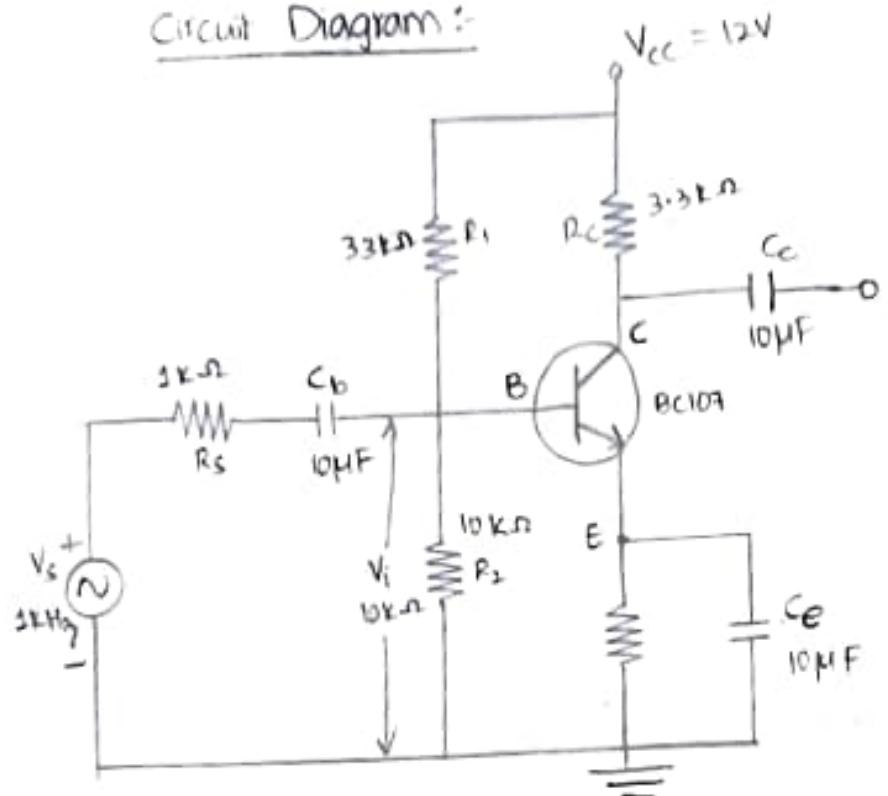
4. Plot the graph : gain (dB) versus frequency on the semilog graph sheet
5. Calculate the 3-dB bandwidth from the frequency response.

Result :-

The frequency response of common Emitter amplifier
is plotted and its bandwidth is calculated.



Circuit Diagram :-



T/p Signal:

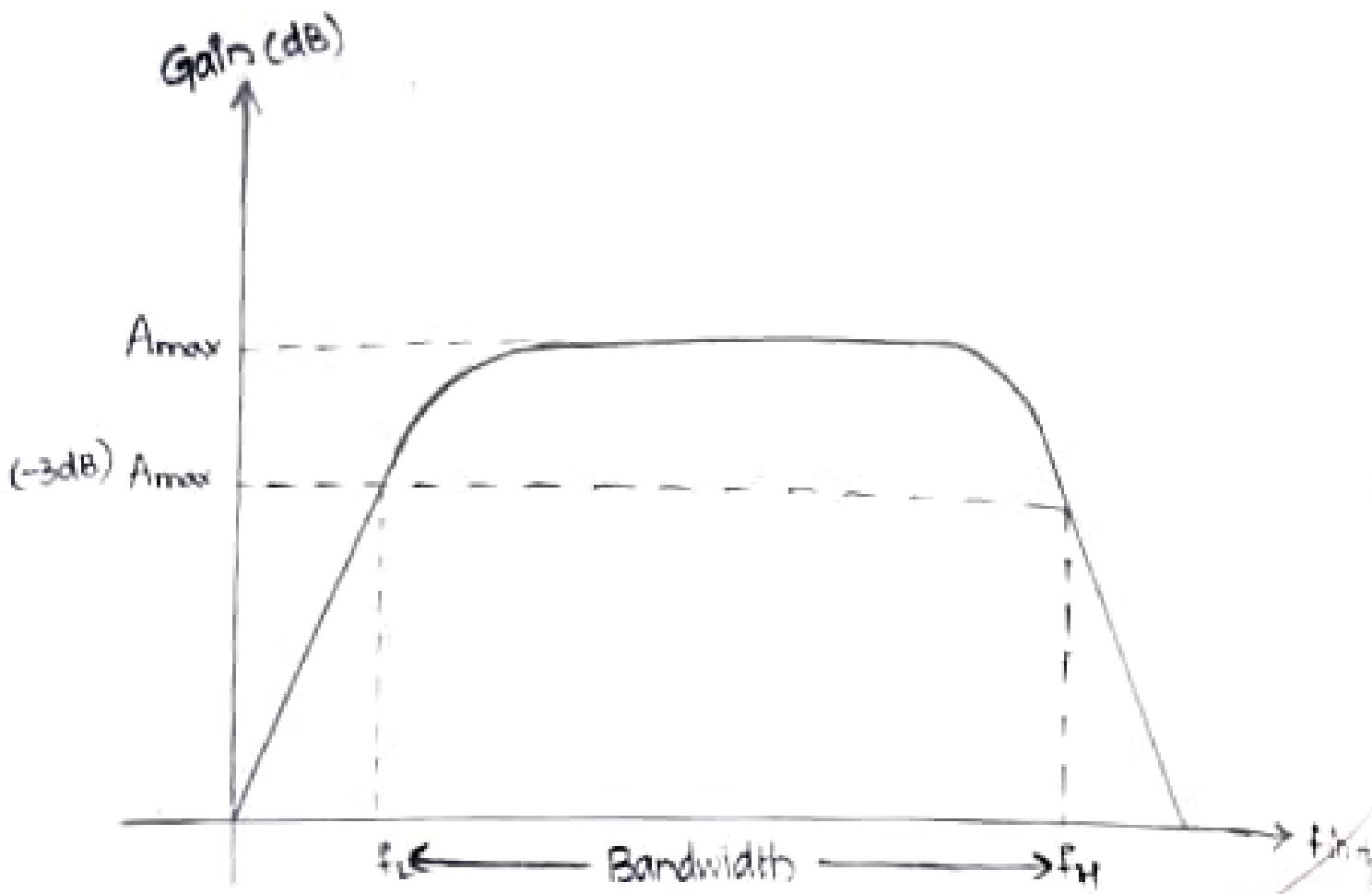
Amplitude:

$$50mV \times 4 = 200mV$$

Observations: Input Voltage = 200mV.

S.N.	Frequency (Hz)	Output Voltage V_o (V)	Voltage Gain $A_v = \frac{V_o}{V_i}$ ($V_i = 200mV$)	A_v in dB $= 20 \log(A_v)$
1.	100	$1.6 \times 2 = 3.2$	$\frac{3.2}{200 \times 10^{-3}} = 16$	24.082
2.	200	$2.6 \times 2 = 5.2$	26	28.299
3.	500	$2 \times 5 = 10$	50	33.977
4.	1K	$2.8 \times 5 = 14.0$	70	36.901
5.	5K	$3.6 \times 5 = 18$	90	39.084
6.	10K	$3.6 \times 5 = 18$	90	39.084
7.	20K	$5.6 \times 5 = 18$	90	39.084
8.	50K	$3.2 \times 5 = 16$	80	38.061
9.	100K	$2.4 \times 5 = 12$	60	35.563
10.	200K	$1.8 \times 5 = 9$	45	33.064
11.	500K	$0.5 \times 5 = 4$	20	26.020
12.	1M	$0.4 \times 5 = 2$	10	20

Model Graph

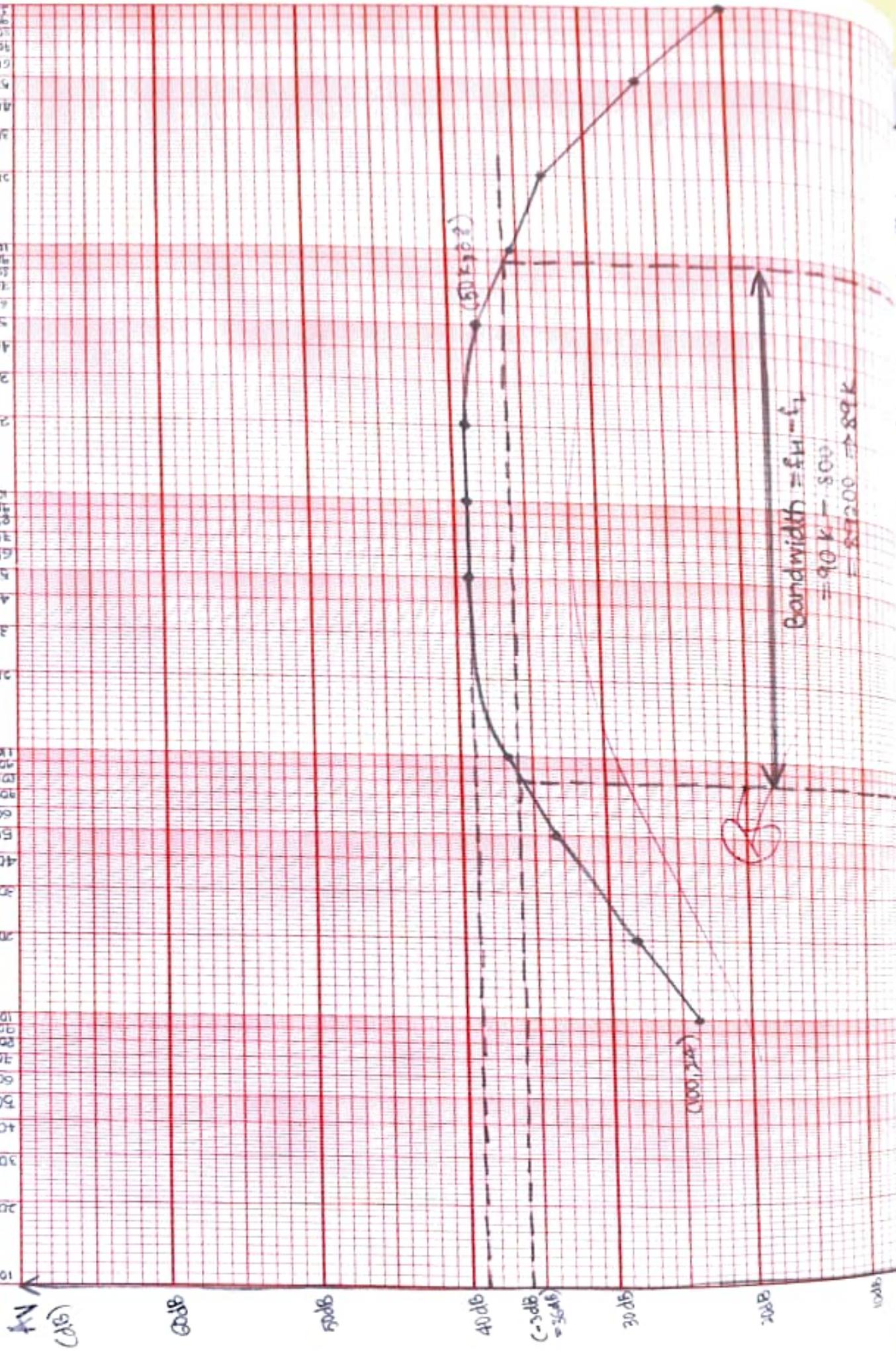


The difference between higher cut-off and lower frequency is referred to as bandwidth ($f_H - f_L$). ~~po~~

$$\text{Bandwidth} = f_H - f_L$$

$$= 90 \text{ K} - 50 \text{ K}$$

$$= 40 \text{ K} \rightarrow 89 \text{ K}$$



Frequency Response of Common Source FET Amplifier

Aim:- To plot the frequency response of common source FET amplifier and calculate its Bandwidth.

Apparatus Required :-

Sl. No.	Description	Range / Number / Value	Quantity
1.	Regulated Power Supply	(0-30)V	1
2.	FET	BFW 10/11	1
3.	Resistors	2KΩ, 2.2MΩ, 4.7KΩ, 470Ω	Each 2
4.	Capacitors	47 μF	2
5.	Capacitors	0.001 μF	1
6.	Signal Generator	(0-1 MHz)	1
7.	Dual Trace CRO	20MHz	1
8.	Bread board and Connecting wires.		

Theory:-

OF the possible three configurations of JFET amplifiers , common source (CS) configuration is mostly used. The advantage of using CS configuration is that it has very high input Impedance .

At the mid-frequency range , there is no effect of input and output coupling capacitors . Therefore the voltage gain and phase angle are constant in this frequency range . The amplifier shown in the circuit diagram has only two RC networks that influence its low- frequency response . One network is

formed by the output coupling capacitors and the output impedance at the drain. Just as in the case of BJT amplifier, the reactance of the input coupling capacitor, reactance increases as the frequency decreases. The phase angle also changes with change in frequency.

As the frequency is increased beyond mid frequency range the internal transistor capacitance effect is predominant. In JFET's the internal capacitance is formed between gate and source. This is also called input capacitance. The other internal capacitance, which effects the performance is acts as a feedback circuit, which couples both input and output.

Procedure :-

- Connect the circuit as shown in the circuit diagram.
- Set source voltage $V_s = 50\text{mV}$ (say) at 1 KHz frequency using the function generator. Observe the phase difference between input and output by giving these two signals to the dual channels of CRO.

Keeping the input voltage constant, vary the frequency from 50 Hz to 1 MHz in regular steps and note down the corresponding output voltage. Calculate gain in dB as shown in the tabular column.

- Plot the graph: gain (dB) versus frequency on a semilog graph sheet.

Title :

Date : 29/06/24

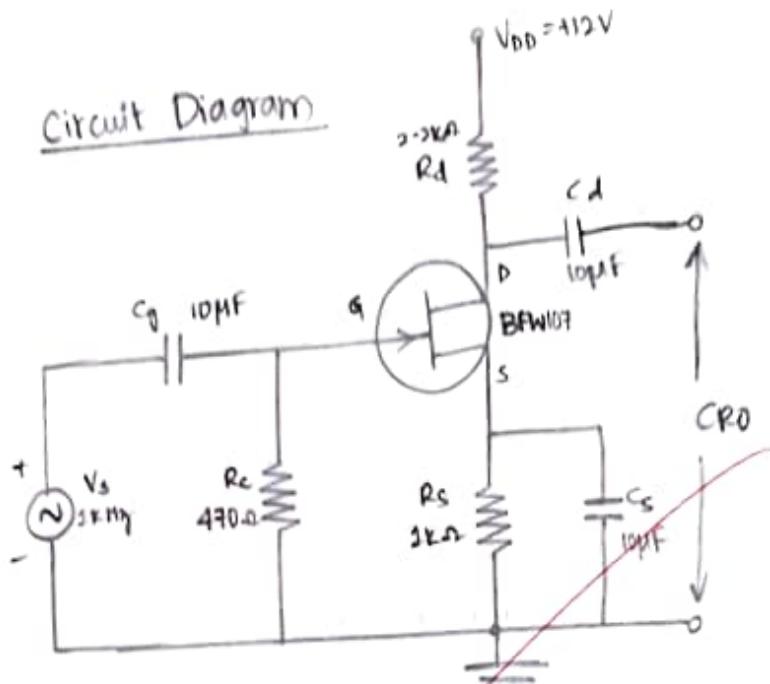
Page No. : 73

5. Calculate the 3-dB bandwidth from the frequency response.

Result :- The frequency response of common source FET amplifier is plotted and its bandwidth is calculated.

AGS/1/2022

Circuit Diagram



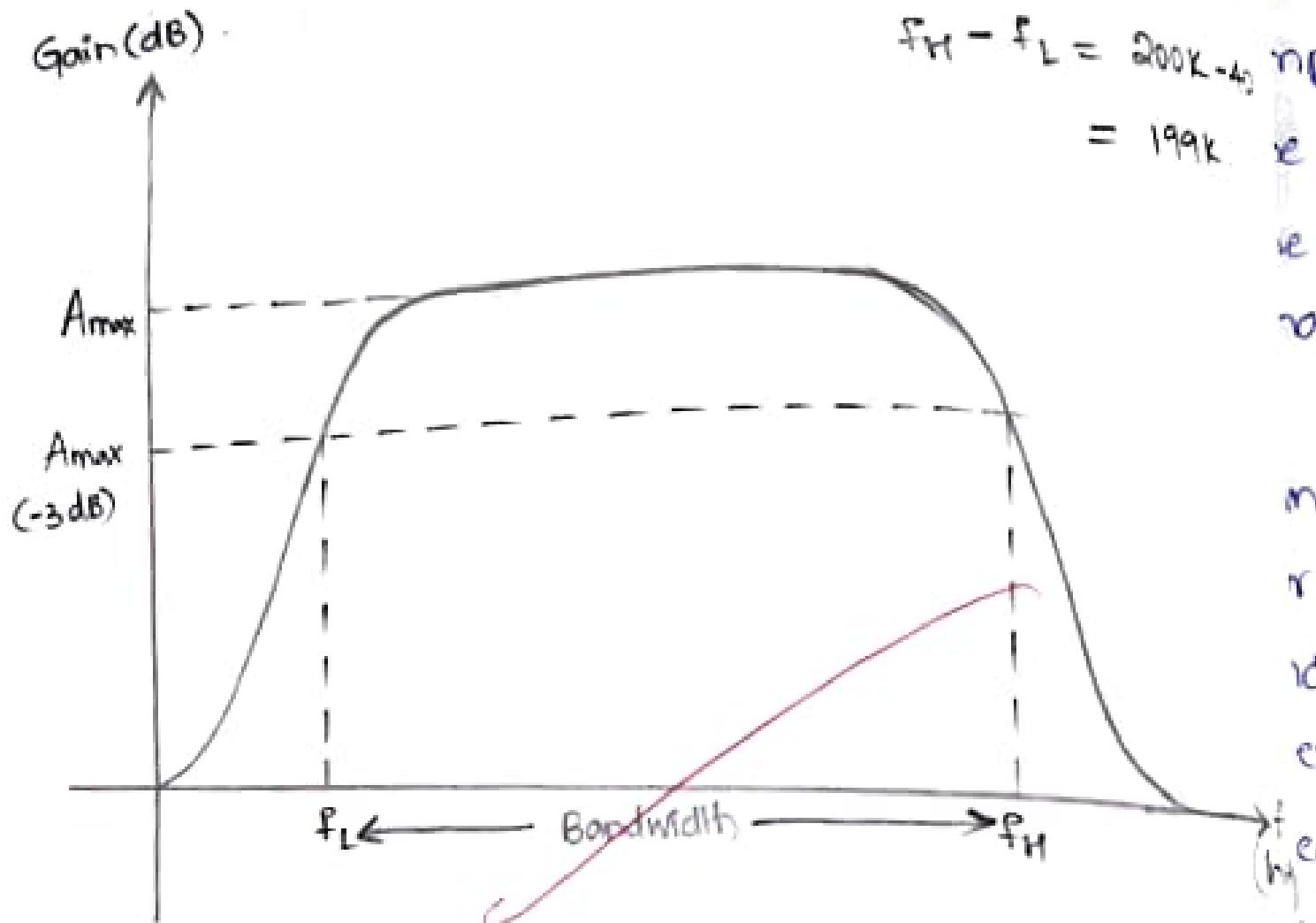
J/p Signal.

Amplitude:
50 mV $\times 10 \rightarrow$

Observation : Input Voltage $V_i = 50\text{mV}$

S/N	Frequency (Hz)	Output Voltage $V_o(\text{V})$	Voltage Gain $A_v = V_o/V_i$	$A_v \text{ in dB}$ $= 20 \log(A_v)$
1.	20	$1.8 \times 0.1 = 0.18$	0.9	-0.91
2.	30	$2.6 \times 0.1 = 0.26$	1.3	2.27
3.	50	$3.4 \times 0.1 = 0.34$	1.7	4.60
4.	100	$4 \times 0.1 = 0.4$	2	6.02
5.	200	$4.2 \times 0.1 = 0.42$	2.1	6.44
6.	500	$4.6 \times 0.1 = 0.46$	2.3	7.23
7.	1K	$4.6 \times 0.1 = 0.46$	2.3	7.23
8.	2K	$4.6 \times 0.1 = 0.46$	2.3	7.23
9.	5K	$4.6 \times 0.1 = 0.46$	2.3	7.23
10.	10K	$4.4 \times 0.1 = 0.44$	2.2	6.84
11.	20K	$4.2 \times 0.1 = 0.42$	2.1	6.44
12.	50K	$4 \times 0.1 = 0.4$	2	6.02
13.	100K	$3.8 \times 0.1 = 0.38$	1.9	5.53
14.	200K	$3.6 \times 0.1 = 0.36$	1.8	5.10
15.	500K	$2.6 \times 0.1 = 0.26$	1.3	2.27
16.	1M	$1.4 \times 0.1 = 0.14$	0.7	-3.09

Model Graph:

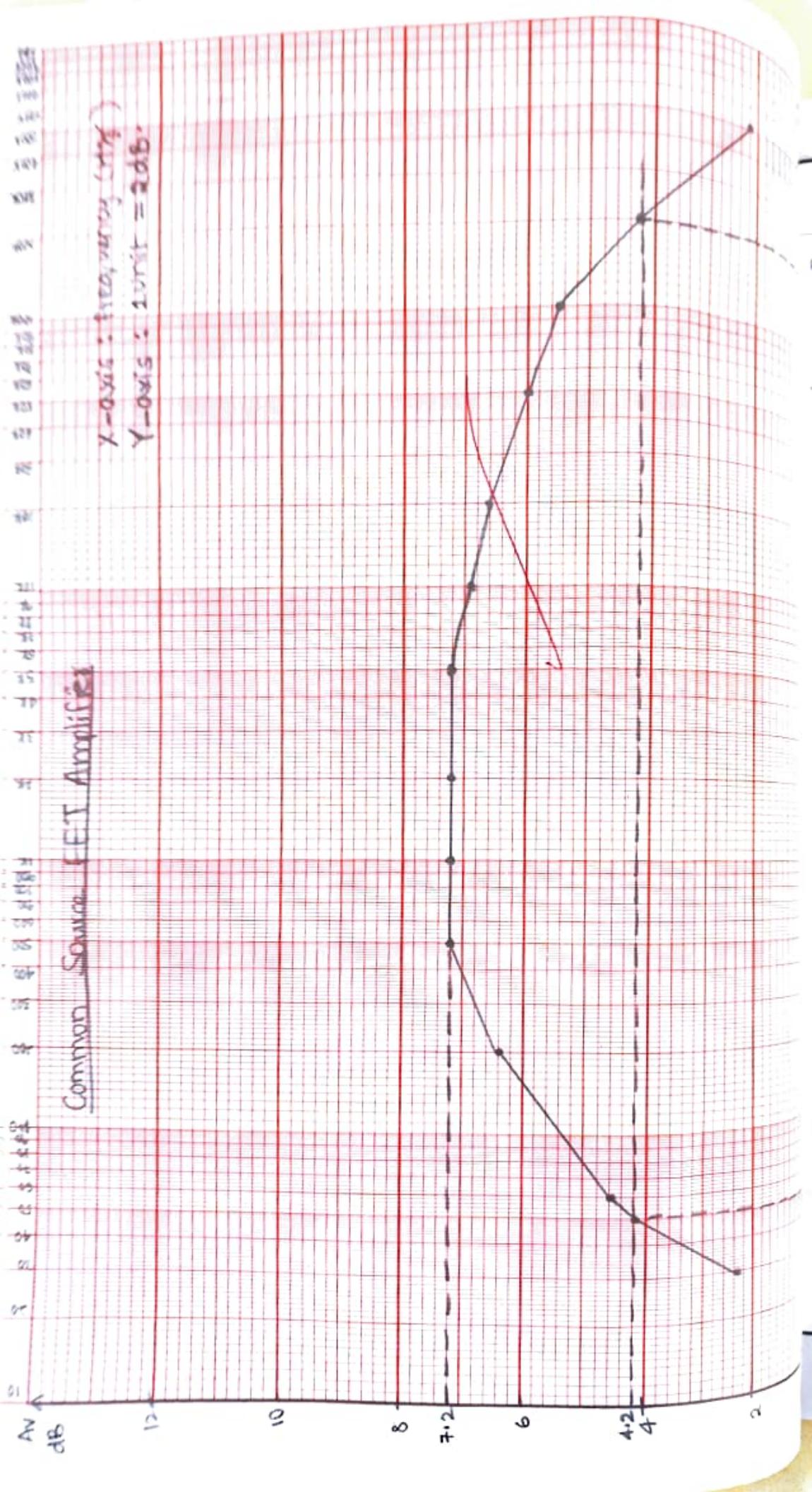


The difference between higher cut-off and lower cut-off frequency is referred to as bandwidth ($f_H - f_L$)

$$f_H - f_L = 200K_4$$
$$= 199K$$

Common Source FET Amplifier

$X\text{-axis} : \text{freq, mHz}$
 $Y\text{-axis} : 20\text{dB} = 2\text{mV}$



Characteristics of FET

Aim:- To plot the drain and transfer characteristics of a FET (Field Effect Transistor). Also calculate its drain resistance (R_d), transconductance (g_m), and amplification factor (μ).

Apparatus Required :-

Sl.No.	Description	Range / Value	Quantity
1.	Regulated Power Supply	(0-30)V	1
2.	JFET	BFW10	1
3.	DC Ammeter	(0-200)mA	1
4.	DC Voltmeter	(0-20)V	2
5.	Resistor	100 Ω , 560 Ω	1 of each
6.	Breadboard and Connecting wires		

Theory :-

The FET is a unipolar transistor and voltage controlled device. It has three terminals gate, source and drain.

A FET can be either a JFET or MOSFET. Again a JFET can either have N-channel or P-channel. The gate is given a negative bias with respect to the source. The drain is given a positive potential with respect to source.

The drain current I_D is controlled by electric field

that extends into the channel due to reverse biased voltage applied to the gate. The drain current depends on the drain voltage V_{DS} and gate source voltage V_{GS} . Any of these variables may be fixed and the relation between the other two are determined when $V_{DS} = V_p$, I_D becomes maximum. When $V_{DS} \uparrow$ beyond V_p , the length of pinch off region increases.

Procedure :-

A) Drain Characteristics:

1. All the connections are made as per the circuit diagram.
2. To plot the drain characteristics, keep V_{GS} as constant at 0V.
3. Vary the V_{DS} and observe the values of V_{DS} & I_D .
4. Repeat the above steps 2,3 for different values of V_{GS} at -1V and -2V.
5. All the readings are tabulated.
6. Draw the graph V_{DS} (V_s) I_D against V_{GS} as parameter on graph.
7. From the above graph calculate r_d and notedown the corresponding diode current against the voltage in the tabular form.

B) Transfer Characteristics:

1. To plot the transfer characteristics, keep V_{DS} as constant at 2V.
2. Vary V_{GS} and observe the values of V_{GS} & I_D .

3. Repeat steps 2 and 3 for different values of V_{DS} at 4V & 6V.
4. The readings are tabulated.
5. Draw graph between V_{GS} (V_s) I_D with V_{DS} as parameter.
6. From the graph find g_m .
7. Now, $\mu = g_m \times r_d$.

~~Calculation of r_d :-~~

Construct a triangle on one of the drain characteristics for a particular V_{GS} in the active region and find ΔV_{DS} & ΔI_D .

$$r_d = \frac{\Delta V_{DS}}{\Delta I_D} \quad | \quad V_{GS} = \text{constant}$$

~~Calculation of g_m :-~~

Construct the triangle on one of the transfer characteristics for a particular V_{DS} , find ΔV_{GS} and ΔI_D .

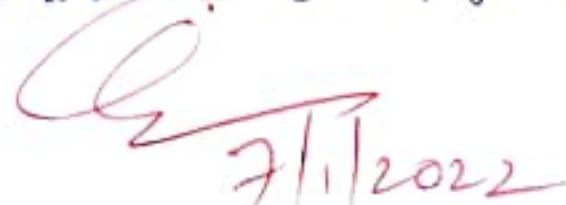
$$g_m = \frac{\Delta I_D}{\Delta V_{GS}} \quad | \quad V_{DS} = \text{constant}$$

~~Calculation of μ :-~~

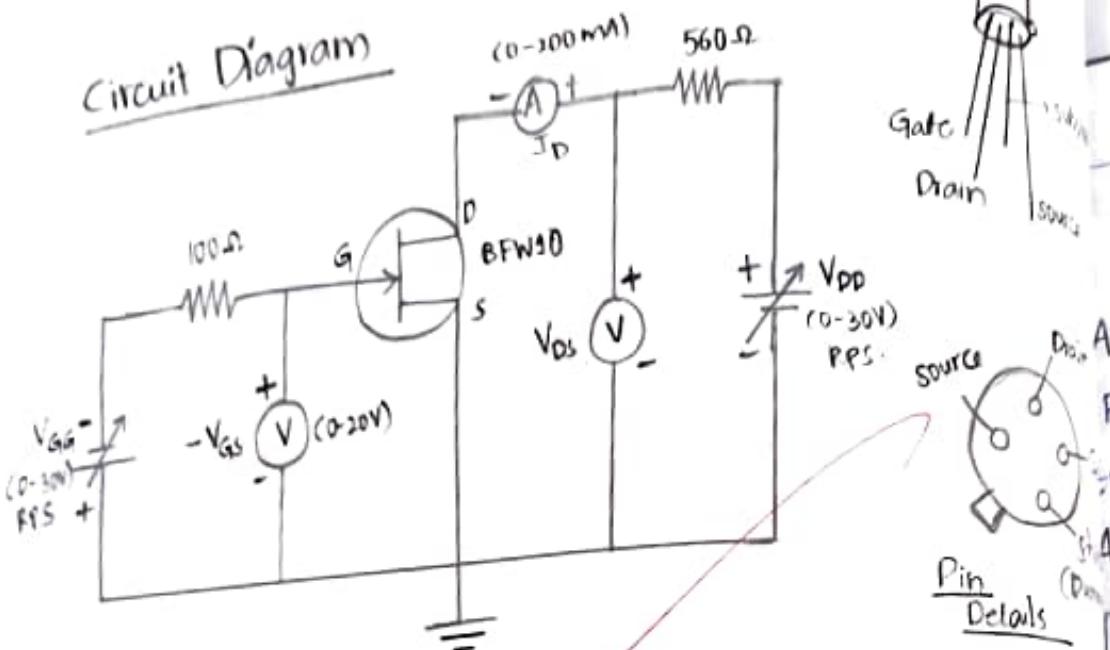
$$\mu = g_m \times r_d$$

~~Result :- The drain and transfer characteristics of a Field Effect transistor (FET) are plotted on graph & drain resistance (r_d), transconductance (g_m), and amplification factor (μ) are calculated.~~

$$r_d = 755 \Omega ; \quad g_m = 2.27 \times 10^{-3} \text{ S} ; \quad \mu = 1.71$$


7/1/2022

Circuit Diagram



Observations :- A) Drain characteristics

No.	RPS	$V_{GS} = 0V$		$V_{GS} = 2V$	
		$V_{DD}(V)$	$V_{DS}(V)$	$I_D(MA)$	$I_D(MA)$
1.	0.5	0.15	0.6	0.35	0.3
2.	0.7	0.2	0.9	0.49	0.3
3.	1.0	0.29	1.2	0.75	0.4
4.	1.5	0.45	1.8	1.18	0.4
5.	1.8	0.54	2.2	1.44	0.4
6.	2.0	0.61	2.4	1.66	0.4
7.	2.2	0.7	2.7	1.9	0.4
8.	2.5	0.78	2.9	2.16	0.4
9.	2.7	0.88	3.2	2.28	0.4
10.	3.0	1.00	3.4	2.63	0.4
11.	3.5	1.22	4.0	3.08	0.4
12.	3.7	1.33	4.1	3.22	0.4
13.	4.0	1.47	4.3	3.59	0.5
14.	4.5	1.97	4.47	3.99	0.5
15.	5.0	2.12	4.49	4.48	0.5
16.	5.5	2.49	5.1	4.97	0.5
17.	6.0	2.89	5.2	5.48	0.5

B) Transfer Characteristics :

V _D (V)	R _P S V _{GS} (V)	V _{DS} = 2V	
		V _{GS} (V)	I _D (mA)
3.	0.5	-0.53	3.4
2.	0.9	-0.97	3.1
3.	1.0	-1.03	2.5
4.	1.2	-1.25	2.0
5.	1.4	-1.49	1.5
6.	1.6	-1.63	1.1
7.	1.8	-1.84	0.7
8.	2.0	-2.09	0.3
9.	2.2	-2.30	0.2
10.	2.3	-2.34	0.1
11.	2.4	-2.53	0

$$r_d = \frac{\Delta V_{DS}}{\Delta I_D} = \frac{(2.12 - 0.61)}{(4.5 - 2.5) \times 10^{-3}} = 755 \Omega$$

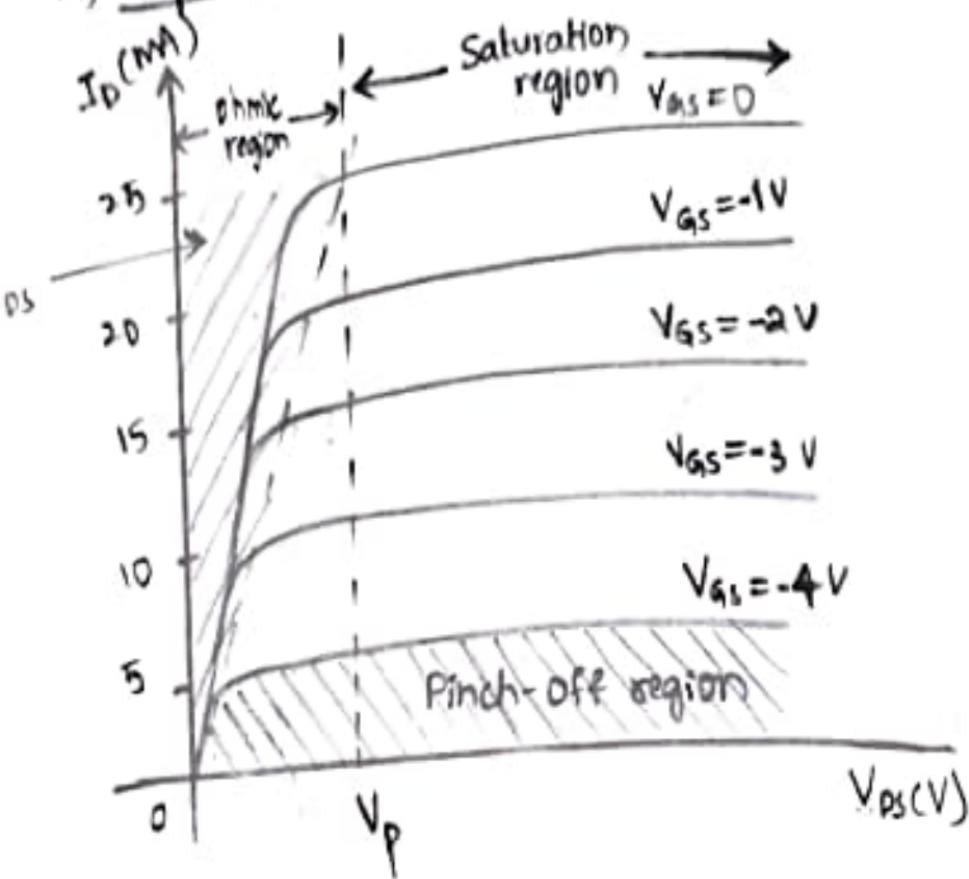
$$g_m = \frac{\Delta I_d}{\Delta V_{GS}} = \frac{(2.5 - 2) \times 10^{-3}}{-1.03 + 1.25} = 2.27 \times 10^{-3} \text{ A/V}$$

$$\mu = r_d \cdot g_m = 755 \times 2.27 \times 10^{-3}$$

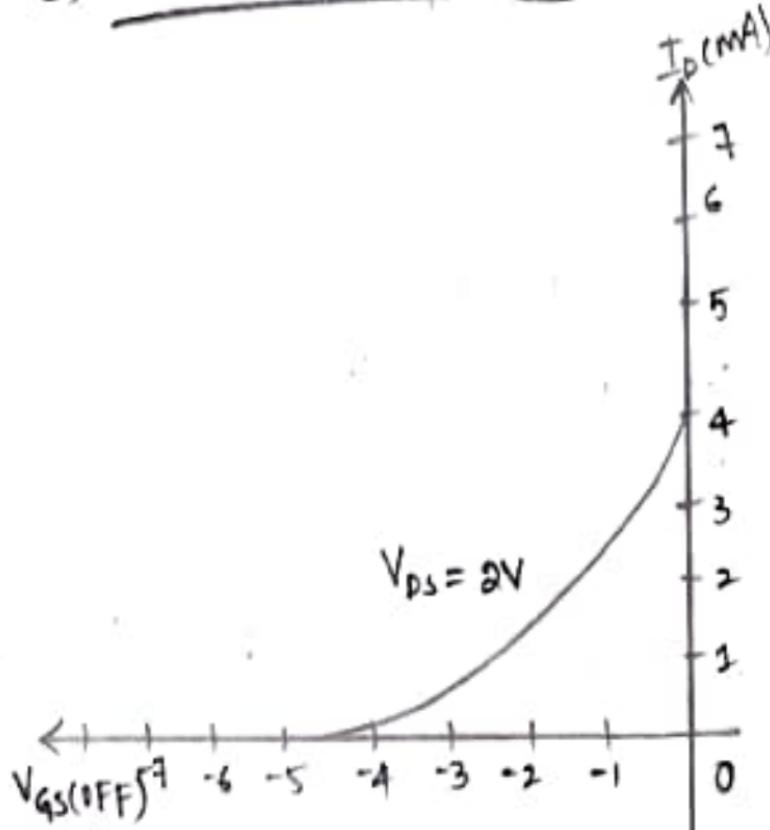
$$\mu = 1.71$$

Model Graph:

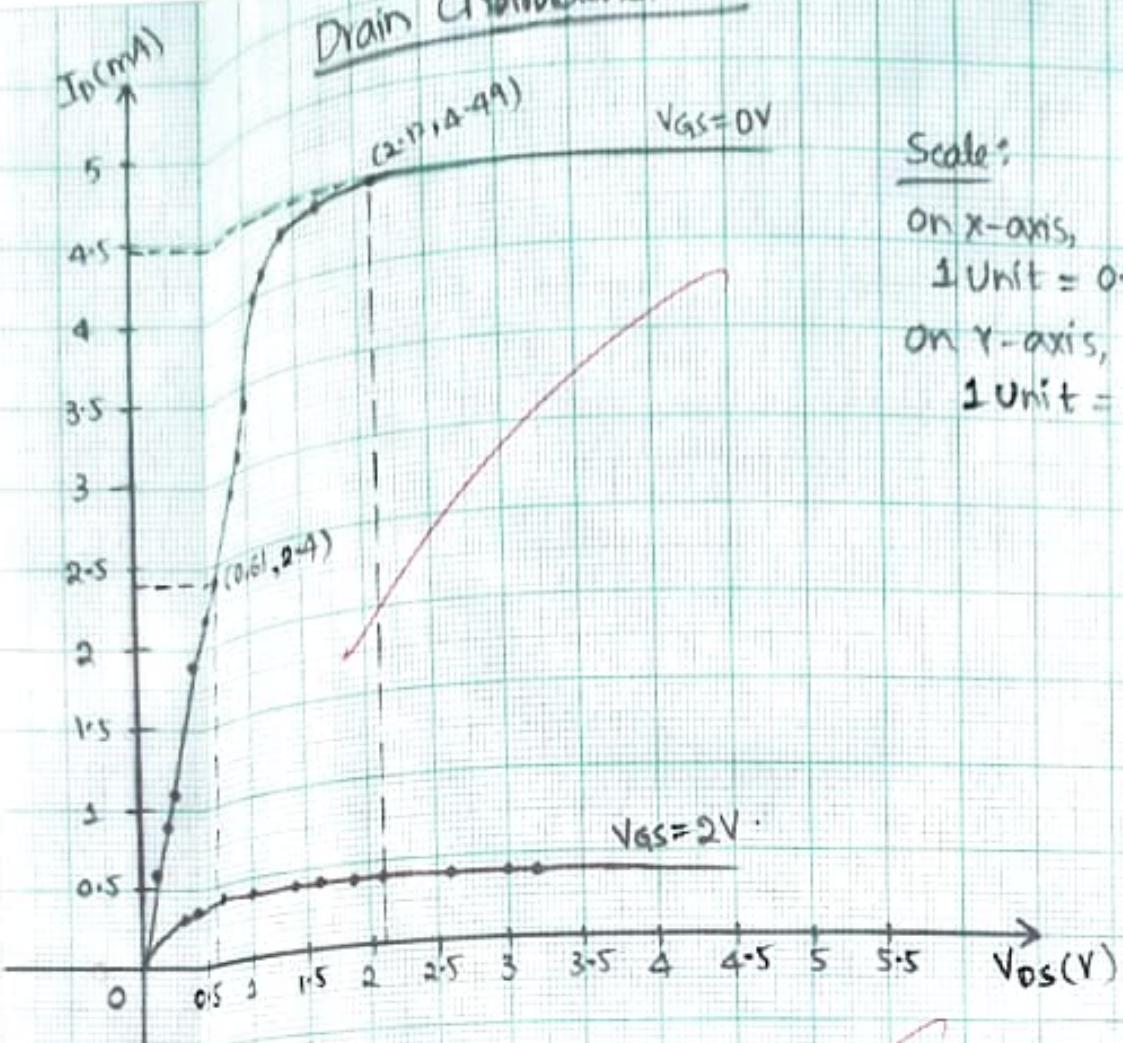
A) Input Characteristics:



B) Transfer Characteristics:



Drain Characteristics



Transfer Characteristics

